WIT2012 Workshop on Intelligent Trackers

Thursday 3 May 2012

Application of intelligent detectors / Coupled sensors and monolithic architectures (09:30 - 20:00)

time	[id] title	presenter
09:30	[19] A Fast Hardware Tracker for the ATLAS Trigger System	Dr PENNING, Bjorn
10:00	[49] A Level-1 Tracking Trigger for the CMS Upgrade using stacked silicon strip detectors and advanced pattern recognition technologies	BOUDOUL, Gaelle
10:30	[54] Trigger and Data Acquisition Strategy for the LHCb Upgrade	ARTUSO, Marina
11:00	Coffee Break	
11:30	[10] Application for front end intelligence in gaseous pixel detectors for triggering	Dr HESSEY, Nigel
12:00	[7] Instrumentation of a track trigger with double buffer front-end architecture	WARDROPE, David
12:30	[56] FPGA and ASIC based algorithms for the present and upgraded LHCb silicon vertex detector	Dr SZUMLAK, Tomasz
13:00	Lunch	
14:30	[66] A Level-1 Track Trigger for CMS with double stack detectors and long barrel approach	SALVATI, Emmanuele
15:00	[41] A hybrid module architecture for a prompt momentum discriminating tracker at HL-LHC	ABBANEO, Duccio
15:30	[60] An innovative detection module concept for PET	MARINO, Nahema MARINO, Nahema Dr MARINO, nahema
16:00	Coffee break	
16:30	[65] Discussion	
17:30	[16] Active Pixel Sensors in high-voltage CMOS technologies for ATLAS	Dr PERIC, Ivan
18:00	[14] 3D Monolithically Stacked CMOS Active Pixel Sensor Detectors for Particle Tracking Applications.	Dr PASSERI, Daniele