

Session Program

3-5 May 2012



WIT2012 Workshop on Intelligent Trackers

Real time pattern-recognition and advanced algorithms

INFN Pisa
Largo Bruno Pontecorvo 3 56127 Pisa Italy

Saturday 5 May

08:00

Real time pattern-recognition and advanced algorithms

Session | **Location:** INFN Pisa, Largo Bruno Pontecorvo 3 56127 Pisa Italy

08:30–09:00 **A clusterization algorithm for ATLAS pixel upgrade.**

Speaker

Teddy Todorov

09:00–09:30 **A Self Seeded First Level Track Trigger for ATLAS**

Speakers

Andre Schoening, Sebastian Schmitt

09:30–10:00 **Use of Associative Memories for L1 triggering in LHC environment.**

Speaker

Daniel Magalotti

10:00–10:30

The new variable resolution Associative Memory for Fast Track finding

Speaker

Alberto Annovi

10:30–11:00 **Coffee break**

11:00–11:30 **3D Vertical Integration Technology for Fast Pattern Recognition**

Speaker

Tiehui Ted Liu

11:30–12:00 **Pattern recognition with vector-type detector hits**

Speaker

Dr Rudolf Frühwirth

12:00–13:00 **Discussion**

13:00