



# WIT2012 Workshop on Intelligent Trackers

## Friday, 4 May 2012

### On-module electronic circuits (3D and conventional), intra-module and off-detector communication (11:30 - 19:35)

time	[id] title	presenter
11:30	[37] The CBC microstrip readout chip for LHC phase II	PESARESI, Mark
12:00	[48] Asynchronous readout architectures for Tracker Front-End ASICs	Dr JOHNSON, Marvin JOHNSON, Marvin
12:30	[8] A real-time clustering ASIC for the PXD in Belle II	WASSATSCH, Andreas
13:00	Lunch	
14:30	[30] CBC2: a microstrip readout ASIC with coincidence logic for trigger primitives at HL-LHC	BRAGA, Davide
15:00	[9] A Fast Clustering Block for Silicon Strip Seeded Track Trigger	Mr NEWCOMER, Mitch
15:30	[26] Front end intelligence for triggering and local track measurement in gaseous pixel detectors	GROMOV, Vladimir
16:00	Coffee break	
16:30	[20] Multi_Gigabit wireless data transfer at 60 GHz	Mr SOLTVEIT, Hans Kristian
17:00	[59] Radiation-Hard and High-Speed Parallel Optical Engine	Prof. GAN, KK
17:30	[63] Discussion	