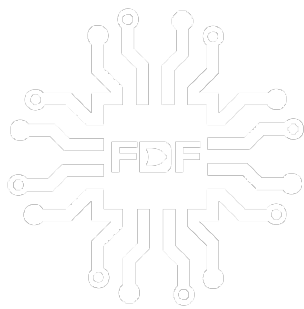


Session Program

27-29 May 2026



FPGA
DEVELOPERS'
FORUM

3rd FPGA Developers' Forum (FDF) meeting

Introduction

CERN, 500/1-001 - Main Auditorium

Wednesday 27 May

14:00

Introduction

Session | **Location:** CERN, 500/1-001 - Main Auditorium | **Convener:** Dr Francesco Gonnella

14:00–14:10 **Welcome to FDF26**

Speaker

Davide Cieri

14:10–14:55 **What Star Trek can teach us about using AI in FPGA design**

Speaker

Mr Adam Taylor

15:00