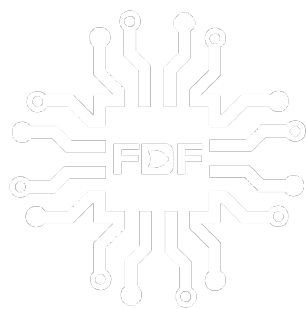


Session Program

27-29 May 2026



FPGA
DEVELOPERS'
FORUM

3rd FPGA Developers' Forum (FDF) meeting

AI

CERN, 500/1-001 - Main Auditorium

Thursday 28 May

11:00

AI: 1

Session | **Location:** CERN, 500/1-001 - Main Auditorium | **Convener:** Dr Rui Zou

11:00-11:45 **AMD Embedded AI Solutions**

Speaker
Mr Thomas Gmeinder

11:45-12:05

KalEdge-Lite: Hardware-Aware ML-to-FPGA Deployment with Automated hls4ml Integration

Speaker
Romina Soledad Molina

12:05-12:35

HGQ: High Granularity Quantization for Real-time Neural Networks and LUT-Based Inference

Speaker
Chang Sun

12:35