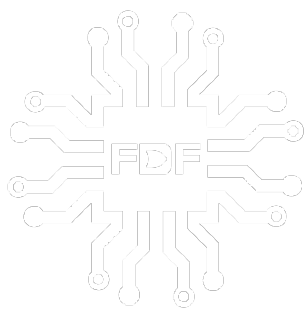


## **Session Program**

**27-29 May 2026**



FPGA  
DEVELOPERS'  
FORUM

## **3rd FPGA Developers' Forum (FDF) meeting** ***Sponsor Presentations***

CERN, 500/1-001 - Main Auditorium

## Wednesday 27 May

15:00

### Sponsor Presentations: Sponsor Presentaton

**Session** | **Location:** CERN, 500/1-001 - Main Auditorium**15:00-15:03 Altera****Speaker**

Christian Faerber

**15:03-15:06 AMD****Speaker**

Thilo Ohlemueller

**15:06-15:09 CAEN****Speaker**

Luca Colombini

**15:09-15:12 CAST****Speaker**

Calliope-Louisa Sotiropoulou

**15:12-15:15 Efinix****Speaker**

Mr Fabian Heinrici

**15:15-15:18 iWave****Speaker**

Sharath Kumar

**15:18-15:21 Knowledge Resources****Speaker**

Bruno Monteiro

**15:21-15:24 Lattice Semiconductor****Speaker**

Hardik Shah

**15:24-15:27 Microchip****Speaker**

Mr Jens Huettemann

**15:27-15:30 NanoXplore****Speaker**

JEAN LOUIS FRIGOUL

**15:30-15:33 PLC2****Speaker**

Patrick Lehmann

15:33-15:36

**ProDesign****Speaker**

Stefan Rooseboom

15:36-15:39

**Samtec****Speakers**

Neil Potter, Veronique Branca

15:39-15:42

**Techway****Speaker**

Romain Larousse

15:42-15:45

**Trenz Electronic****Speaker**

Martin Rohrmüller

15:45