

Session Program

23-26 Sept 2025

DRD7 annual workshop

TU-12

CERN, 30/7-018 - Kjell Johnsen Auditorium

Tuesday 23 September

14:00

TU-12: WP7.1 Parallel

Session | **Location:** CERN, 13/3-005 | **Convener:** Michael Karagounis

14:00–14:30 Status of DC/DC developments

Speaker

Marta Macaluso

14:30–15:00

Characterization of the bPOL48V and integration tests with a CMS Phase-2 strip module

Speaker

Joelle Savelberg

15:00–15:30

Status of 48V-to-2.5V DC-DC Point of load Converter

Speaker

Abdul Majid Bhat

15:30–16:00

Status of DC/DC converter with advanced architecture and external coil

Speaker

Michael Karagounis

16:00–16:20

Serial powering multi chip modules

Speaker

Riccardo Zanzottera

16:20–16:40

Status of switch-mode shunt regulator and SLDO in 28nm

Speaker

Michael Karagounis

16:40–16:55

Updates from ITA (Instituto Tecnológico de Aragón)

Speaker

Alvaro Pradas Luengo

16:55–17:00

Opportunities for collaboration and new ideas

Speaker

Michael Karagounis

17:00

17:00

TU-12: WP7.1 Parallel

Session | **Location:** CERN, 13/3-005 | **Convener:** Elizabeth Locci

17:00–17:15

Introduction to the DRD7.1c project

Speaker

Elizabeth Locci

17:15–17:35

Uppsala contribution

Speaker

Richard Brenner

17:35-17:45 Tel-Aviv contribution

Speakers

Richard Brenner, Yan Benhammou

17:45-18:00 News from Leti

Speaker

cedric dehos

18:00-18:10 IHEP recent progress

Speakers

Jun Hu, Jun Hu

18:10-18:30

Activities of the Noise and Tetrahertz Electronics group at Vilnius University

Speaker

Kestutis Ikamas

18:30-19:00 Discussion

Speakers

Elizabeth Locci, Fabrizio Palla, Prof. Fairouz Malek, Gain Kim, Richard Brenner, Yan Benhammou, cedric dehos

19:00