

Session Program

23-26 Sept 2025

DRD7 annual workshop

TU-5

CERN, 30/7-018 - Kjell Johnsen Auditorium

Tuesday 23 September

08:30

TU-5: WP7.5 Parallel

Session | Location: CERN, 4/S-030 | Conveners:
Yun-Tsung Lai, Federico Alessio, Niko Neufeld, Alex Keshavarzi, Conor Fitzpatrick

08:30–08:35 (7.5a) Intro

Speakers
Alex Keshavarzi, Yun-Tsung Lai

08:35–08:45 (7.5a) Shandong University

Speaker
Qidong Zhou

08:45–08:55 (7.5a) IHEP

Speaker
Jingzhou Zhao

08:55–09:05 (7.5a) ANL

Speaker
Alexander Paramonov

09:05–09:15 (7.5a) University of Birmingham

Speakers
Alan Watson, Juraj Bracinik

09:15–09:25 (7.5a) University of Bristol

Speaker
Christopher James Wright

09:25–09:35 (7.5a) CIEMAT

Speaker
Alvaro Navarro Tobar

09:35–09:40 Break

09:40–09:45 (7.5a) University of Geneva

Speaker
Alexis Marquet

09:45–09:55 (7.5a) KEK

Speaker
Yun-Tsung Lai

09:55–10:05 (7.5a) KIT

Speaker
Luis Ardila-Perez

10:05–10:15 (7.5a) University of Manchester

Speaker

Elena Gramellini

10:15–10:25 (7.5a) University of Oviedo**Speakers**

Pelayo Leguina, Santiago Folgueras

10:25–10:35 (7.5a) QMUL**10:35–10:40 Break****10:40–10:50 (7.5a) Rutherford Appleton Laboratory (RAL)****Speaker**

Ian Brawn

10:50–11:00 (7.5a) University College London (UCL)**Speaker**

Andreas Korn

11:00–11:10 (7.5a) UCM-GAE**Speakers**

Prof. Juan Abel Barrio, María Molina Delicado

11:10–11:20 (7.5a) IFIC Valencia**Speakers**

Arantza Oyanguren, Luca Fiorini

12:00–12:05 (7.5b) Intro**Speaker**

Federico Alessio

12:05–12:20 (7.5b) The no-backend approach**Speaker**

Valentin Stumpert

12:20–12:30 (7.5b) Backend Boards adaptation / CPPM**Speaker**

Julien Jiro Langouët

12:30–12:40 (7.5b) Backend Boards adaptation / NIKHEF & BNL**Speaker**

Mauricio Feo

12:40–12:50 (7.5b) Front-End ASIC Readout @ 25GbE**Speaker**

Thomas Charles Gardiner

12:50–13:05 (7.5b) FPGA-based RDMA communication system**Speaker**

Matei Vasile

13:30