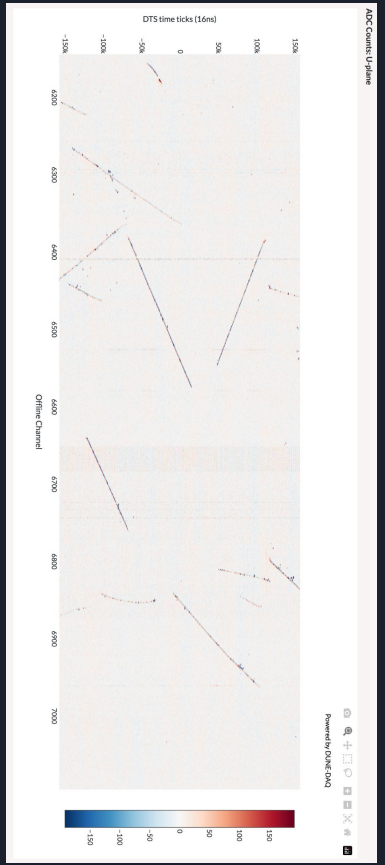
# DAQ - What was done last week

- TDE integration
  - The TDE team updated the firmware on the hardware to streaming mode
  - In the last two days more and more crates were read out, and yesterday we did read out both CRPs (10 TDE crates) with DUNE-DAQ
  - readout tuning optimisation (server + applications)
  - Preliminary results
    - Interface between TDE and DAQ is correct
    - The data integrity is validated and we can decode correctly the data stream
    - Performance
      - We managed to run without dropped packets
      - We also tried TPG and we saw good behaviour
  - DAQ-TDE integration going well and looks ready for continuous running in that mode
  - Ongoing work
    - driving the TDE via the DTS - good progress already on this
- PDS
  - Additional daphne for the PMTs is installed
    - All fibers are connected
  - We are preparing the configurations
    - We already proved that we can read membranes and PMTs together with the same felix
- DAQ release
  - New release v5.3.3 is out
    - There will be another one before the beam in September
- elog messages
  - reminder that it's possible to automatically add logging information when you start a run with the DAQ
    start-run --elisa-post "Comment" --run-type TEST/PROD
  - We are updating the automatic messages so you will see a link to the run registry that contains the tarball of the actual configuration used in the run + some metadata



# DAQ - The plan for this week

- TDE integration
  - Continue TDE integration
- PMTs
  - Continue integration - at this point it's mostly configuration optimisation of the DAPHNE boards themselves
- CTB development
  - Switch to the new firmware/software in preparation for the next beam
- DAQ development for next release
  - We have a number of deliverables for the next release and we will need some time out of the monitoring runs to test
  - Coordination via the shifter-assistant channels as usual
  - Would be it possible to identify at least a day in each week in which the system is mostly for DAQ?
    - Ideally the same day, but we can try to be flexible
    - And ideally in running conditions?
      - HV nominal, bias on, PDS calibrated, firmware ready, etc
- We've been requested long readout windows (~ 1s) to study  any low-mid frequency behaviors in the waveforms
  - With both HV on and off
  - And bias ON in both HV cases
  - We might try by the end of the week