

## **Session Program**

**10-14 Nov 2025**

# **4th DRD3 week on Solid State Detectors R&D**

## ***WG/WP1 - CMOS technologies***

CERN, 6/2-024 - BE Auditorium Meyrin

# Monday 10 November

14:15

## WG/WP1 - CMOS technologies: Scientific results from WPs

**Session** | **Location:** CERN, 6/2-024 - BE Auditorium Meyrin | **Conveners:**  
Heinz Pernegger, Jerome Baudot, Eva Vilella Figueras

14:15–14:35

### First results of COFFEE3, a small prototype for 55nm HVCMOS validation

**Speaker**

Zijun Xu

14:35–14:55

### DRD3 Community Shared Submission in LF15A Technology

**Speaker**

Eva Vilella Figueras

14:55–15:15

### Testbeam results of the MiniCactus V2 timing demonstrator

**Speaker**

Joaquim Pinol Bel

15:15–15:35

### Development of sensors with intrinsic gain in LFoundry 150 nm technology

**Speaker**

Prof. Philippe Schwemling

15:35–15:55

### Status on access and submission preparation for MAPS technologies within DRD7.6a

**Speaker**

Walter Snoeys

15:55–16:25

### Coffee/tea

16:25–16:45

### Recent measurements on the CASSIA1 sensors

**Speaker**

Anastasia Kotsokechagia

16:45–17:05

### Limits of Spatial Resolution in TJ-65nm --- How Much Charge Information Do We Need?

**Speaker**

Finn King

17:05–17:25

### The OCTOPUS project: optimizing monolithic active pixel sensors for the next generation of lepton collider experiments

**Speaker**

Roberto Russo

17:25–17:45

### Status of the MANTA project

**Speaker**

Michael Deveau

17:45-18:05

**Porting the MALTA chip to the 65 nm TPSCo node: Conceptual Design Overview**

**Speakers**

Carlos Solans Sanchez, Thomas Koffas

18:15