

WG5 – Electronics Draft Agenda

Tuesday Nov 22, WG5 (Electronics) 160-1-009

14:00 Announcements, H.Muller CERN PH

14:05 MICROROC: MICROMEGAS Read-Out Chip, Renaud Gaglione, LAPP Annecy

14:30 Progress with GEMROC front-end and fast DAQ part 1, Bartosz Mindur (Cracow)

Progress with GEMROC front-end and fast DAQ part 2, Martina Bucciantonio (TERA)

15:00 Electronics for GEM R&D at the University of Virginia, Kondo Gnanvo Jeff Lab Virginia

15:20 Experience with readout of Micromegas for NA62 Antonino Sergi, CERN

15:45 Coffee break

16:00 A quality control system for integrity check of MPGD readout planes exploiting TDR technique, Giampiero Fanizzi, Kloe Experiment

16:20 Status of the BNL chip for Micromegas, Venetios Polychronakos, BNL / ATLAS

16:40 First data with the AZ-SRS adapter for BNL chip, Ken Johns , Univ Arizona (via EVO)

17:00 Experience with medium-size SRS for muon tomography, Michael Staib, Florida Tech. univ. (via EVO)

Wednesday Nov 23, WG5 (Electronics) IT Auditorium, 31-3-004

9:00 Announcements, H.Muller CERN PH

9:05 Labview software for SRS readout and testing, Riccardo de Asmundis, INFN (via EVO)

9:30 Update on MMDAQ readout of SRS Marcin Byszewski CERN /ATLAS

9:50 Timepix adapter for SRS, Michael Lupberger, Bonn University

10:10 Lessons learnt with the first deployed SRS systems, Sorin Martoiu CERN

10:30 Update on SRS for the NEXT experiment, Jose Toledo UPV Valencia

10:50 Coffee break

11:00 SRS Beetle chip carrier design at WIS, Lorne Levinson , WIS/ ATLAS

11:20 Status of commercial SRS production for CERN store, Konstantina Mermikli, PRISMA Electronics

11:40 SRS, status and preview Hans Muller, CERN PH

12:00: Discussions and AOB