

Session Program

6-8 Feb 2012



Belle II SVD-PXD Meeting

***SVD-PXD: Software (Background,
Simulation, Tracking, Alignment, ...)***

University of Technology, Vienna, Austria
Karlsplatz 13 A-1040 Vienna Austria

Wednesday 8 February

09:00

SVD-PXD: Software (Background, Simulation, Tracking, Alignment, ...)

Session | **Location:** University of Technology, Vienna, Austria, Festsaal, Karlsplatz 13 A-1040 Vienna Austria |
Convener: Christian Kiesling

09:00–09:15 **Simulation of Thin Depfets**

Speaker

Benjamin Schwenker

09:15–09:30 **Update on Background Simulation in the PXD**

Speaker

Martin Ritter

09:30–09:45 **SVD Simulation: Digitizer and Clustering**

Speaker

Peter Kvasnicka

09:45–10:00 **Silicon-only Track Finding**

Speaker

Jakob Lettenbichler

10:00–10:15 **Tracking and Vertexing**

Speaker

Martin Heck

10:15–10:30 **Status of new Compute Node (incl. daughter boards for DATCON)**

Speaker

Zhen-An Liu

10:30–10:45 **Status of FTB**

Speaker

Wacek Ostrowicz

10:45–11:00 **Status of SVD Simulation: Digitizer and Clustering**

11:00