

# LLRF System for Pulsed Linacs

(modeling, simulation, design and  
implementation)

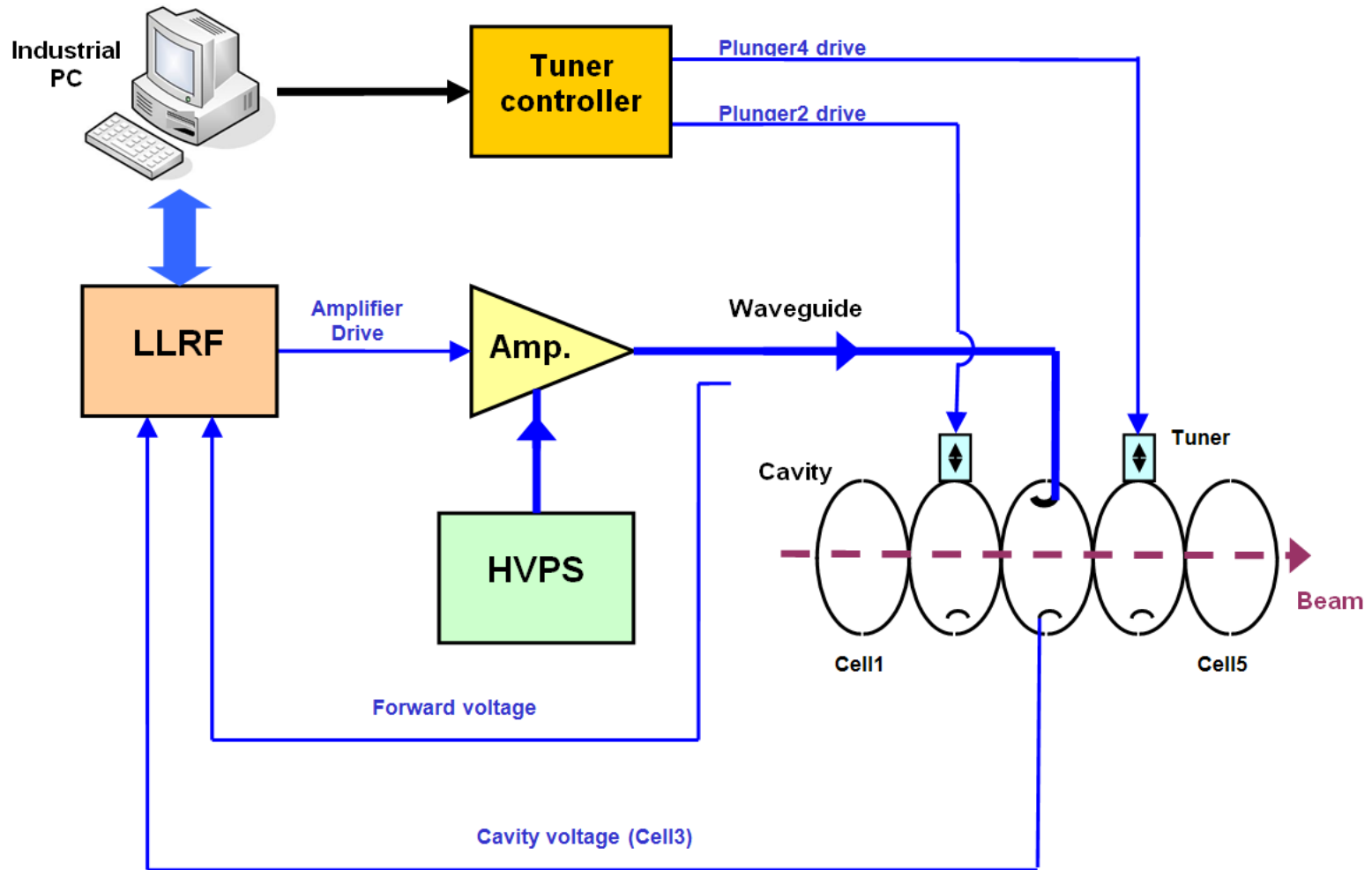


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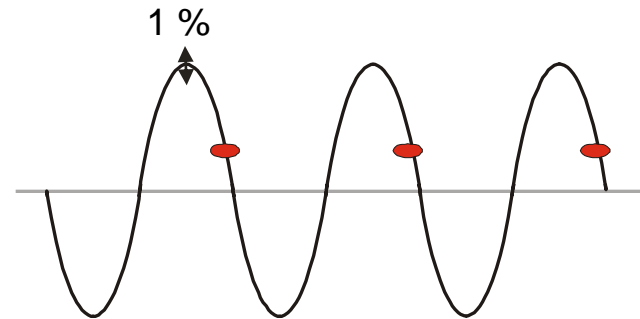
# Simplified schematics of a typical RF plant and the LLRF feedback loops



# Main functions of a LLRF system

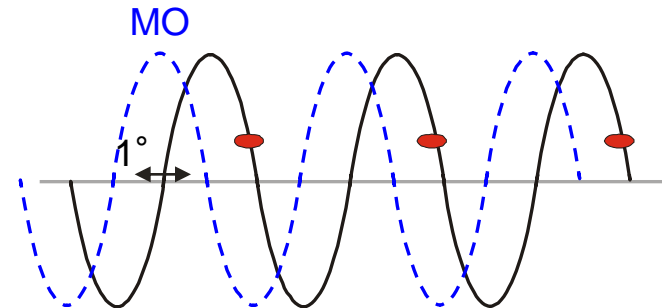
## 1) Amplitude regulation

regulates the cavity voltage against disturbances such as HVPS ripples, beam loading, cavity warming, tuner movements, etc.



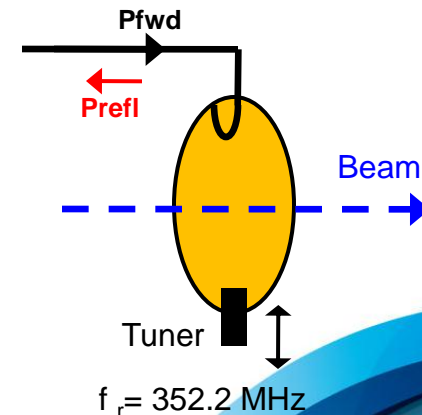
## 2) Phase regulation

regulates the cavity phase against disturbances such as HVPS ripples, beam loading, cavity warming, tuner movements, etc.



## 3) Cavity tuning

tunes the cavity to compensate the effect of beam loading and cavity warming thus minimizing the reflected power.





## Figure of merit of a LLRF system

- The goal in the design of an amplitude/phase loop is:
  - to give as much stability as possible to the RF field (typical values are  $\pm 1\%$  and  $\pm 1^\circ$  of amplitude and phase stability respectively).
  - to provide a large-enough bandwidth to suppress the highest frequency disturbance that may affect the RF field in the cavity.
  - to have a good stability margin (phase margins of  $45^\circ$  or more).
  - to have a large dynamic range (23 dB or more) if it is intended for energy ramps.
  
- Similarly, the tuning loop should provide enough accuracy in cavity tuning to have the least amount of reflected power although the cavity may suffer from a number of disturbances including beam loading, field ramping and temperature variations.



## Conventional vs. state of the art

### ➤ Control method / topology

- PID controllers vs. pole-placement feedbacks and Kalman filters

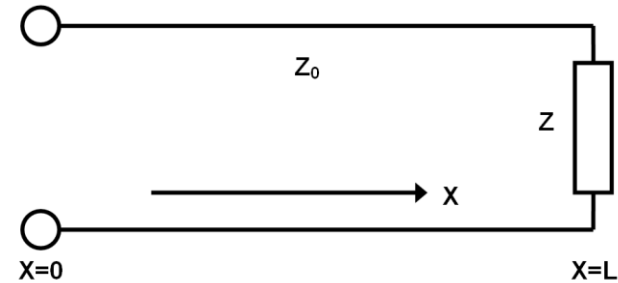
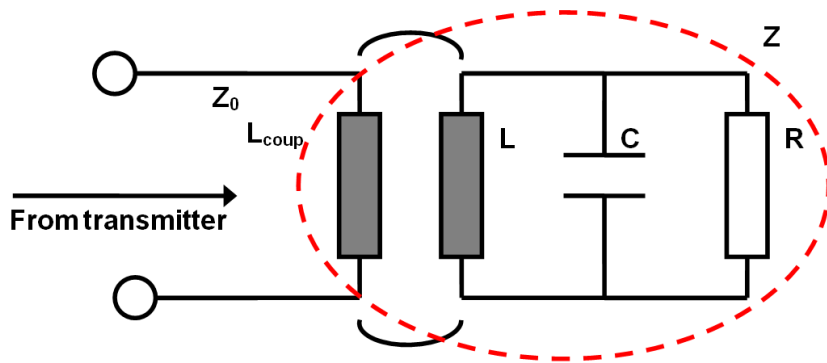
### ➤ RF modeling and simulation

- Steady-state models vs. transient models
- Simple RLC models vs. models dealing with the cavity reflected voltage
- Mixed RF-baseband models vs. baseband-equivalent models

### ➤ Design and implementation

- Analog vs. digital
- Amp/phase loops vs. IQ loops

# Cavity modeling with coupler (no beam)



$$Z = \frac{L_{coup}}{L} Z_C = \frac{L_{coup}}{L} \frac{RL.s}{RLC.s^2 + L.s + R}$$

$$\beta = \frac{L_{coup}}{L} \frac{R}{Z_0}$$

$$Z = \frac{\beta Z_0 L.s}{RLC.s^2 + L.s + R}$$

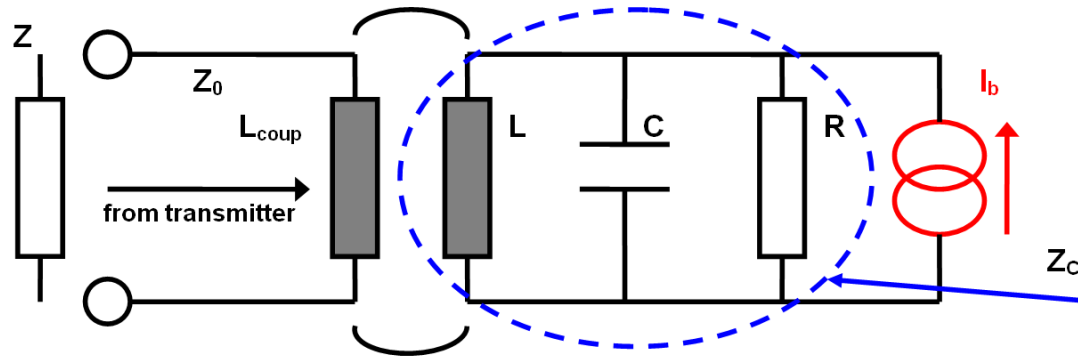
$$V(x) = V_{fwd}(x) + V_{refl}(x)$$

$$I(x) = I_{fwd}(x) - I_{refl}(x)$$

$$r = \frac{\beta L.s - RLC.s^2 - L.s - R}{\beta L.s + RLC.s^2 + L.s + R}$$

$$\dot{Z}_{cav-amp} = \frac{\dot{V}_{cav-amp}}{\dot{I}_{fwd}} = \frac{2\beta Z_0}{\beta + 1 + Q_0 \left( \frac{s}{\omega_0} + \frac{\omega_0}{s} \right)}$$

# Cavity modeling with beam



$$\dot{Z}_{cav-beam} = \frac{\beta Z_0 L s}{RLC.s^2 + L(\beta + 1).s + R} \quad \left| \dot{I}_{beam} \right| = 2I_{DC} \sqrt{\frac{L}{L_{coupl}}} = 2I_{DC} \sqrt{\frac{R}{\beta Z_0}}$$

$$\begin{aligned} \dot{V}_{total}(s) &= \dot{V}_{cav-amp}(s) + \dot{V}_{cav-beam}(s) \\ &= \dot{I}_{amp}(s) \cdot \dot{Z}_{cav-amp}(s) - \dot{I}_{beam}(s) \cdot \dot{Z}_{cav-beam}(s) \end{aligned}$$

$$\dot{V}_{total}(s) = \left( 2\dot{I}_{amp}(s) - \dot{I}_{beam}(s) \right) \frac{\beta Z_0 L s}{RLC.s^2 + L(\beta + 1).s + R}$$



## Compensation of steady-state beam loading

$$\dot{V}_{total} = \dot{I}_{amp}(2 - A.e^{j\omega T}). \frac{\beta Z_0}{\beta + 1 + jQ_0\zeta} \quad (\text{steady state})$$

$$A = \frac{2I_{DC}}{|\dot{I}_{amp}|} \cdot \sqrt{\frac{R}{\beta Z_0}} \quad T = \frac{\varphi - 90^\circ}{\omega} \cdot \frac{\pi}{180^\circ}$$

$$\dot{Z}_{total} = \frac{\dot{V}_{total}}{\dot{I}_{amp}} = \frac{\beta(2 - A.\sin\varphi_S - jA.\cos\varphi_S)Z_0}{\beta + 1 + jQ_0\zeta}$$

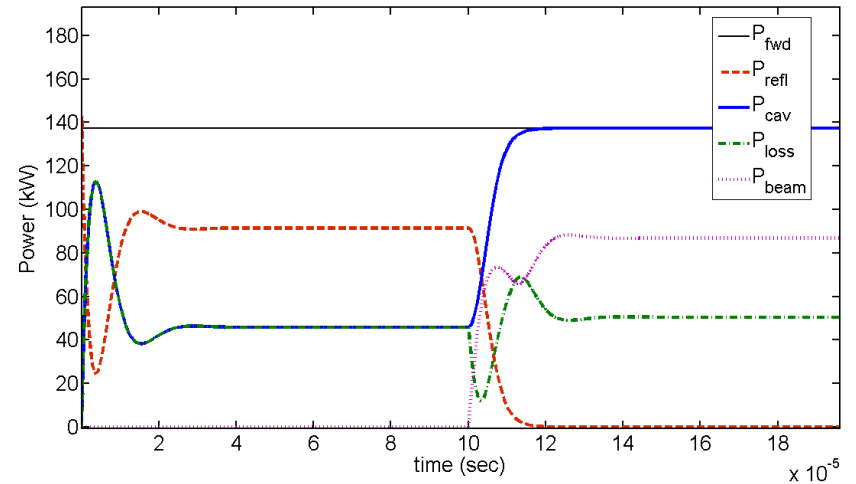
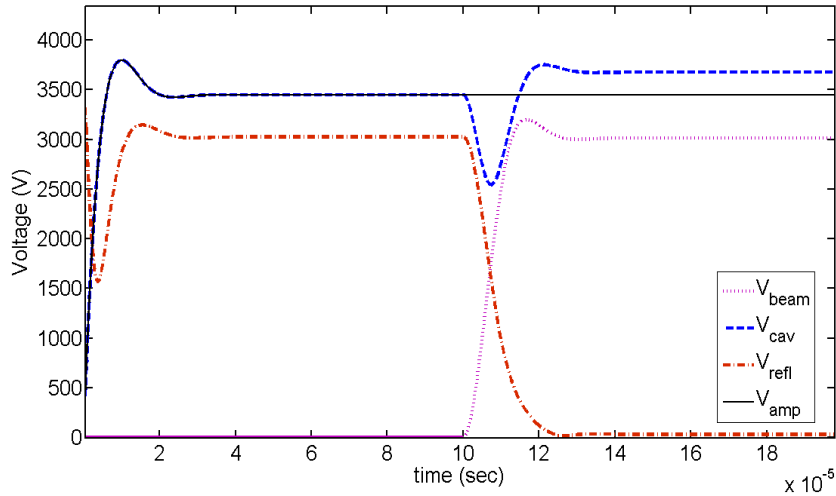
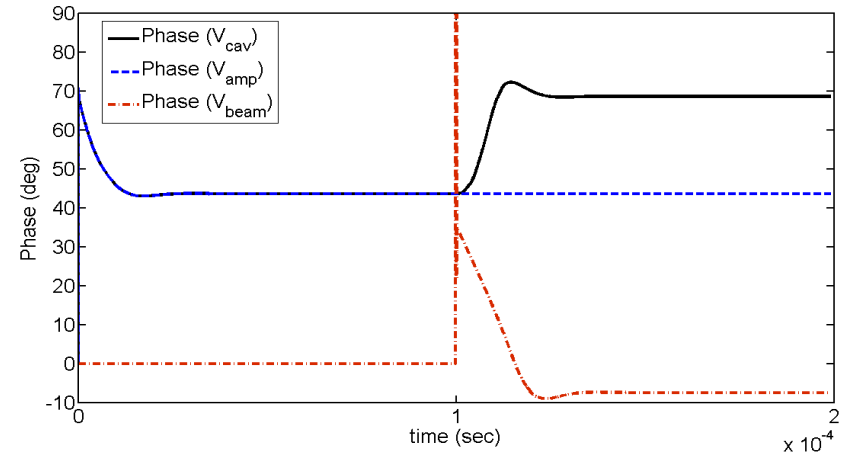
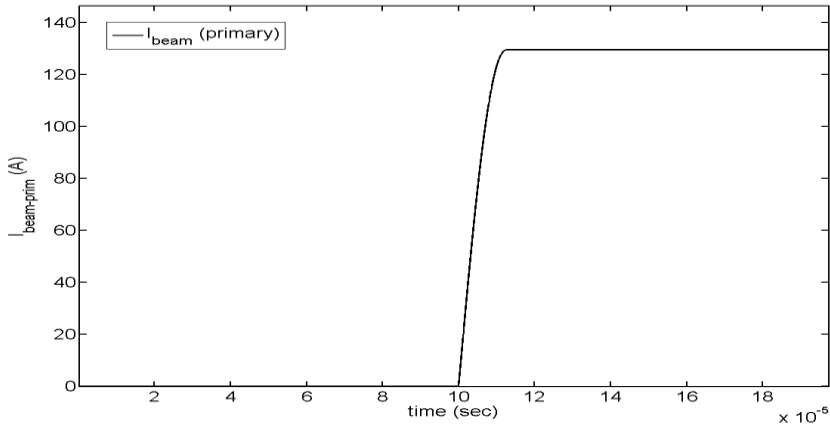
$$\beta = \frac{1}{1 - A.\sin\varphi_S} = \frac{1}{1 - \frac{2I_{DC}}{|\dot{I}_{amp}|} \sqrt{\frac{R}{\beta Z_0}} \sin\varphi_S}$$

$$\zeta = \frac{-A\beta.\cos\varphi_S}{Q_0} = -\frac{2I_{DC}}{|\dot{I}_{amp}|} \cdot \sqrt{\frac{R}{\beta Z_0}} \cdot \frac{\beta.\cos\varphi_S}{Q_0}$$





# Cavity transient simulation





# ESS-Bilbao RFQ / LLRF specifications

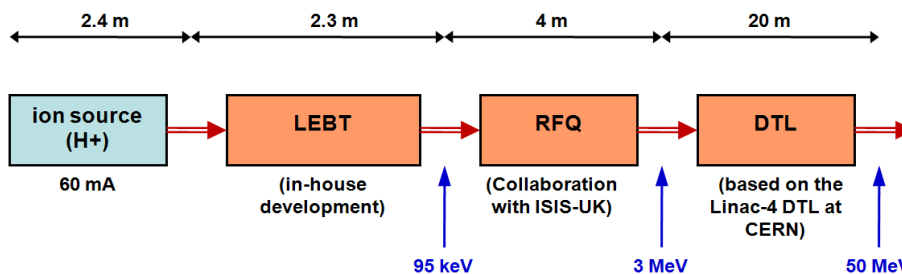
## RFQ parameters

| Parameter   | Value    | unit        |
|---|----------|-------------|
| RF frequency                                      | 352.209  | MHz         |
| RF pulse rate (max)                               | 50       | Hz          |
| RF pulse width (max)                              | 1.5      | ms          |
| Peak Klystron power                               | 2.8      | MW          |
| Unloaded Q  | 9000     |             |
| Ratio of $P_{\text{Copper}}$ to $P_{\text{beam}}$ | 5 to 1   |             |
| Emittance   | $0.2\pi$ | mm.<br>mrad |
| Beam energy at RFQ entrance                       | 95       | keV         |
| Beam energy at RFQ exit                           | 3        | MeV         |

## LLRF specifications / performance

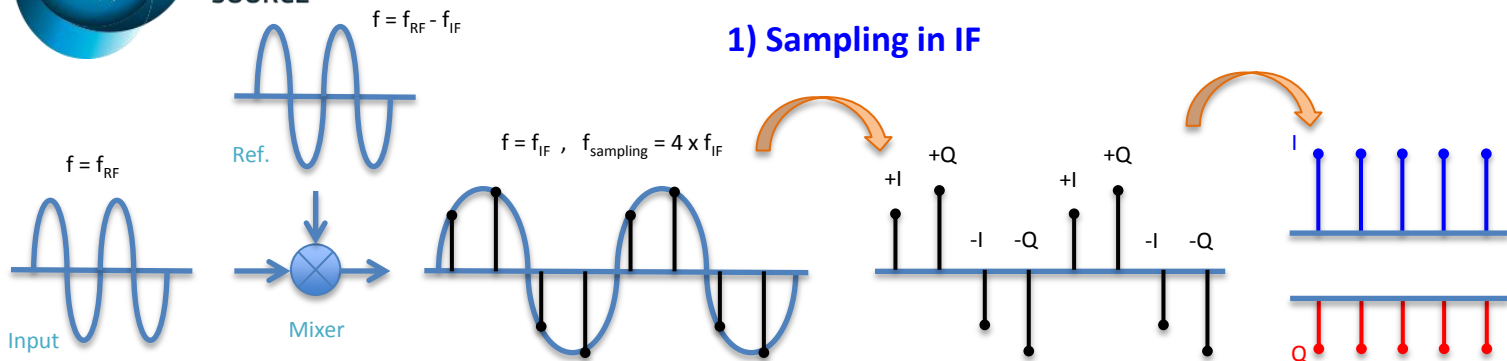
| Parameter                         | Spec.      | Actual      | Unit          |
|-----------------------------------|------------|-------------|---------------|
| Operating mode                    | pulsed     | CW/pulsed   |               |
| Settling time                     | $\leq 100$ | <100        | $\mu\text{s}$ |
| Loop delay                        |            | 800 app.    | ns            |
| Phase noise                       | $\pm 0.5$  | $\pm 0.1$   | $^{\circ}$    |
| Short-term amp. stability         | $\pm 0.5$  | $< \pm 0.1$ | %             |
| Long-term amp. Stability (drifts) | $\pm 0.5$  | $< \pm 0.5$ | %             |
| Linearity                         |            | 100 app.    | %             |
| Dynamic range                     |            | > 30        | dB            |
| Phase margin                      |            | $\pm 55$    | $^{\circ}$    |
| Max. reflected power              |            | <1          | %             |

( $f_{\text{RF}} = 352 \text{ MHz}$ , pulse rate = 50 Hz, pulse width = 1.5 ms)

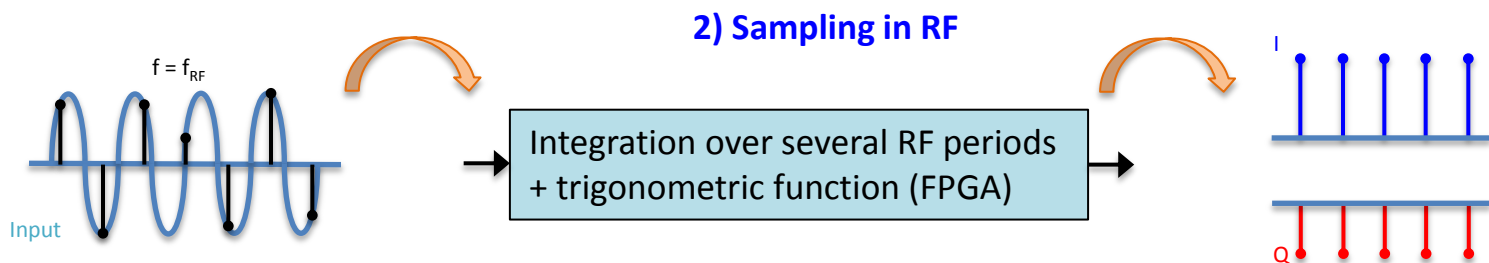




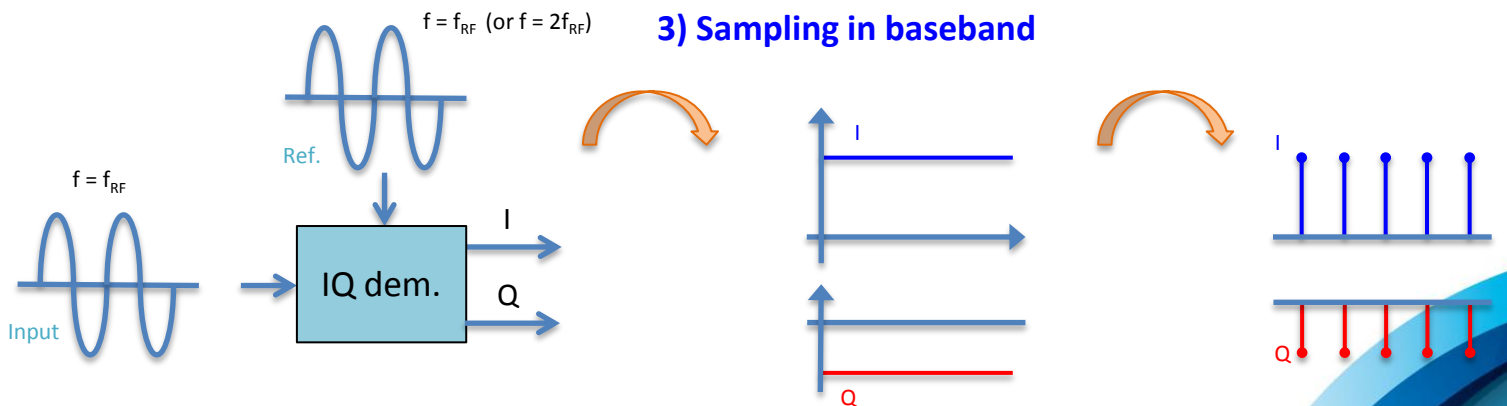
# RF conversion to digital I/Q



- Widely used for DLLRF
- Needs several clocks
- Clocks must be in tight synchronization (PLL or DDS)
- Not reconfigurable



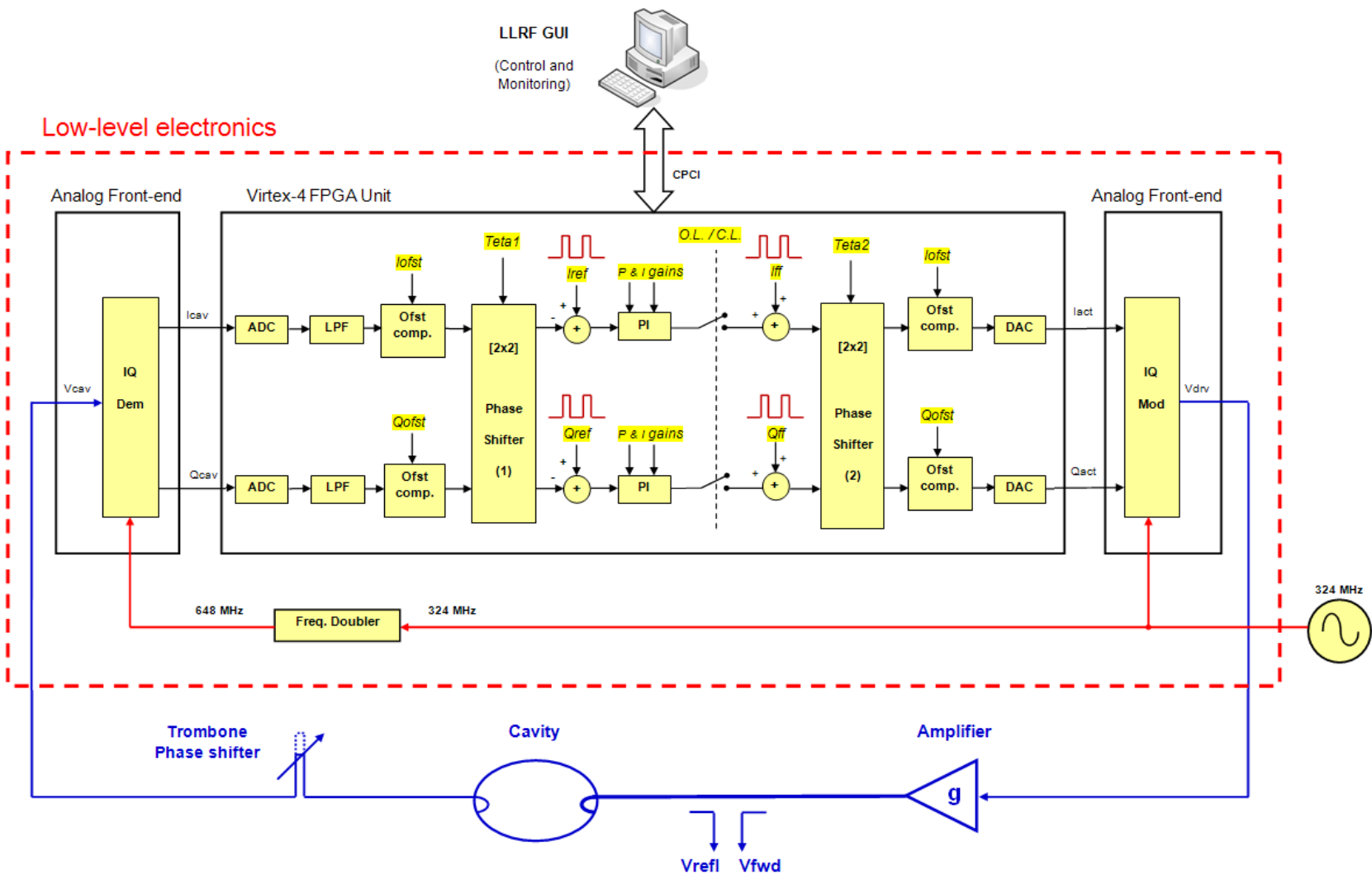
- Recently tested for the ILC
- Improves measurement bandwidth and eliminates errors of RF-IF conversion
- Needs precise clock generation
- Needs very fast ADC and FPGA with extremely low clock jitter



- Easy to implement
- Reconfigurable
- Very simple timing
- Needs 2 ADCs per RF measurement
- IQ dem. errors should be compensated to improve accuracy

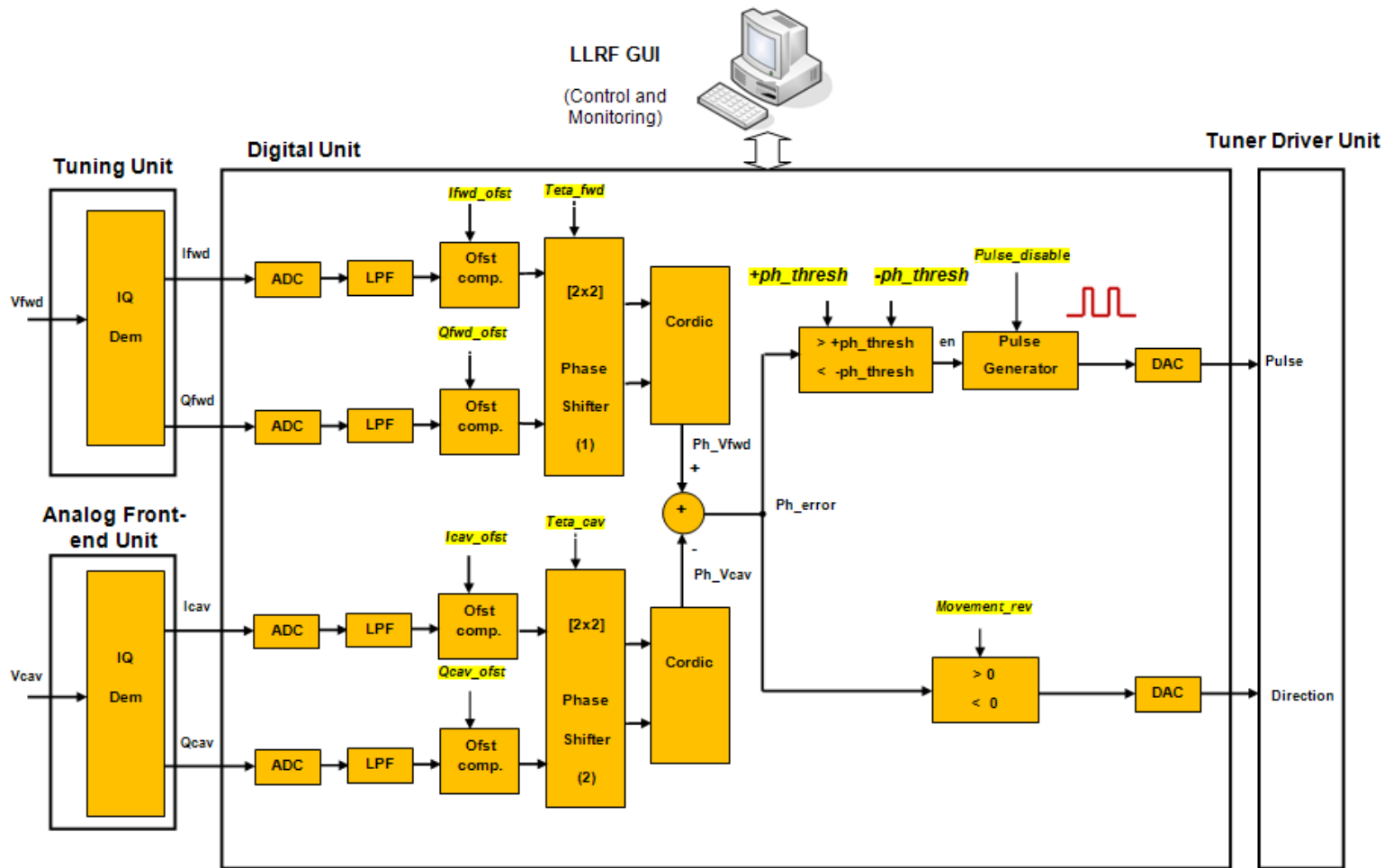


# DLLRF design (amp/ph loops)



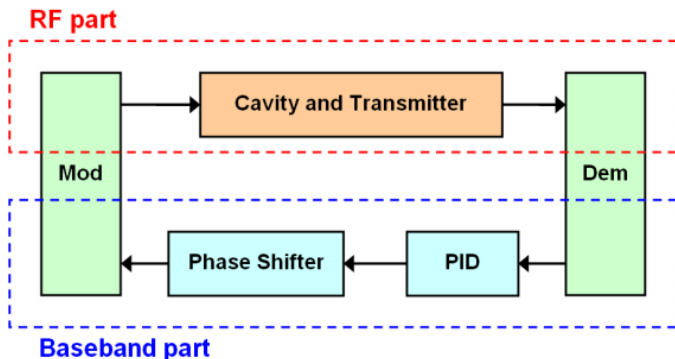


# DLLRF design (tuning loop)

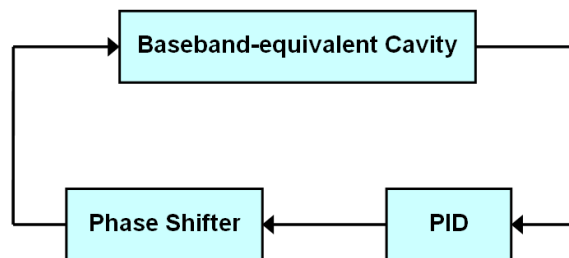




## Baseband-eq. model of the RF cavity



Conventional model of the LLRF Feedback loop



Baseband-eq. model of the LLRF Feedback loop

- A conventional time-domain simulation of the LLRF feedback loop is usually very slow.
- The simulation speed is low because a very small sample time is normally needed for the simulation of the RF signals. On the other hand, the baseband signals have a relatively slow variation with time because of the high cavity quality factor.
- This drawback is resolved in the ADS software from Agilent which only simulates the envelope of RF signals (without RF carrier), hence significantly improves the simulation speed.
- A similar method is presented here for translating the cavity resonant frequency to baseband, leading to a baseband-equivalent model for the LLRF feedback loop with a much higher simulation speed compared to the conventional methods.



## Baseband-eq. modeling of the RF cavity (cont.)

$$\dot{V}_{total}(s) = \left(2\dot{I}_{amp}(s) - \dot{I}_{beam}(s)\right) \frac{\beta Z_0 L s}{RLC.s^2 + L(\beta + 1).s + R}$$

$$\dot{V}_{total}(t) = \left[\dot{V}_r(t) + j\dot{V}_i(t)\right] \cdot e^{j\omega_{RF}t}$$

$$\dot{I}_{total}(t) = \left[\dot{I}_r(t) + j\dot{I}_i(t)\right] \cdot e^{j\omega_{RF}t}$$

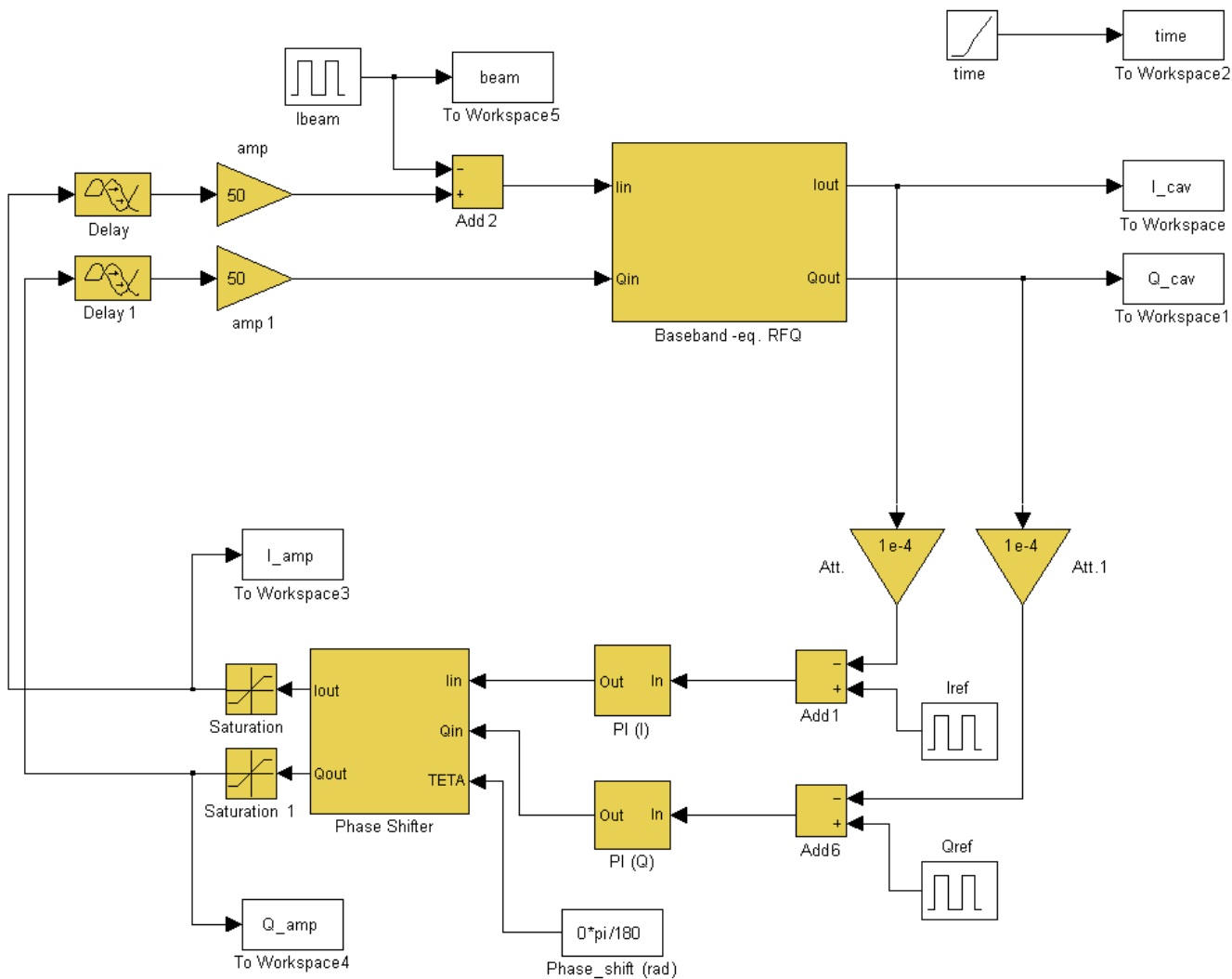
$$\begin{aligned} & \frac{d^2 \left[ (\dot{V}_r + j\dot{V}_i) \cdot e^{j\omega_{RF}t} \right]}{dt^2} + \frac{(\beta + 1)\omega_0}{Q_0} \frac{d \left[ (\dot{V}_r + j\dot{V}_i) \cdot e^{j\omega_{RF}t} \right]}{dt} \\ & + \omega_0^2 (\dot{V}_r + j\dot{V}_i) \cdot e^{j\omega_{RF}t} = \frac{\beta Z_0 \omega_0}{Q_0} \frac{d \left[ (\dot{I}_r + j\dot{I}_i) \cdot e^{j\omega_{RF}t} \right]}{dt} \end{aligned}$$

$$\dot{V}_r(s) = \dot{I}_r(s) \frac{\beta Z_0 \omega_{RF}}{2Q_0.s + (\beta + 1)\omega_{RF}} - \dot{V}_i(s) \frac{(\beta + 1).s + 2Q_0(\omega_0 - \omega_{RF})}{2Q_0.s + (\beta + 1)\omega_{RF}}$$

$$\dot{V}_i(s) = \dot{I}_i(s) \frac{\beta Z_0 \omega_{RF}}{2Q_0.s + (\beta + 1)\omega_{RF}} + \dot{V}_r(s) \frac{(\beta + 1).s + 2Q_0(\omega_0 - \omega_{RF})}{2Q_0.s + (\beta + 1)\omega_{RF}}$$



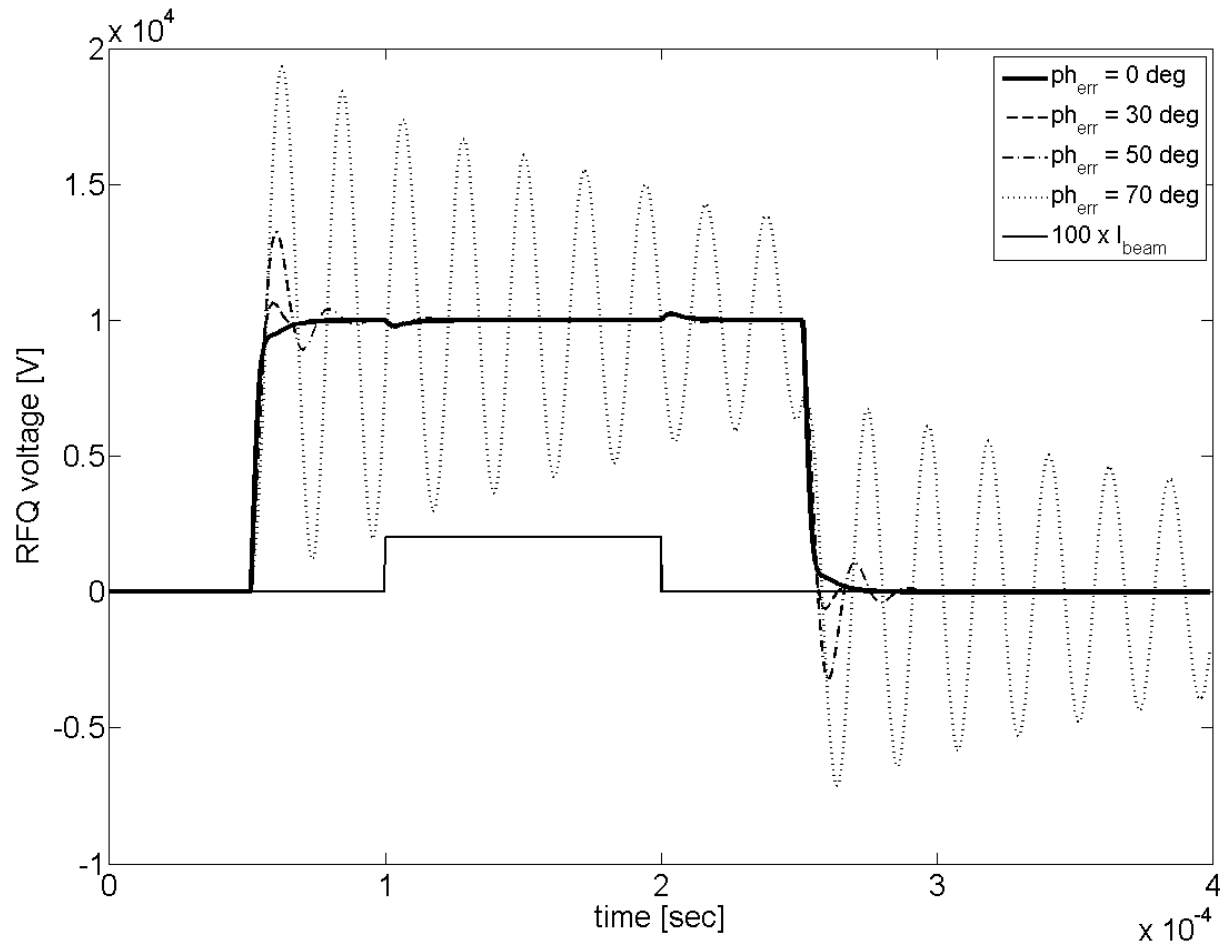
# Baseband-eq. model of the LLRF loop







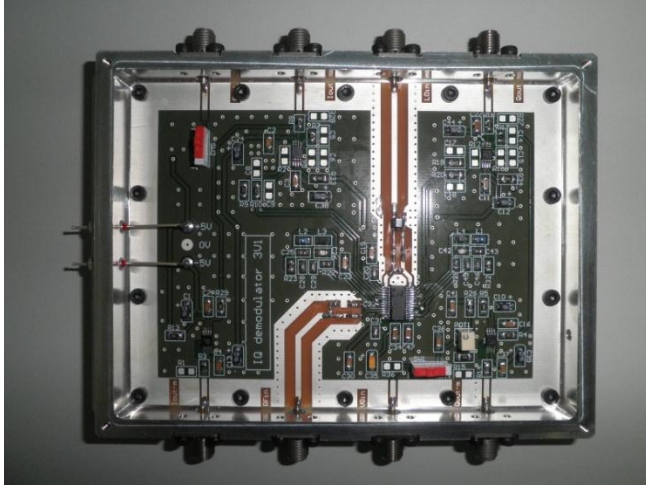
# Baseband-eq. simulation of the LLRF loop



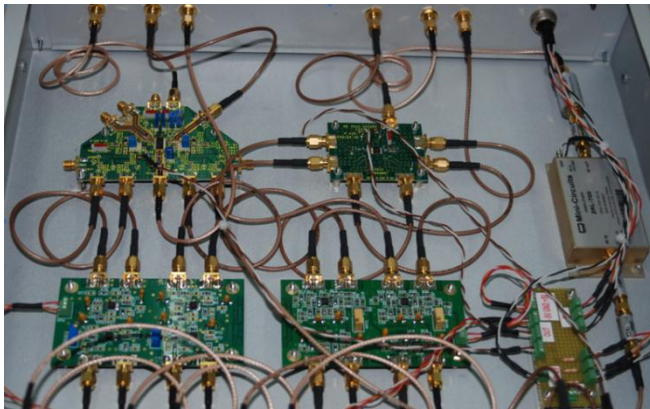


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# Implementation



**In-house developed IQ dem.**



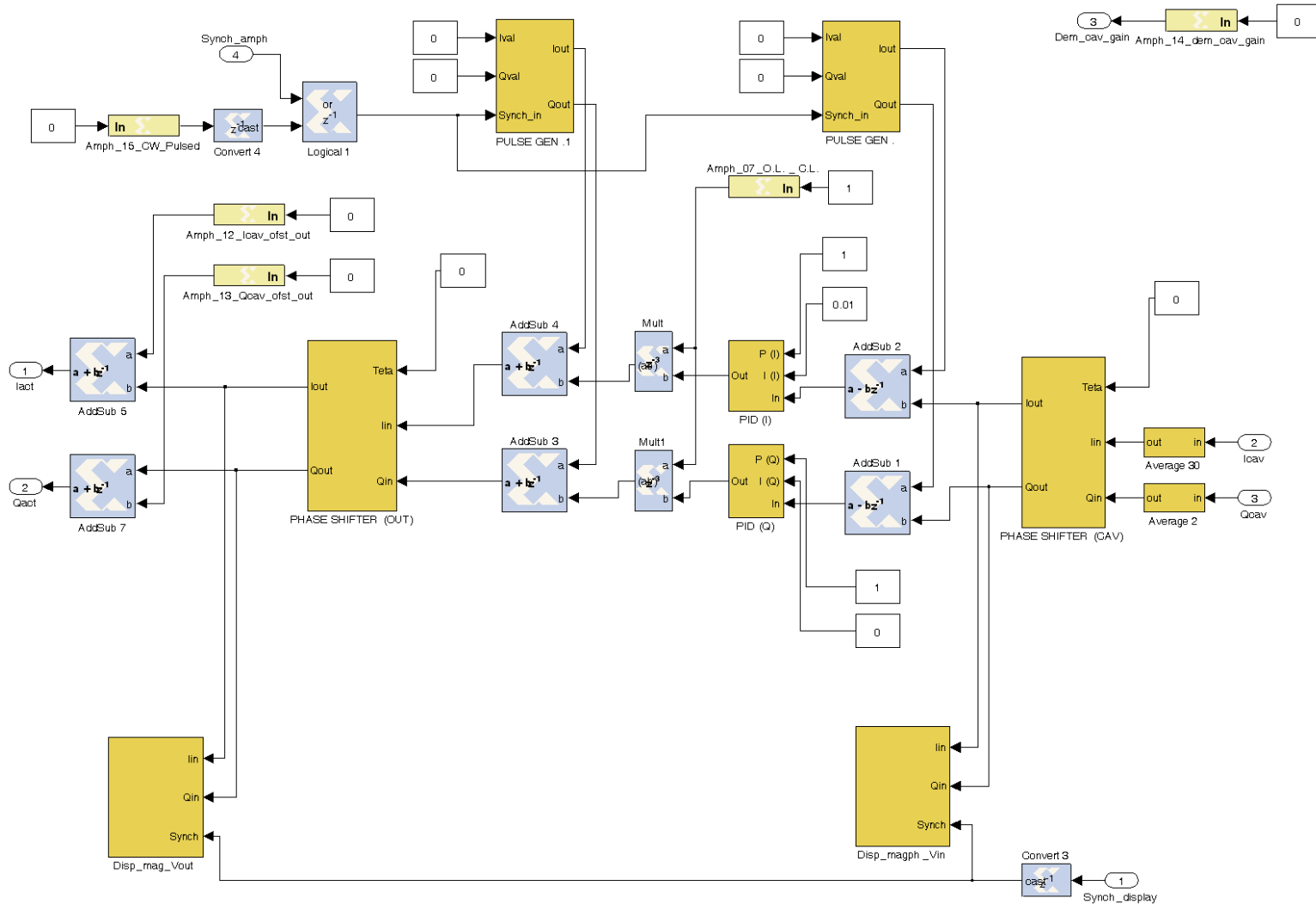
**Analog front-end unit**



**DLLRF prototype (UPV/EHU RF lab)**

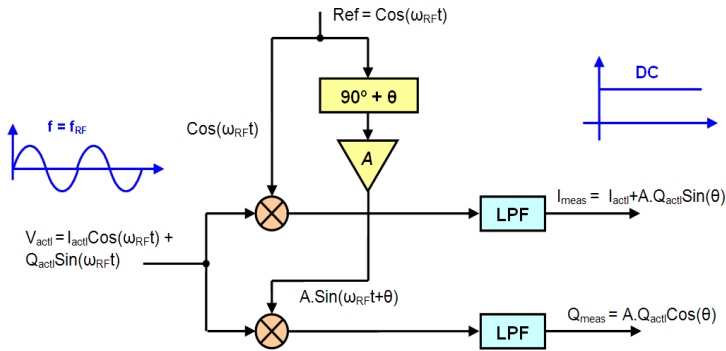


# FPGA programming (model-based)

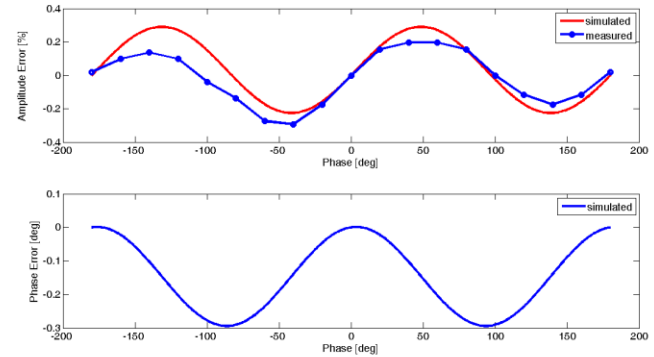




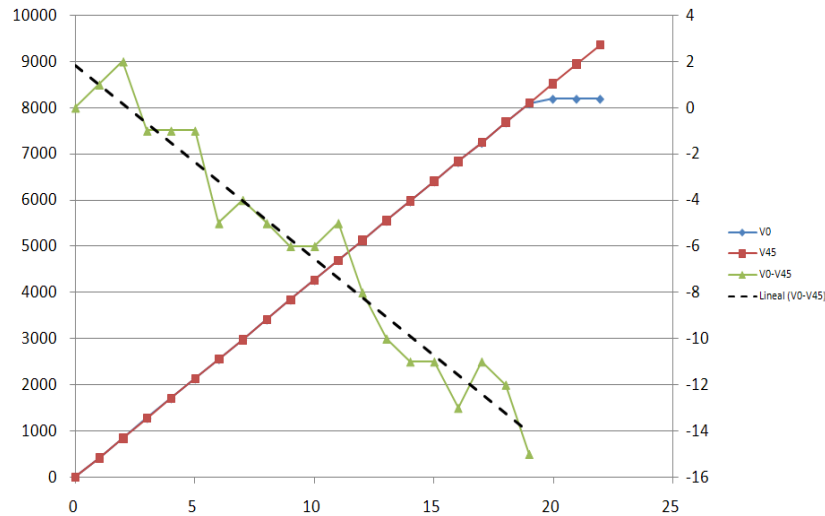
# IQ dem. error compensation



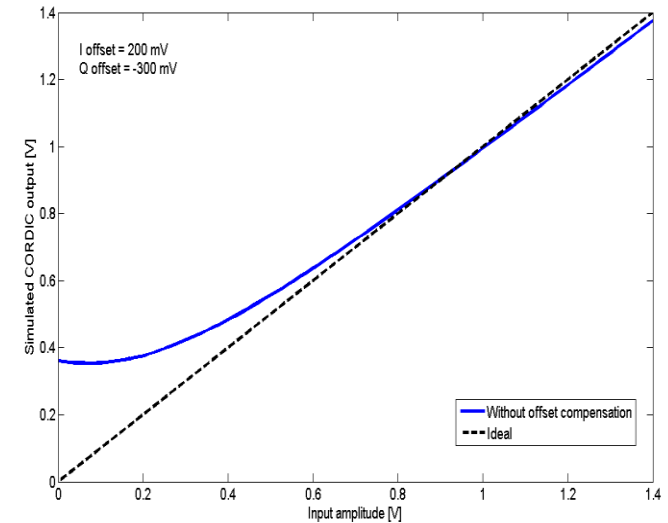
IQ dem. schematics



Amp/ph errors due to gain/ph imbalances



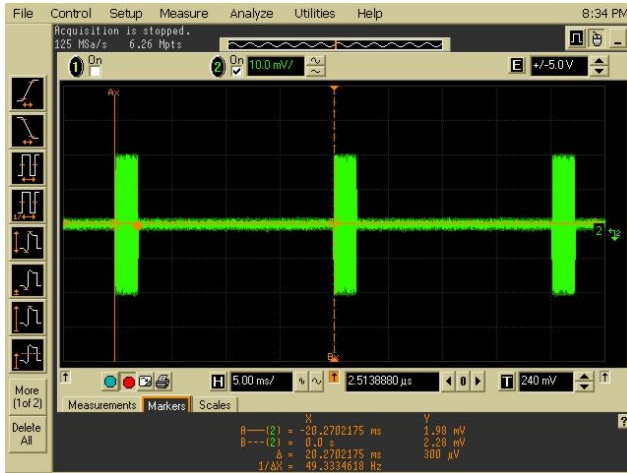
IQ dem. linearity with input phase of  $0^\circ$  and  $45^\circ$



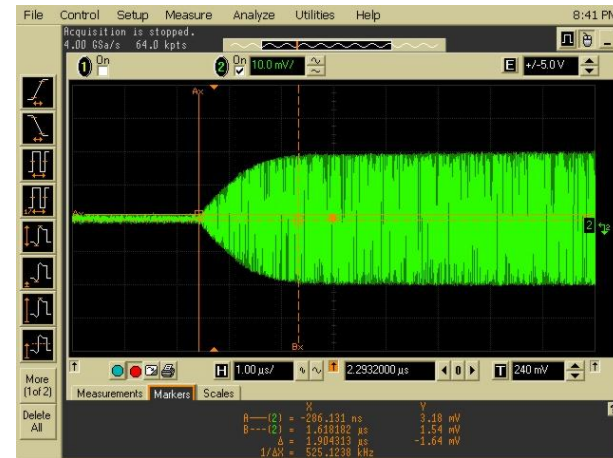
IQ dem. Linearity due to I/Q DC offsets



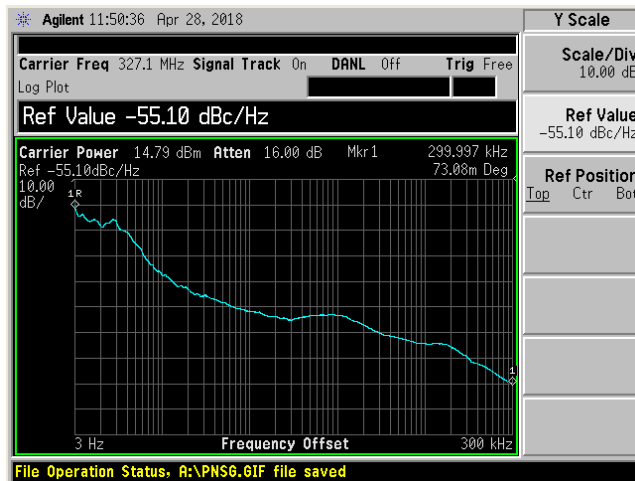
# DLLRF experimental results (pill-box cavity)



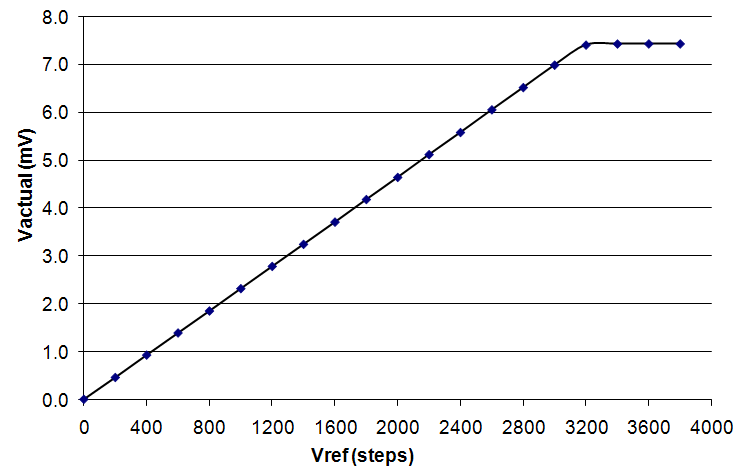
Settling time = 1.9  $\mu$ s



Phase noise = 0.074°

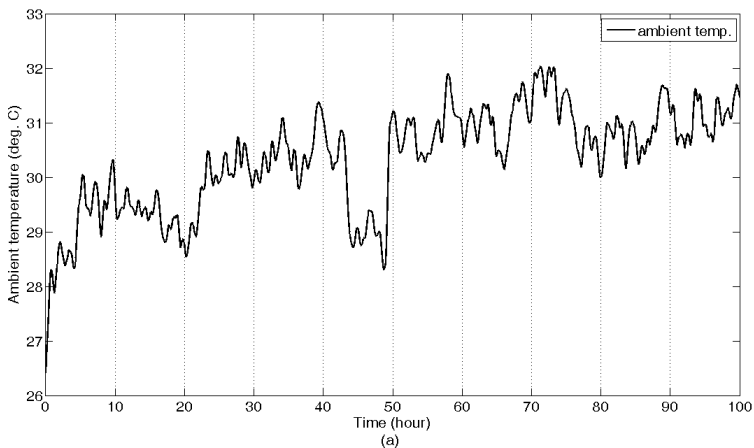


LLRF Linearity

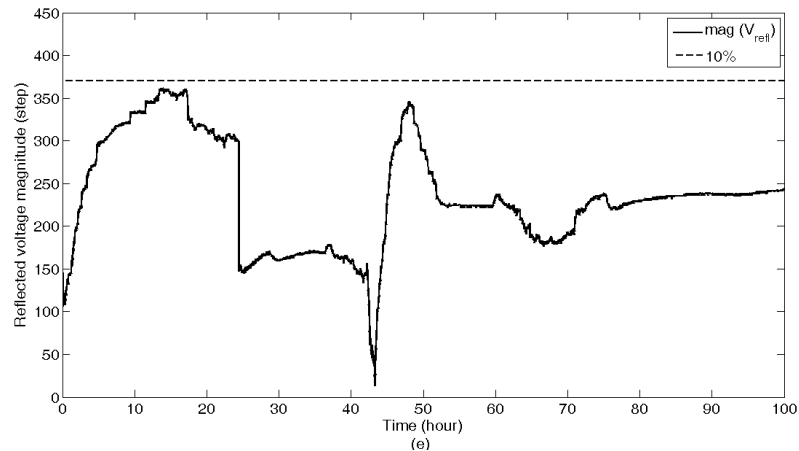




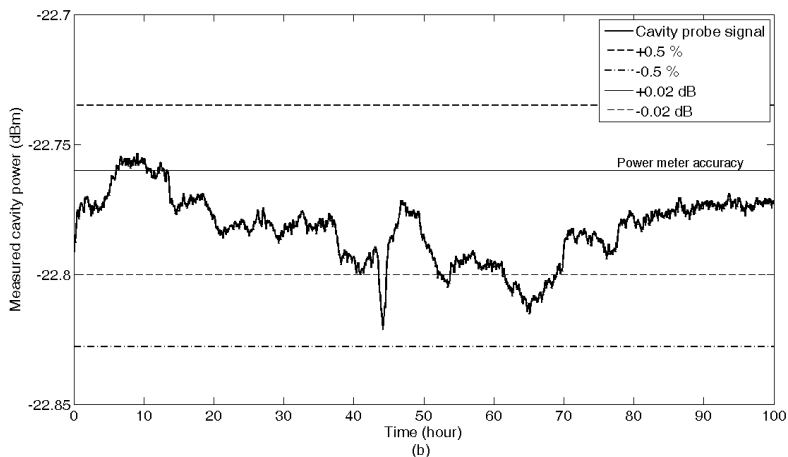
# DLLRF experimental results (100-h tests)



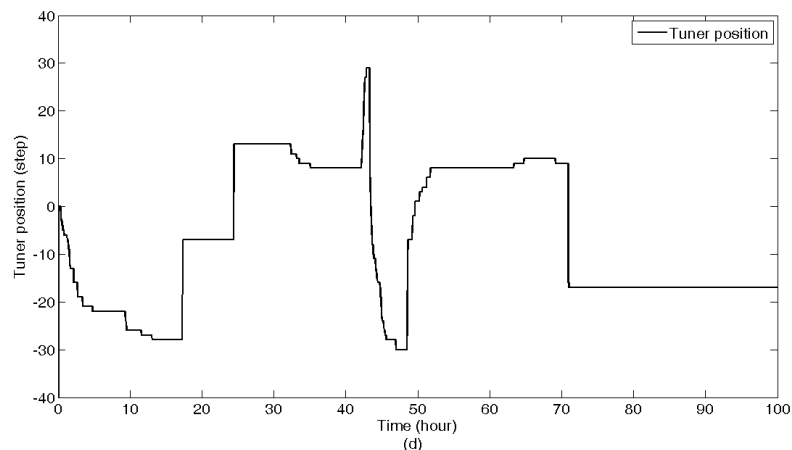
Ambient temperature



Reflected voltage



Cavity voltage



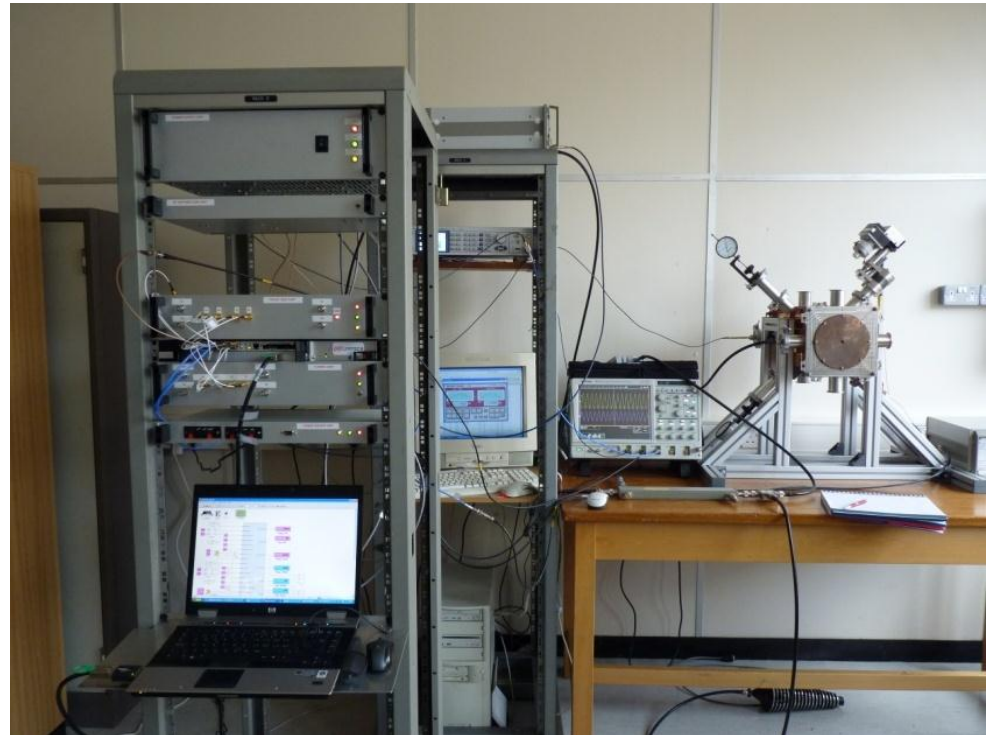
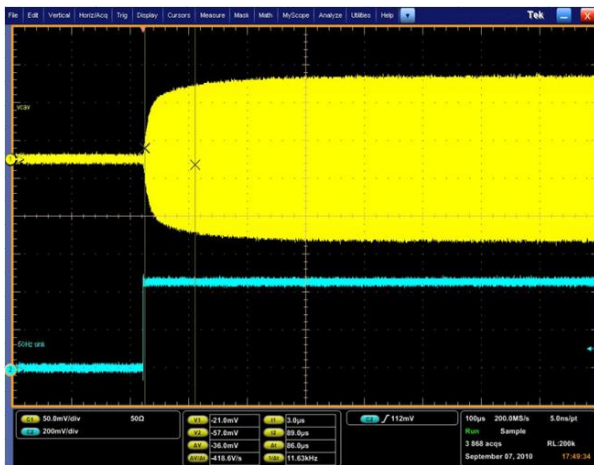
Tuner position





# DLLRF tests with an RFQ copper model

Settling time < 100  $\mu$ s



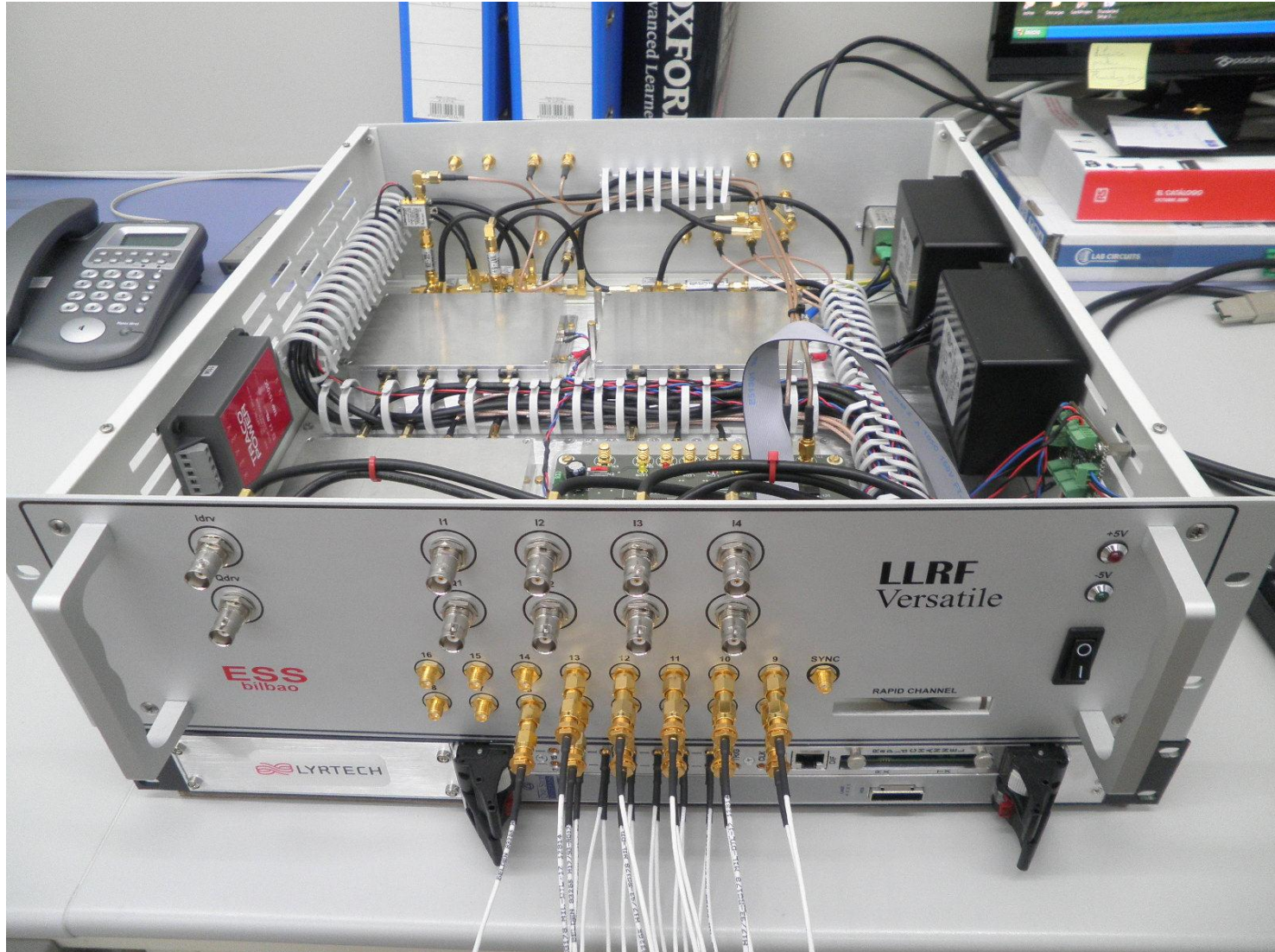
LLRF test setup (Imperial College London)

The test results with the RFQ cold model were very similar to the ones from the pillbox cavity except the settling time which was much larger due the RFQ quality factor.



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# Final version of the DLLRF system







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