

ATx Module Design

- Our goal is to develop VCSEL array based parallel transmitter module with 12 x 10Gbps throughput, MPO optical connection to fiber ribbon, PCB or ceramic substrate for electrical connection.
- Three module designs adapted from miniPOD, QSFP and iFlame transceivers are investigated.
- Driver arrays: Iptronics and Gigopitx EVMs designed. SMU LOCl4 EVMs designed. Will contact Ohio State Univ.
- lasers: ULM and Finisar VCSEL arrays procured
- Mirco optics: Usconec MOI and lens holder assembly procured
- Process options are in discussion with assembly vendors. Ceramic substrate design is under review.

