TWEPP 2012 Topical Workshop on Electronics for Particle Physics

Tuesday, 18 September 2012

POSTERS: First Session (17:00 - 19:00)

  -**Conveners: Mitch Newcomer**

time	[id] title	presenter
17:00	[117] A two-channel, 8-Gbps serializer ASIC for the ATLAS liquid argon calorimeter upgrade	GONG, Datao
17:01	[113] An 8-channel Programmable 80/160/320 Mbit/s Radiation-Hard Phase-Aligner Circuit in 130 nm CMOS	TAVERNIER, Filip Francis
17:02	[13] A CMOS Pixel Sensor with 4-bit Column-Level ADCs for the ILD Vertex Detector	Dr MOREL, FREDERIC
17:03	[14] The Design of 8-Gbps VCSEL Drivers for the ATLAS Liquid Argon Calorimeter Upgrade	LIANG, Futian
17:04	[123] Multi-Gigabit Wireless data transfer at 60 GHz	Mr SOLTVEIT, Hans Kristian
17:05	[26] Implementation and Tests of FPGA-embedded PowerPC in the control system of the ATLAS IBL ROD card	TRAVAGLINI, Riccardo
17:06	[29] Laser Tests of the DEPFET Gated Operation	SCHEIRICH, Jan
17:07	[52] Initial prototype design for the VIPRAM: Vertically Integrated Patten Recognition Associative Memory	HOFF, Jim
17:08	[61] Development of a Readout System for the PANDA Micro Vertex Detector	ESCH, Simone
17:09	[8] Testing and firmware development for the ATLAS IBL BOC prototype	WENSING, Marius
17:10	[95] A low-latency, low-overhead, quick resynchronization line code for the optical data links of the ATLAS liquid argon calorimeter upgrade	Dr LIU, Tiankuan
17:18	[104] The Gigabit Link Interface Board (GLIB) ecosystem	Dr VICHOUDIS, Paschalis
17:19	[128] Temperature Characterization of Versatile Transceivers	OLANTERA, Lauri Juhani
17:20	[15] Upgrade of the Cathode Strip Chamber Level 1 Trigger Optical Links at CMS	Mr MATVEEV, Mikhail
17:21	[152] Irradiation tests on InP based Mach Zehnder Modulators	GAJANANA, Deepak
17:22	[32] Low-cost, high-precision propagation delay measurement of 12-fibre MPO cables for the CMS DT Electronics Upgrade	NAVARRO TOBAR, Alvaro
17:23	[50] CuOF : an electrical to optical interface for the upgrade of the CMS Muon Drift Tubes system	DE REMIGIS, Paolo
17:24	[87] The 120Gbps optical transmitter development for the High-Luminosity LHC (HL-LHC) experiments	XIANG, annie
17:31	[1] Triggerless Readout Architecture for the Silicon Pixel Detector of the PANDA Experiment	MAZZA, Giovanni
17:32	[100] Real Time Event Building for a Pixel Tracking Telescope Using an Advanced Mezzanine Card and MicroTCA	Mr PROSSER, Alan
17:33	[153] The ATLAS Pixel nSQP readout chain	WELCH, Steven
17:34	[16] The LHCb Silicon Tracker: Running experience	Ms SAORNIL GAMARRA, Sandra

17:35	[62] Further Development of the MTCA.4 Clock and Control System for the EuXFEL Megapixel Detectors	COOK, Sam
17:36	[96] The PANDA MVD Strip Detector	Mr SCHNELL, Robert
17:43	[116] CMS Trigger Drift Tube Track Finder electronics upgrade Hardware Simulations	ERO, Janos
17:44	[20] An FPGA based topological processor prototype for the ATLAS Level-1 trigger upgrade	WENZEL, Volker
17:45	[21] Instrumentation of a Level-1 Track Trigger at ATLAS with Double Buffer Front-End Architecture	COOPER, Ben
17:46	[33] The Upgrade of the PreProcessor System of the ATLAS Level-1 Calorimeter Trigger	ANDREI, Victor
17:47	[41] The Trigger System in the NEXT-DEMO detector	Mr ESTEVE, Raul
17:48	[7] the Optical Synchronization and Link Board project, oSLB	Mr DA SILVA, Jose Carlos
17:49	[97] Multi-hundred Gbps processing boards for calorimeter trigger upgrades at CMS	Dr ROSE, Andrew William

Thursday, 20 September 2012

POSTERS: Second Session (17:00 - 19:00)

  -Conveners: Mitch Newcomer

time	[id] title	presenter
17:00	[151] ASIC design in the KM3NeT Detector	GAJANANA, Deepak
17:01	[155] A 0.18 μm CMOS Low-Power Radiation Sensor for UWB Wireless Transmission	GABRIELLI, Alessandro
17:02	[18] SPIROC: design and performance of a dedicated very front-end electronics for an ILC Hadronic CALimeter (HCAL) prototype with SiPM read-out	CONFORTI DI LORENZO, Selma
17:03	[34] Very fast front end ASIC associated with multi anode PMT for a scintillating-fiber beam hodoscope	Mr DENG, Shiming
17:05	[53] CLARO-CMOS, an ASIC for single photon counting with PMTs, MCPs and SiPMs	GOTTI, Claudio
17:06	[76] Front End ASIC design for SiPM Readout	COMERMA MONTELLS, Albert
17:13	[115] Production, measurement and simulation of a low mass flex cable for multi gigabit/s readout for the LHCb VELO upgrade.	LEMOS CID, Edgar
17:14	[124] Hybrid circuits for the CMS Tracker upgrade front-end electronics	BLANCHOT, Georges
17:15	[143] Developments in the Use of Micro-structured Silicon devices for Thermal Management of HEP Detectors	MAPELLI, Alessandro
17:22	[105] The Upgraded CMS Preshower High Voltage System	Dr VICHOUDIS, Paschalis
17:23	[111] A Serially Powered ATLAS Strip Tracker Stavelet with Improved Referencing Connections	PHILLIPS, Peter
17:24	[125] The CMS ECAL Barrel HV power supply	BARTOLONI, Alessandro
17:25	[129] Prototype linear voltage regulators for the ABC130 front-end chip	BOCHENEK, Michal
17:26	[136] Conducted and radiated noise distribution on Pt rod power network for CMS Tracker upgrade.	ESTEBAN LALLANA, Maria Cristina
17:27	[60] Power pulsing schemes for vertex detectors at CLIC	FUENTES ROJAS, Cristian Alejandro
17:34	[22] Quality assurance and functionality tests on electrical components during the ATLAS IBL production	JENTZSCH, Jennifer
17:35	[28] A new portable test bench for the ATLAS Tile Calorimeter front-end electronics	MORENO, Pablo
17:36	[90] Production Test Engineering in FE-I4 System-on-Chip to boost the Reliability and High-Quality demands in IBL applications	ZIVKOVIC, Vladimir
17:43	[58] New prototypes for components of a control system for the new ATLAS pixel detector at the HL-LHC	PÜLLEN, Lukas
17:50	[118] Development of a custom on-line ultrasonic vapour analyzer/flowmeter instrument for the ATLAS inner detector, with application to gaseous tracking and Cherenkov Detectors	KATUNIN, Sergey
17:51	[114] New data acquisition system for the COMPASS experiment	NOVY, Josef BODLAK, Martin JARY, Vladimir
17:52	[130] firmware approach for TEL62 trigger and data acquisition board	PEDRESCHI, Elena

17:53	[135] Soft Error Recovery during Operation of the CMS Experiment	RAKNESS, Gregory
17:54	[137] Mitigation of Anomalous APD signals in the CMS ECAL	BIALAS, Wojciech
17:55	[2] The First G-APD Cherenkov Telescope (FACT) camera and its electronics - overview, operation experience and outlook	Mr VOGLER, Patrick
17:56	[25] The NA62 Large Angle Veto front end electronic board	Dr GONNELLA, Francesco
17:57	[39] Development of a readout link board for the TileCal phase 2 demonstrator	MUSCHTER, Steffen Lothar
17:58	[69] A new Readout Control system for the LHCb Upgrade at CERN	ALESSIO, Federico
17:59	[71] Readout system for high resolution resistive plate chambers	CUSSANS, David
18:00	[81] Cold electronics for the LBNE LAr TPC	THORN, Craig
18:01	[99] Microcontroller based data acquisition system for silicon photomultiplier detectors.	RYDER, Nick