



# TWEPP 2012 Topical Workshop on Electronics for Particle Physics

## Tuesday, 18 September 2012

### **B2: Programmable Logic, design tools and methods** - Lindemann Lecture Theatre (14:50 - 16:30)

-Conveners: Magnus Hansen

| time  | [id] title  | presenter           |
|-------|---|---------------------|
| 14:50 | [24] A Digitization Scheme of Sub-microampere Current Using a Commercial Comparator with Hysteresis and FPGA-based Wave Union TDC | Dr WU, Jinyuan      |
| 15:15 | [35] The GANDALF 128-channel Time-to-Digital Converter  | BUCHELE, Maximilian |
| 15:40 | [57] Development and Implementation of Optimal Filtering in a Virtex FPGA for the Upgrade of the ATLAS LAr Calorimeter Readout    | STAERZ, Steffen     |
| 16:05 | [67] Field Programmable Gate Array Based Data Digitisation with Commercial Elements   | Dr TRAXLER, Michael |