Operational conditions for enhancement of collected charge via avalanche multiplication in n-on-p strip detectors

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Recent results on the collected charge Qc in heavily irradiated Si detectors developed by RD50 collaboration for SuperLHC revealed a significant Qc enhancement if detectors were operated at the bias voltage beyond 1000 V. Our investigations showed that this enhancement arises from a fundamental effect of carrier avalanche multiplication in high electric field of n+-p junction. This study extends the PTI model and gives the results on the detector bias voltage and the other operational conditions as well as detector geometry which lead to Qc enhancement. Simulations predict that the maximum Qc in irradiated detectors may be larger than in a non-irradiated detector that agrees with the experimental data.

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