Ultra-Fast Silicon Detectors

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Ultra-Fast Silicon Detectors (UFSD)

provide in the same detector and readout chain

- ultra-fast timing resolution [10's of ps]
- precision location information [10's of μm]

(N.B. a time resolution \approx 50 ps would already be competitive with SiPM)

2 questions:

- can they work: signal, capacitance, collection time vs. thickness
- will they work: required gain and E-field, fast readout

Disclaimer: data are still coming in, so conclusions and extrapolation are tenuous!

Motivation

- Up to now, semiconductor sensors have supplied precision data only for the 3 space dimensions (diodes, strips, pixels, even "3D"), while the time dimension has had limited accuracy (e.g. to match the beam structure in the accelerator).
- We believe that being able to resolve the time dimension with ps accuracy would open up completely new applications not limited to HEP
- An example in HEP are forward physics projects at the LHC, like the AFP. Scattered protons are tracked from stations 100's of meters downstream back to the interaction region and the z-vertex is determined from the timing information. So good position resolution and excellent timing are required, which at the moment is done with different detector technologies (pixels and Micro-channel plates.
- Proposal: Combined-function pixel detector will collect electrons from thin non-p pixel sensors read out with short shaping time electronics
- Charge multiplication with gain g increases the collected signal
- Need very fast pixel readout

UFSD Pixel Collected Charge

Signal = thickness*EPM Collection time = thickness/vsat (vsat = 80µm/ns)

				BackPlane	
	Gain req.	Coll. Time	Signal	Capacitance	Thickness
	for 2000 e	[ps]	[# of e-]	[fF]	[um]
Realistic	241.0	1.3	8.3	2500	0.1
gain & cap	24.1	12.5	83	250	1
	12.0	25.0	166	125	2
	4.8	62.5	415	50	5
	2.4	125.0	830	25	10
Cood time	1.2	250.0	1660	13	20
resolution	0.2	1250.0	8300	2.5	100
	0.1	3750.0	24900	0.8	300

For thickness > 5 um, Capacitance to the backplane Cb << Cint For thickness = 2 um, Cb $\sim \frac{1}{2}$ of Cint, and we might need bipolar (SiGe)?

Fast charge collection: Drift Velocity and E-Field

Gregor Kramberger, 19th RD50 Workshop



As long as E-field E > 20kV/cmdrift velocity $v_{drift} = vsat \approx 80\mu m/ns$

Full Depletion Voltage V_{FD}[V]

Res. Rho		Thickness [um]				
[kohm-cm]	20	10	5	2		
0.01	453	113	28	4.5		
0.1	45	11	2.8	0.45		
1	4.5	1.1	0.28	0.045		
10	0.45	0.11	0.028	0.0045		

Bias Voltage for Emin =20kV/cm

Res. Rho	Thickness [um]					
[kohm-cm]	20	10	5	2		
0.01		135	40	9		
0.1	90	35	14	5		
1	50	21	11	4		
10	40	20	10	4		

Benefits of Gain in Detectors

 \oplus Charge multiplication (CM) in silicon sensors (discovered by RD50 institutions) might have applications beyond off-setting charge lost due to trapping during the drift of electrons or holes.

 \oplus Charge multiplication makes silicon sensors similar to drift chambers (DC) or Gas Micro-strip Detectors (GMSD), where a modest number of created charges drift to the sense wire, are amplified there (by factors of > 10⁴) and are then used for fast timing. \oplus We propose considering silicon detectors for simultaneous precision position and fast timing measurements.

 \oplus Recall our experience with DC:

Need to balance the need of high E-field around a wire to have charge multiplication with the need to keep the E-field low to prevent breakdown (wire diameter, field shaping wires,.) and give proper drift field

The drifting electrons contribute mainly to the collected charge after they have undergone charge multiplication, which means that the pulse develops (in principle) in a short time.

But the very large charge density in the "plasma cloud" prevents the electron to move until the ions have drifted away! So electron signal given by hole dynamics!?

⊕ Basic question for use of CM

What field strength do we need?

Can the sensor geometry and doping profile engineered so that the amplification field can be kept high, but just below the breakdown field?

Estimate of pulse shape from Ramo



Timing with falling edge?

Impact ionization - Overstraeten model

In high electric fields electrons and holes acquire enough energy to initiate sizeable charge multiplication

Electrons >~ 300 kV/cm Holes >~ 400 kV/cm

$$\alpha_{\mathrm{e,h}}(\mathcal{E}) = \alpha_{\infty_{\mathrm{e,h}}} \exp(-b_{\mathrm{e,h}}/|\mathcal{E}|).$$

> 300 kV/cm < E < 400 kV/cm
Only electrons ionize
Linear mode
Gain < 10
N(I) = exp(αI) with I=path length in
high electric field

Anna Macchiolo, 16th RD50 Workshop Barcelona

"Avalanche" needs distance to develop, but here I \leq 10µm! Require max and min E-field close to breakdown field (270kV/cm)



Bias Voltages to reach maximum E-field large over-voltage required (diode)

Res. Rho				Thickne	ess [um]			
[kohm-cm]			20			-	0	
	VFD	V	Emax	Emin	VFD	V	Emax	Emin
0.01	453				113	157	270	44
0.1	45	495	270	225	11	259	270	248
1	4.5	535	270	265	1.1	269	270	268
10	0.45	540	270	270	0.11	270	270	270

Res. Rho				Thickne	ess [um]			
[kohm-cm]			5				2	
	VFD	V	Emax	Emin	VFD	V	Emax	Emin
0.01	28	107	270	157	4.5	50	270	225
0.1	2.8	132	270	258	0.45	54	270	265
1	0.28	135	270	268	0.045	54	270	270
10	0.028	135	270	270	0.0045	54	270	270

E-Field across a pixel



Anna Macchiolo, 16th RD50 Workshop Barcelona

Simplified simulation of strip sensors, real E-field variation not as bad because the hole current injection stabilizes the field but even 30% difference (Elena) is bad



I. Mandić, RESMDD08, Florence, Italy, 15th -17th October 2008

E-Field across a pixel will vary, resulting in different gains in the center and the edges.

Estimated e-Yield vs. E-Field and Thickness Thickness [um] Bias V Emax Emin 10 5 2 3k 100% 270 **51k** 0.4k 270 90% 243 243 11k 1.5k 0.3k 217 80% 217 3.2k 0.8k 0.2k

To make use of E-field close to breakdown requires uniformity across pixel

Epi, short drift distances and planar diode gives g = 6.5

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What about fast readout:

- CERN fixed-target experiment (NA62) needs very fast pixel sensors: Gigatracker (GTK)
- Prototype CFD system (INFN Torino) has ~ 100 ps resolution, predicted to be 30 ps in next iteration.
- Optimized for 200µm sensors and hole collection (?), could it be re-designed for electron collection from 2 10µm sensors?

Summary and more questions:

- Thin pixel sensors with moderate gain could give time resolution between 10 and 100 ps.
- Epi diodes seem perform adequately (g=6.5). How can we engineer a pixel sensor to have a uniform field across the pixel like a planar diode? Graded doping concentration? Special deep implants? Trenches?
- How much can we over-deplete pixel sensors to raise E_{min} ?
- Do we need irradiated sensors, or can we use low-ohmic sensors with field shaping?
- Charge multiplication increases after long annealing (Igor, Marco), could this be employed?
- Start making thin epi strip sensors
- Extend simulations to very thin sensors!
- Need to collaborate with chip designers.

Back-up

E-Field, Depletion Voltage, Resistivity, Thickness

- Assumptions:
 - Desired signal charge 2000 e-
 - Observed Gain:
 - irradiated 3D sensors: g ≈ 3.5 (Freiburg),
 - irradiated epi diodes g = 6.6 (Hamburg)
 - gain > 20 unrealistic?
 - N-on-p pixels (collect electrons)
 - Pixel area A = 50 um x 50 um
 - Electron yield EPM = 80 e-/um
 - Saturated drift velocity vsat = 180 um/ns
 - Breakdown field EB ≈ 270 kV/cm
 - Full depletion Voltage VFD = cons*d²/rho

cons = 0.0113, d in um, rho in kOhm-cm

- Max Efield at depletion $Emax = 2*V_{FD}/d$
- Max Efield at Bias Voltage V Emax = $(V+V_{FD})/d$
- Min Efield at Bias Voltage V Emax = $(V-V_{FD})/d$
- Capacitance to backplane Cb = A/d
- Capacitance to neighbors Cint \approx 200 fF