Production and test of the LHCf microstrip silicon system

Florence, June 28th 2007 - RD07

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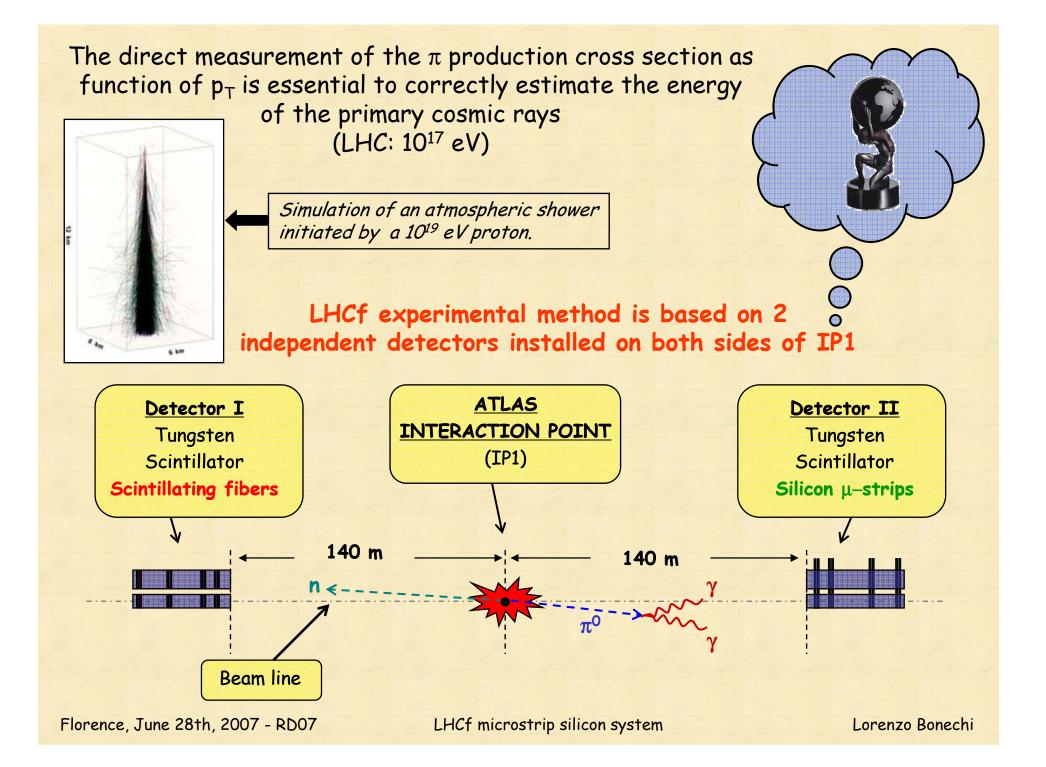
Outline

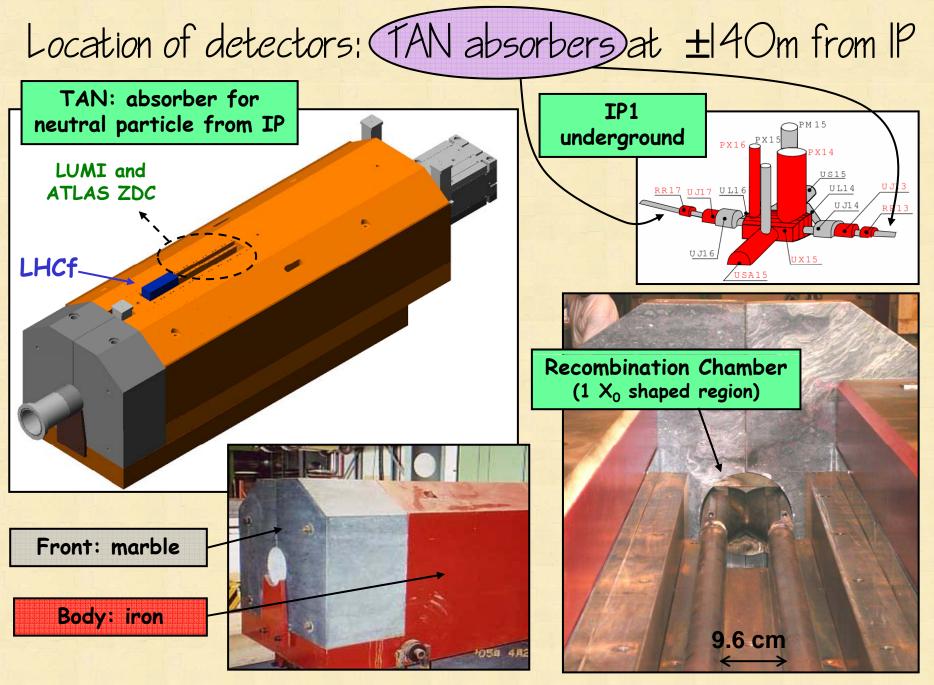
- Introduction
 - About the LHCf experiment (method and location)
- The LHCf apparatus
 - Some details about the detectors
 - The μ -strip silicon system: production and test
- Beam test
 - CERN, Sept. 2006 (few preliminary results)
- Summary and schedule
 - Toward the 2008 LHC operation

LHCf microstrip silicon system

Introduction: the LHCf experiment a) Method b) Location c) Detector

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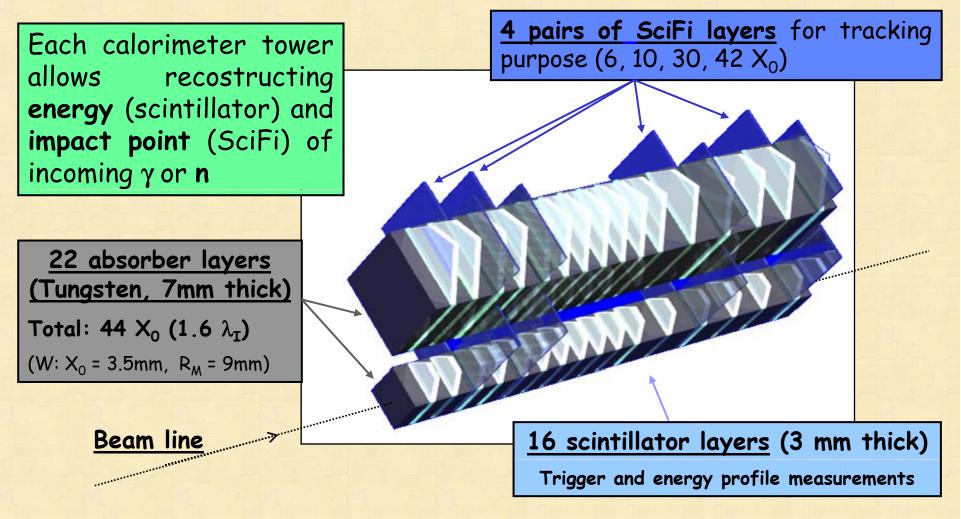
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Detector #1

<u>2 independent calorimeter "towers"</u> 24 cm long vertically stacked (5 mm gap)

Lower tower: 2 cm x 2 cm area

Upper tower: 4 cm x 4 cm area

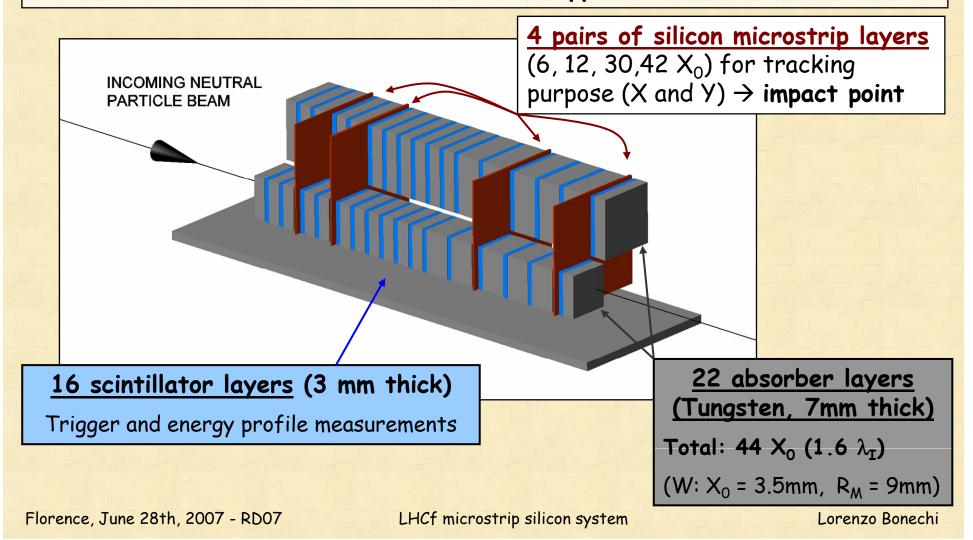


Detector #2

2 independent calorimeter towers 24 cm long stacked on their edges and offset from one another

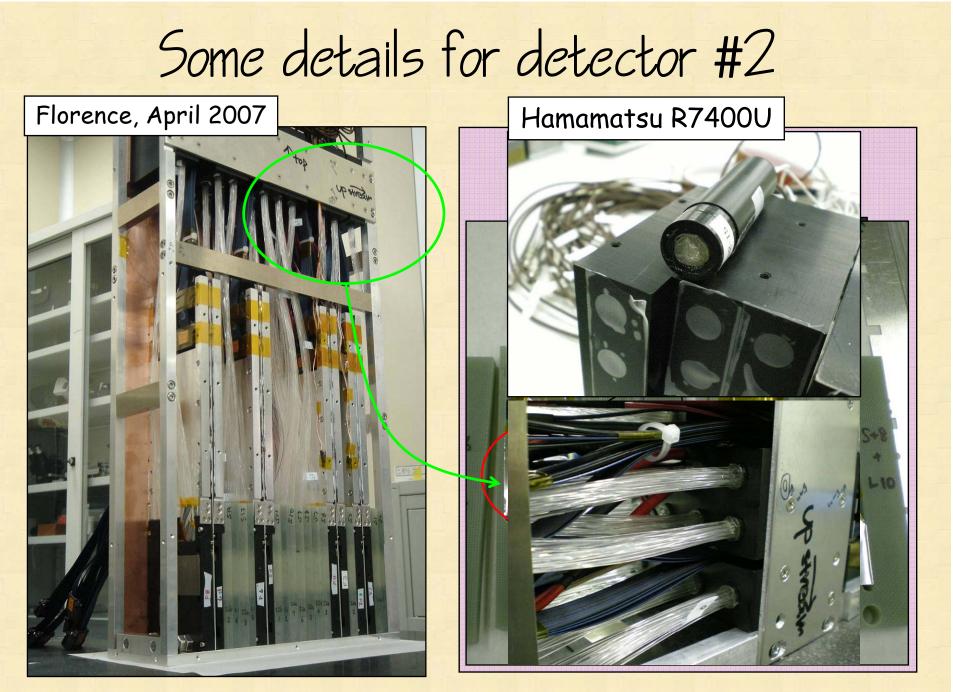
Lower: $2.5 \text{ cm} \times 2.5 \text{ cm}$ area

Upper: 3.2 cm x 3.2 cm area

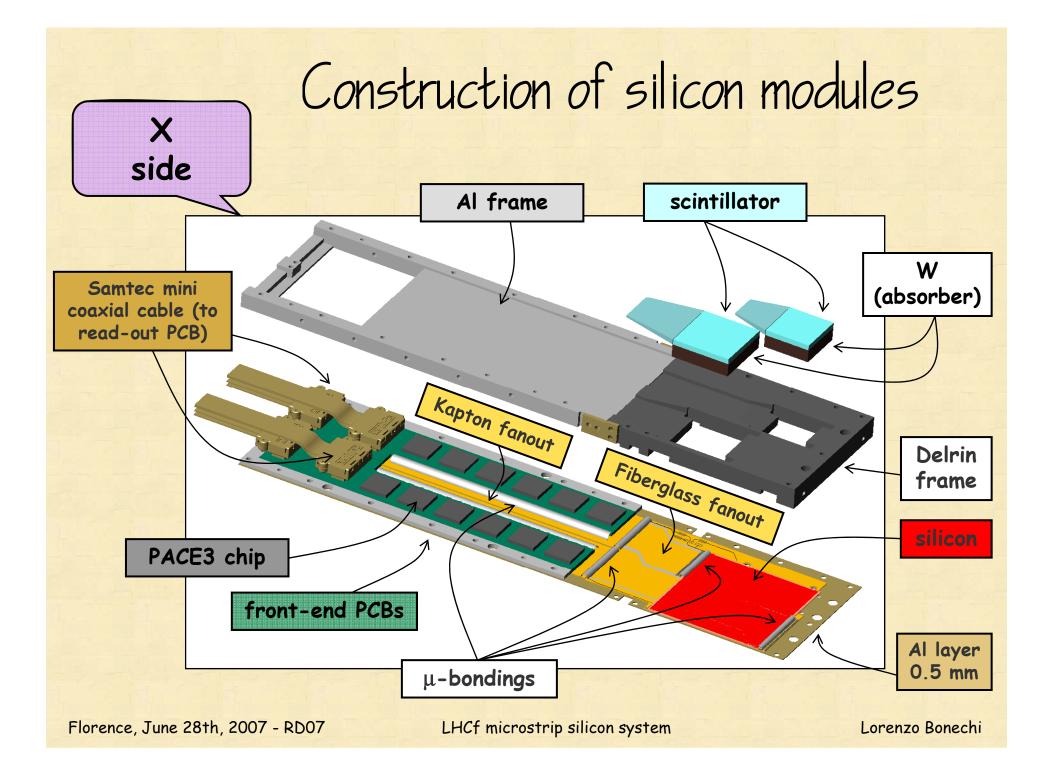


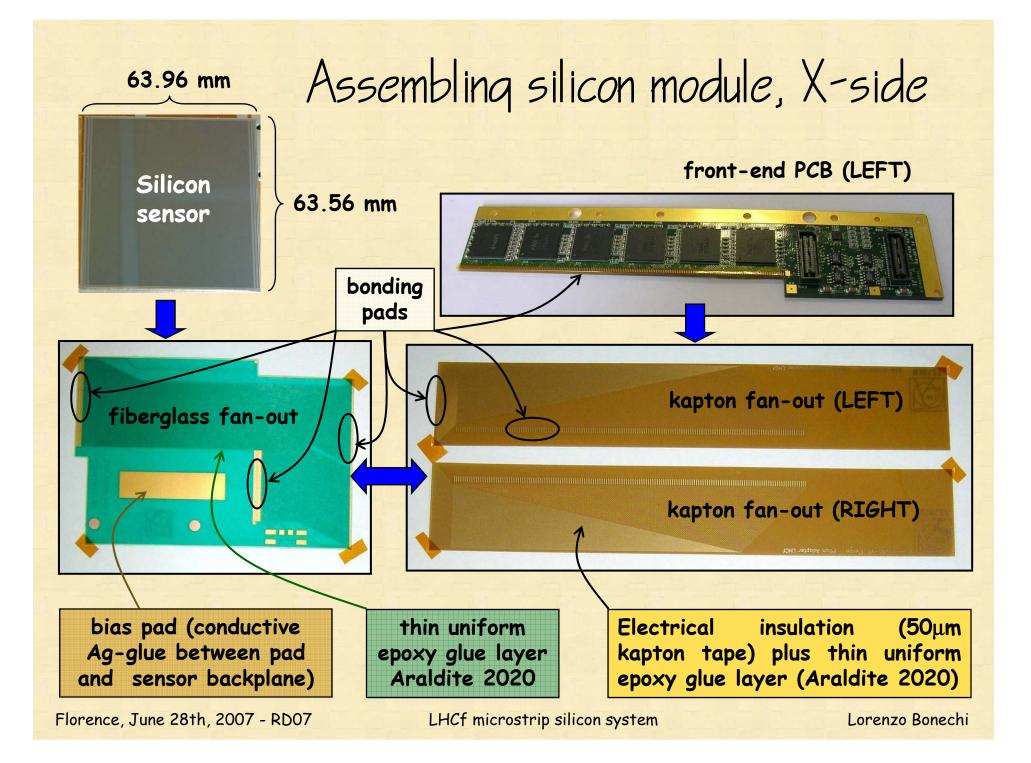
2) The LHCf detector

a) Some details about detector #2
b) Production of silicon modules
c) Test of front-end chip



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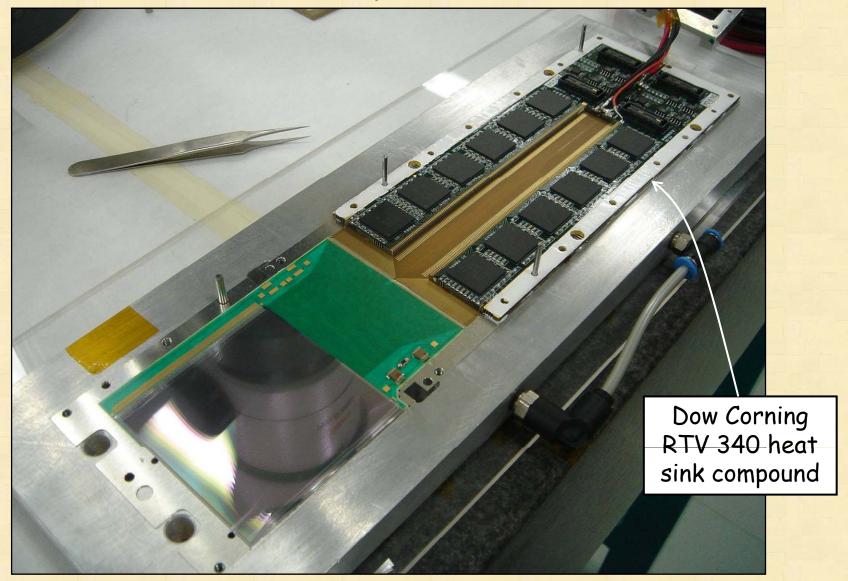




Silicon sensors details

- HAMAMATSU single side sensors developed for the barrel of the ATLAS SCT
- Size: (63.56 x 63.96) $mm^2 x 285 \mu m$
- Implantation pitch: 80µm
- 768 strips + strip 0 and strip 769 as field shaping strips
- About 75V full depletion voltage

Y-side silicon layer with front-end



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Silicon modules final production ($4 \times and 4 \times Y$)



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PACE3 front-end chip

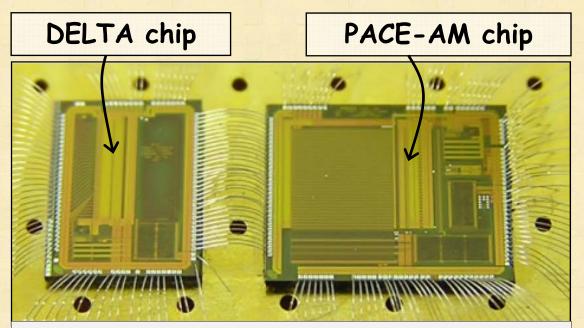
Developed for the CMS ECAL silicon preshower

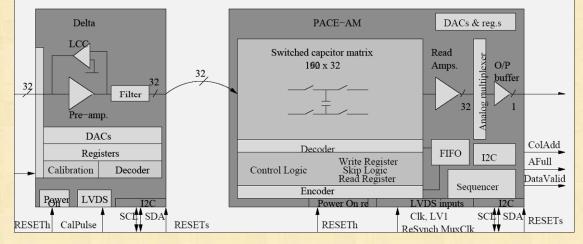
- 32 analog-in channels
- high dynamic range
- 25 ns peaking time
- · CMOS sub-micron
- 600 mW consumption

<u>DELTA chip</u>: preamp. stage, shaping, and internal calib. sys

<u>PACE-AM chip:</u> analog pipeline (matrix 192x32 capacitors), control logic and output lines

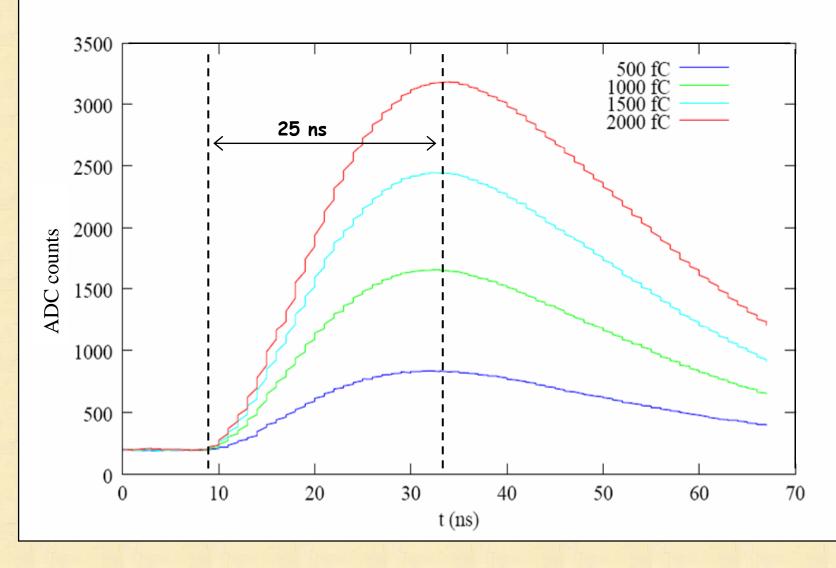
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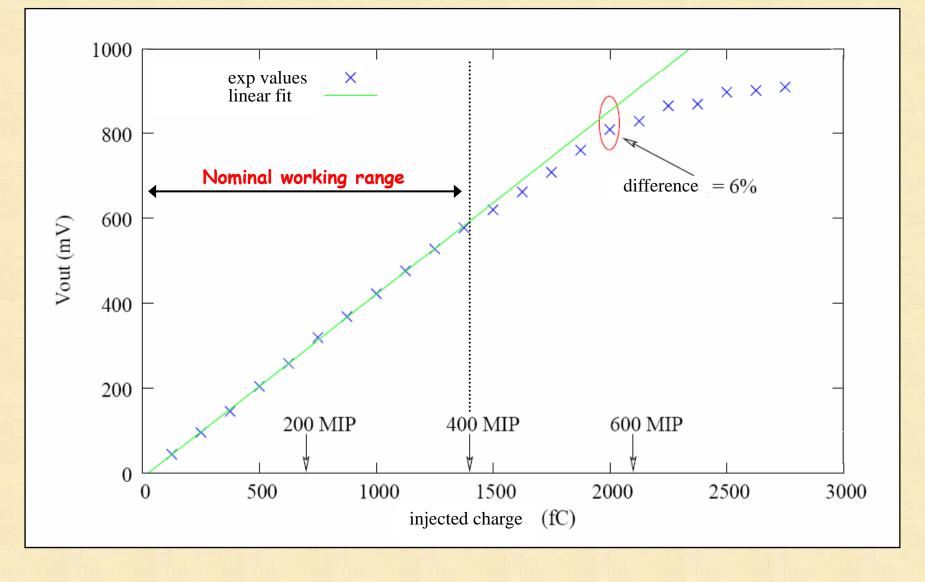
PACE3 analog output for different input charge



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Study of PACE3 output linearity



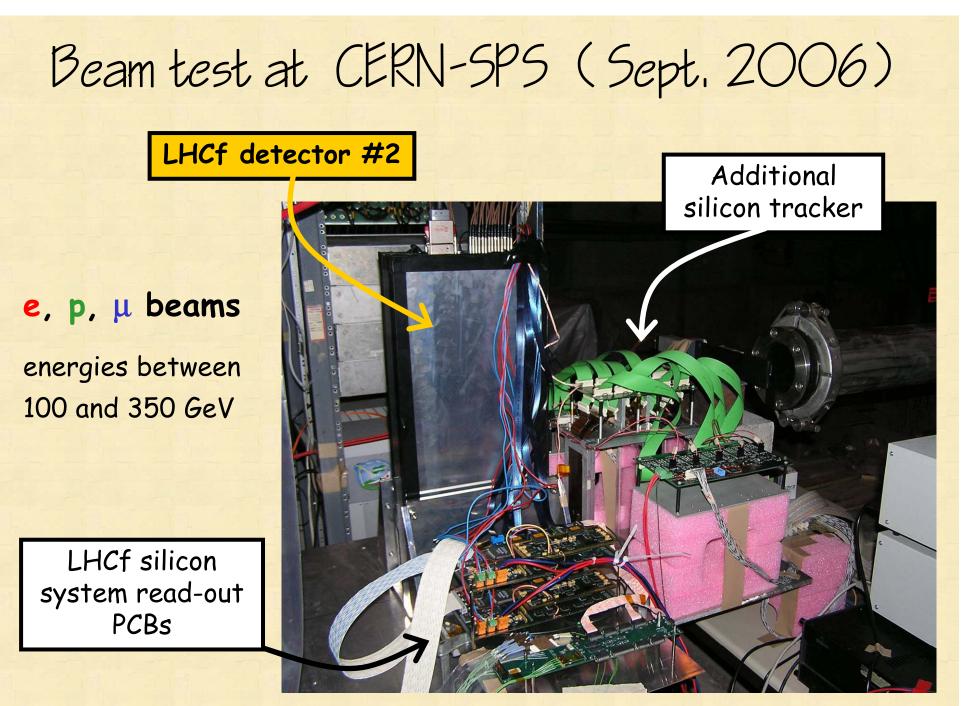
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3) Beam test (sept. 2006)

A few VERY preliminary results

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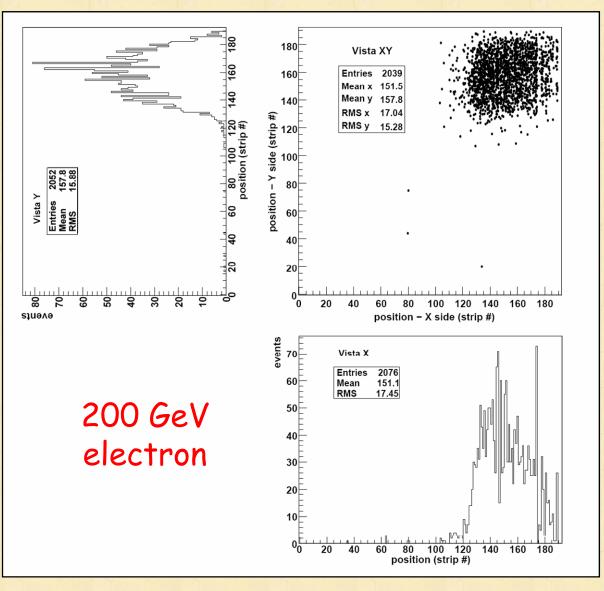


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Beam profile

Beam profile is measured by means of an additional silicon tracking system made of 5 double-sided layers with intrinsic resolutions about 3 μ m and 11 μ m along X and Y direction





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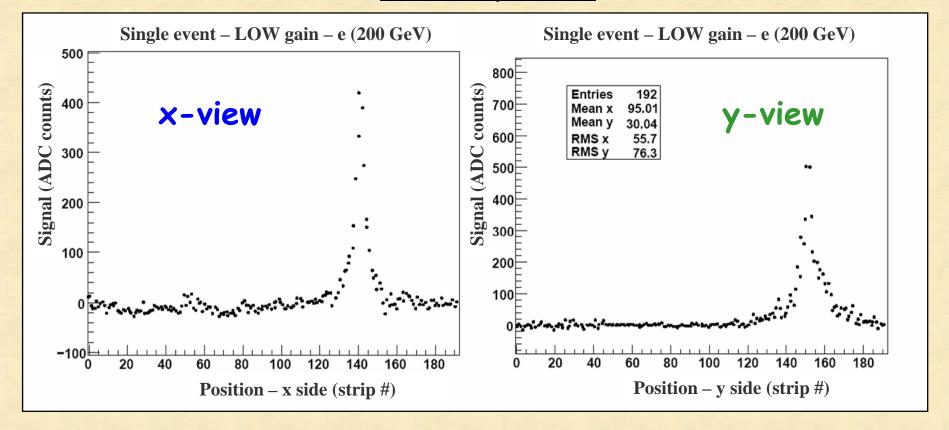
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Shower transverse profiles for a single electron event

200 GeV electron

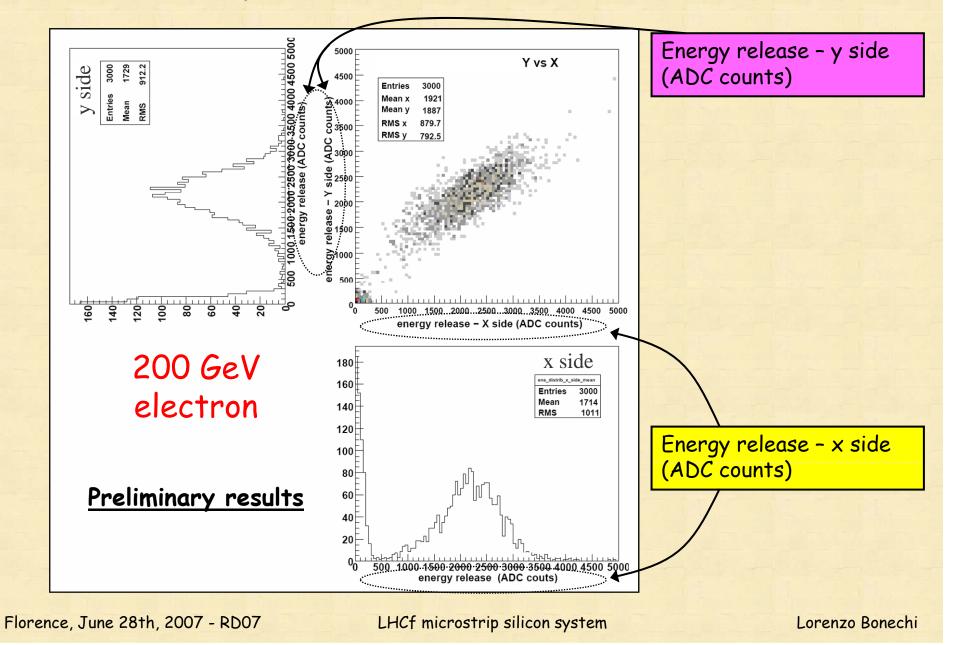
Preliminary results



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Charge distribution and correlation

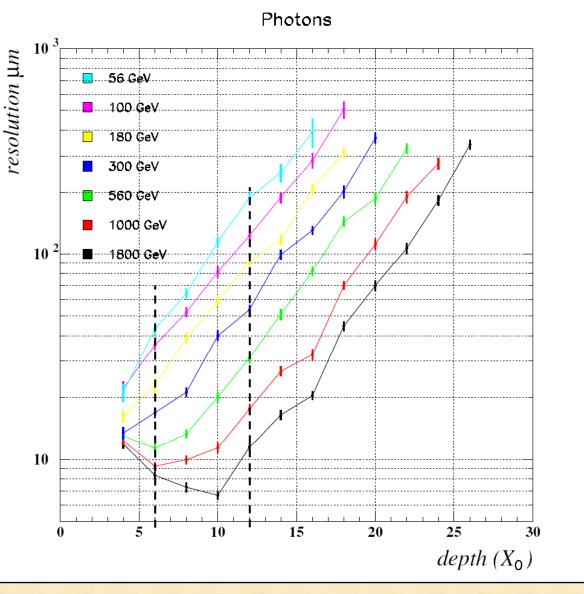


Expected spatial resolution for shower center

Simulation with the FLUKA software of silicon layers installed at different depth inside the LHCf towers.

Shower transversal shape is approximated using a generalized Lorentz function:

$$L = \frac{p_1}{\left[p_2 + (x - p_3)^2\right]^{p_4}}$$



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A few conclusions and schedule

• LHCf apparatus

- Both LHCf detectors have been completed
- Read-out electronics is under development for detector #2 silicon system

Installation

- Successful pre-installation done in 2007 for both detectors
- New pre-installation and test foreseen in autumn for detector #2 to test some modifications with respect to first pre-installation
- Final installation between end 2007 and first months of 2008
- Beam test at CERN SPS (August 24th September 11th)

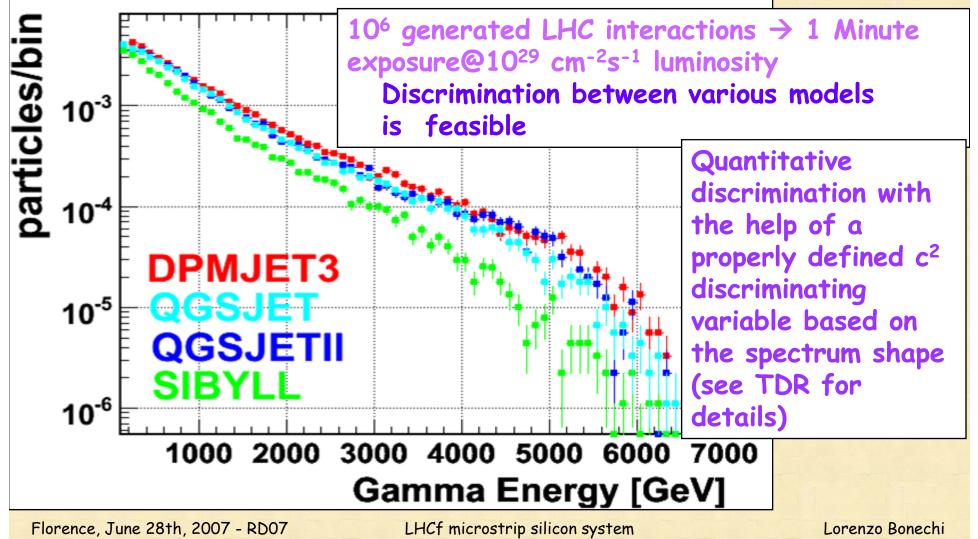
O Running

- Data taking foreseen at the beginning of LHC running at low luminosity (less than 10^{31} cm⁻²s⁻¹), in 2008

Backup slides

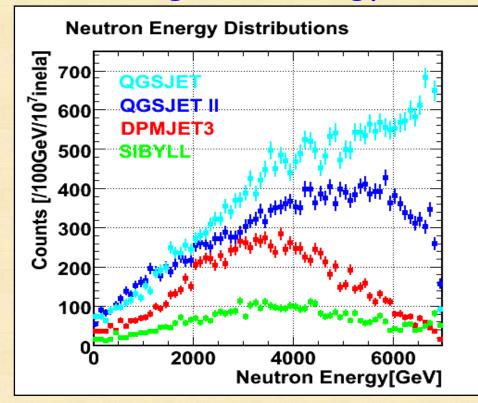
LHCf performances: Monte Carlo γ -ray energy spectrum (5% energy resolution is taken into account)

Gamma Energy Spectrum of 20mm square at Beam Center

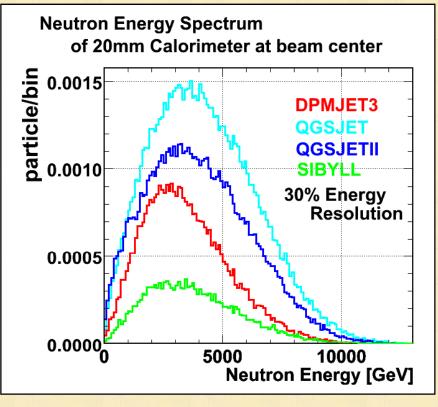


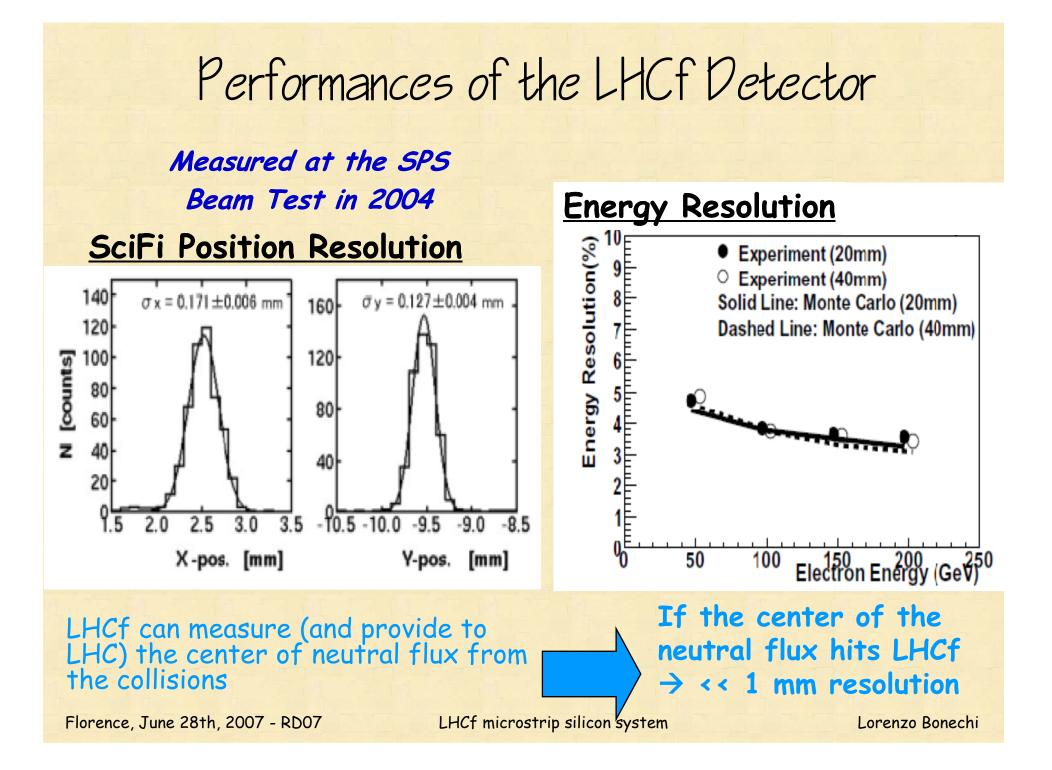
LHCf performances: model dependence of neutron energy distribution

Original n energy



30% energy resolution





Geometrical limits for LHCf measuremets

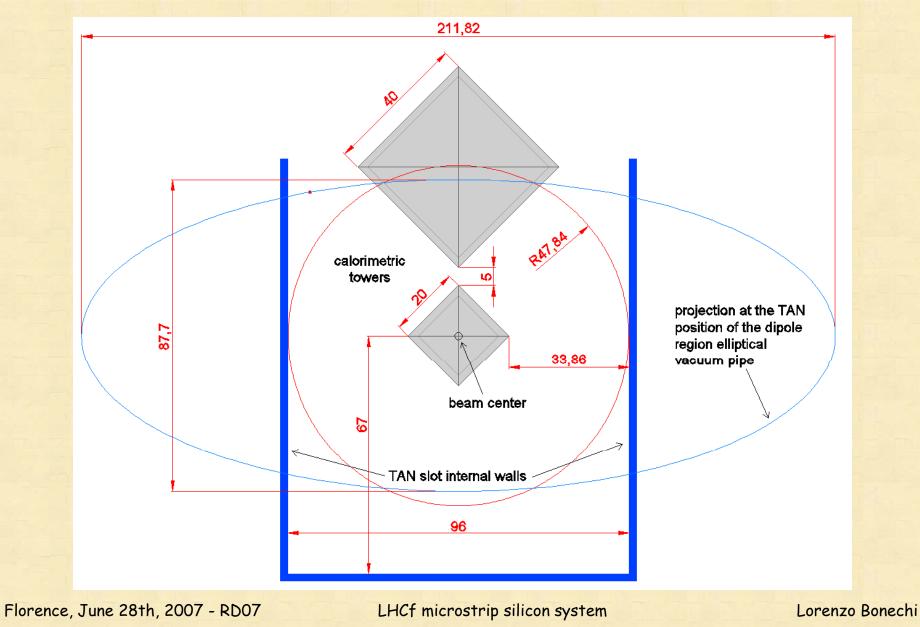
- I. Free space between the two beam pipes in the recombination chamber is 96 mm wide
- II. Critical part of beam pipe is in D1 magnet region, where it is of elliptical shape

Combination of these two limits for detector #1 and #2 is sketched in the two following slides, superimposed to the detector geometry

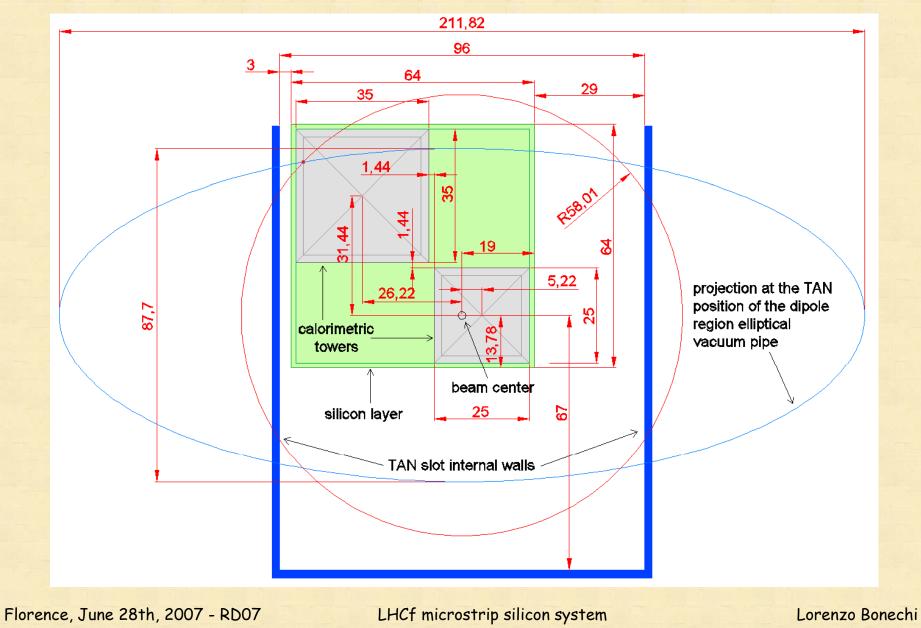


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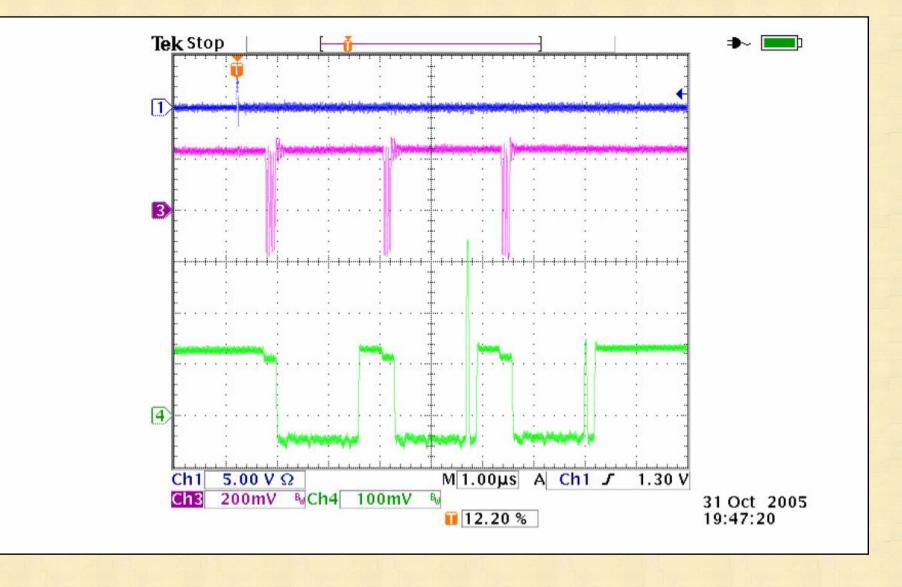
Transverse position of detector #1 in TAN slot



Transverse position of detector #2 in TAN slot

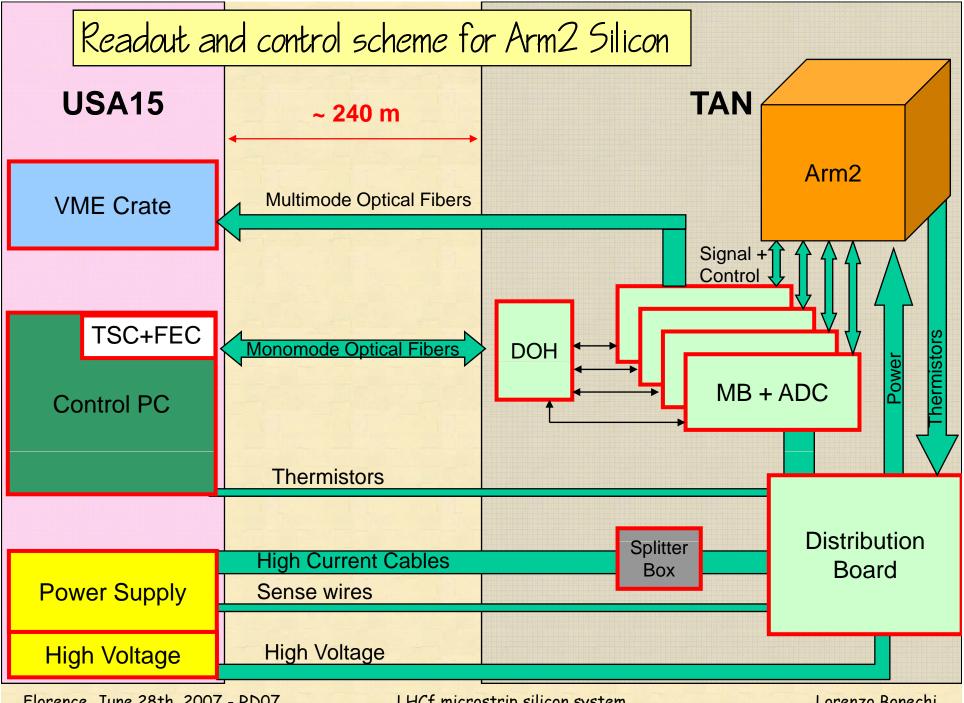


Calibration of PACE3 channels



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