

Equipment Faults Detection Sequence

Bruno PUCCIO

Workshop on Machine Protection,
focusing on Linear Accelerator complexes

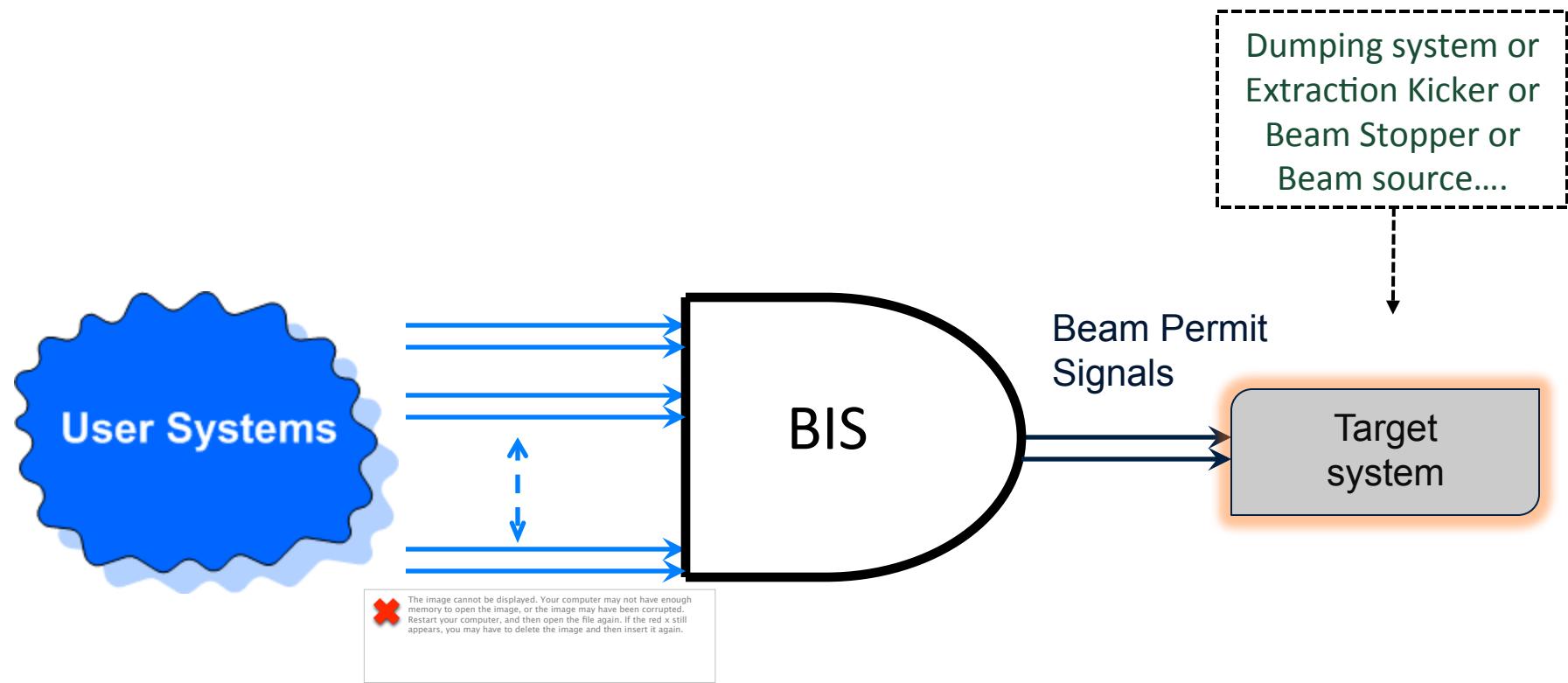
6-8 June 2012

With contributions of Benjamin Todd, Ivàn Romera & Markus Zerlauth

Outline

- ◆ Beam Interlock System overview
- ◆ BIS & Timing Sequences
- ◆ Post-Mortem system
- ◆ Summary

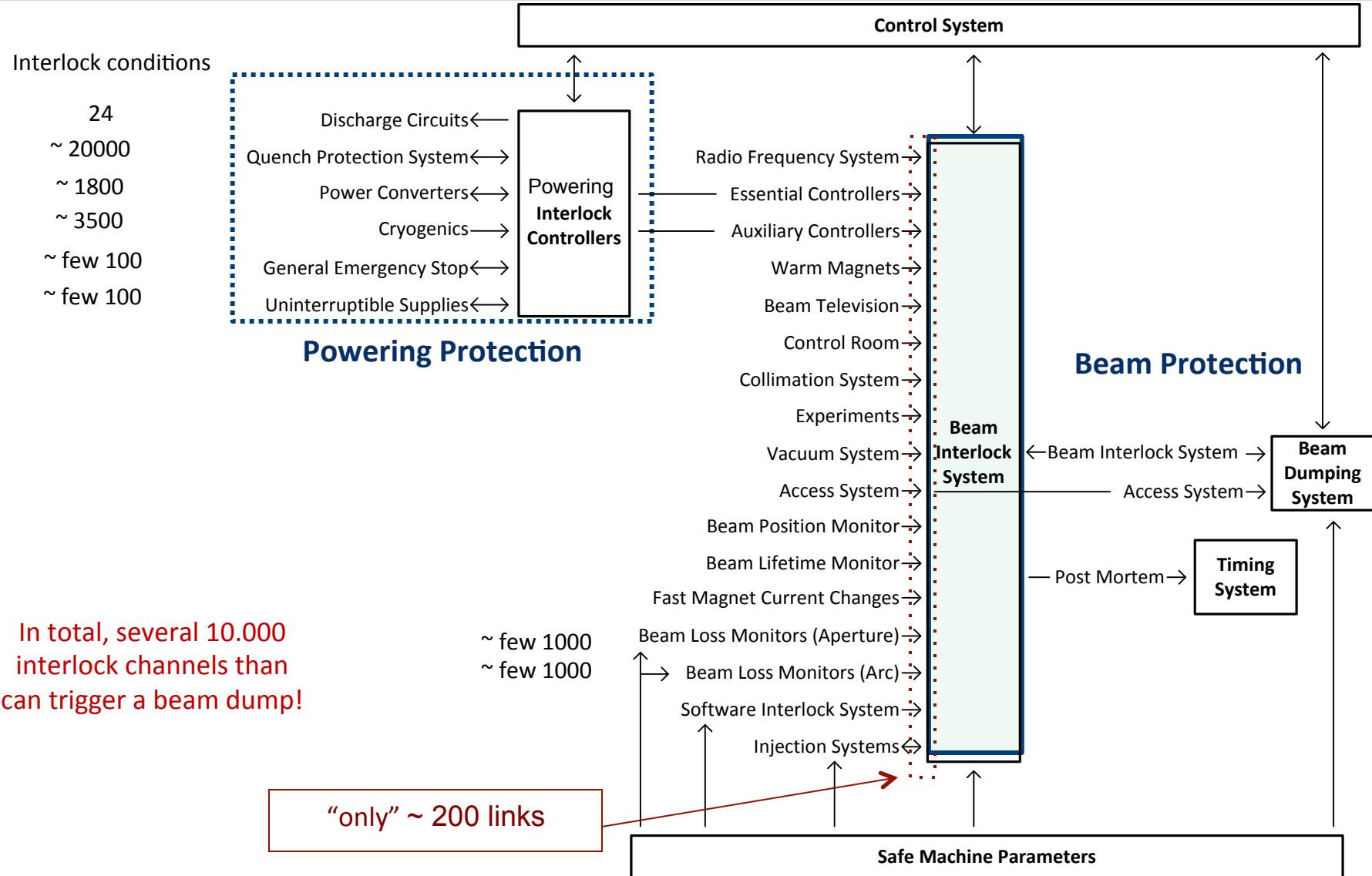
Beam Interlock System Function



$\Sigma(\text{User Permit} = \text{"TRUE"}) \Rightarrow \text{Beam Operation is allowed}$

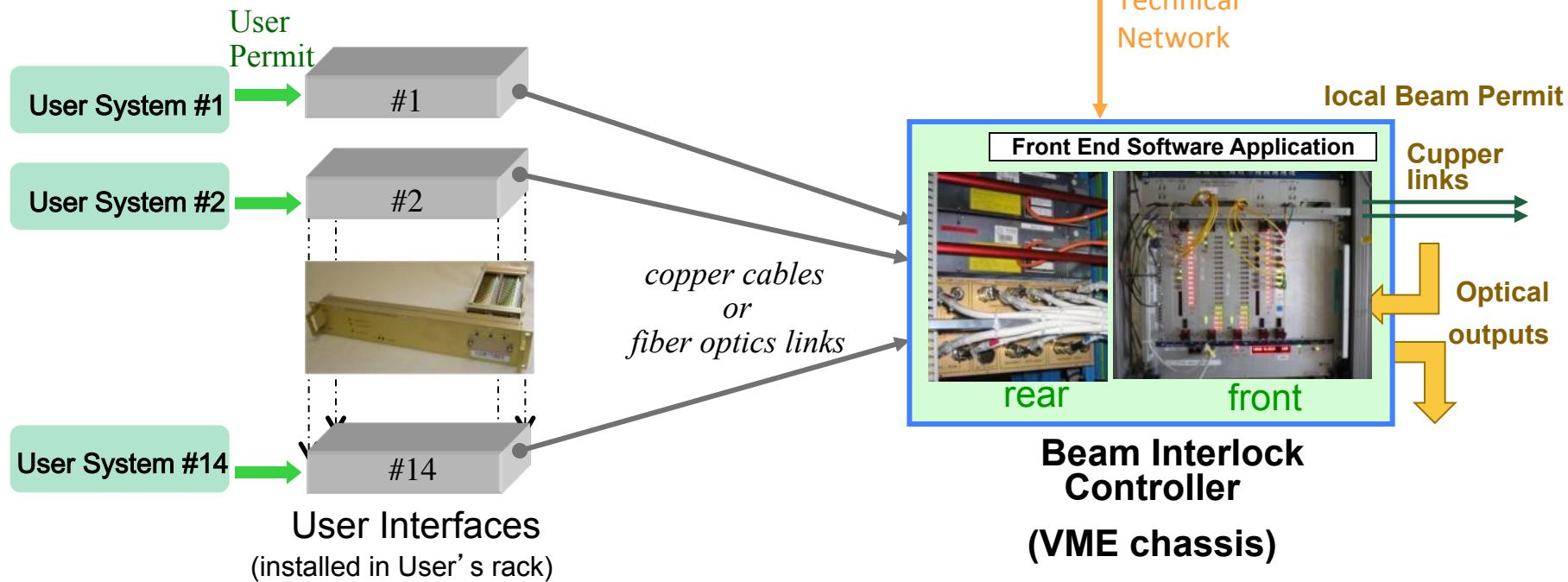
IF one User Permit = "FALSE" \Rightarrow Beam Operation is stopped

BIS = Core of LHC Machine Protection



BIS Overview

- Remote **User Interfaces** safely transmit Permit signals from connected systems to Controller
- **Controller** acts as a concentrator
- collecting **User Systems Permits**
- generating local Beam Permit
- Controllers could be **daisy chained** (Tree architecture) or could share **Beam Permit Loops** (Ring architecture)



LHC Beam Permit Loops

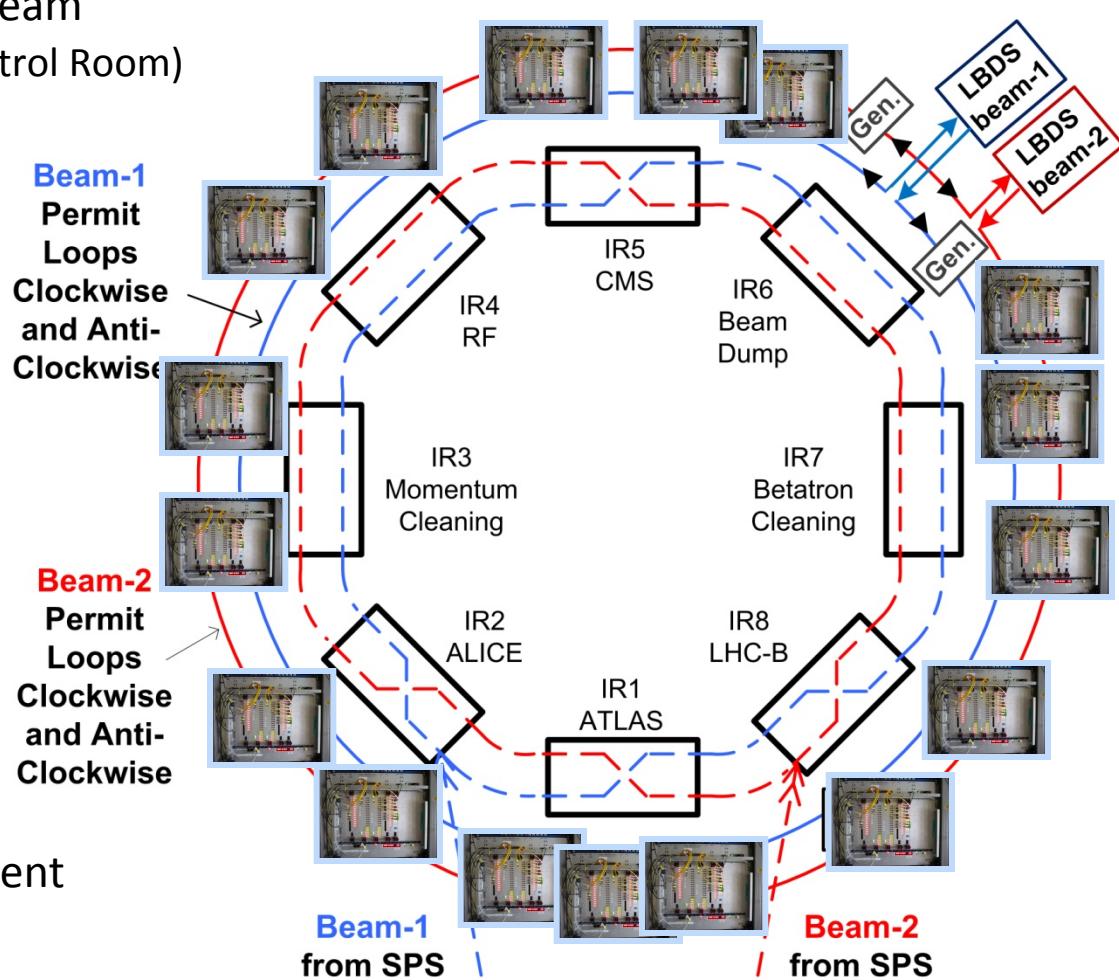
17 Beam Interlock Controllers per beam
(2 per Insertion Region (IR) + 1 near Control Room)

4 fibre-optic channels:
1 clockwise & 1 anticlockwise
for **each** beam

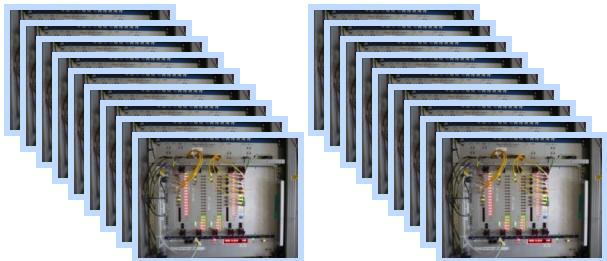
Square wave generated at IR6:
Signal can be **cut** and **monitored** by
any Controller

When any of the four signals are
absent at IR6, **BEAM DUMP!**

Beam-1 / Beam-2 loops are independent
but they can be linked (or unlinked)

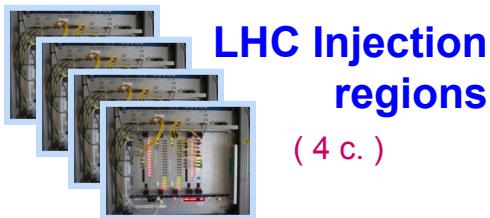


Beam Interlock Systems currently in Operation

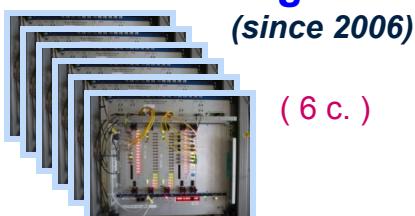


50 Controllers in total
~ 370 connected systems

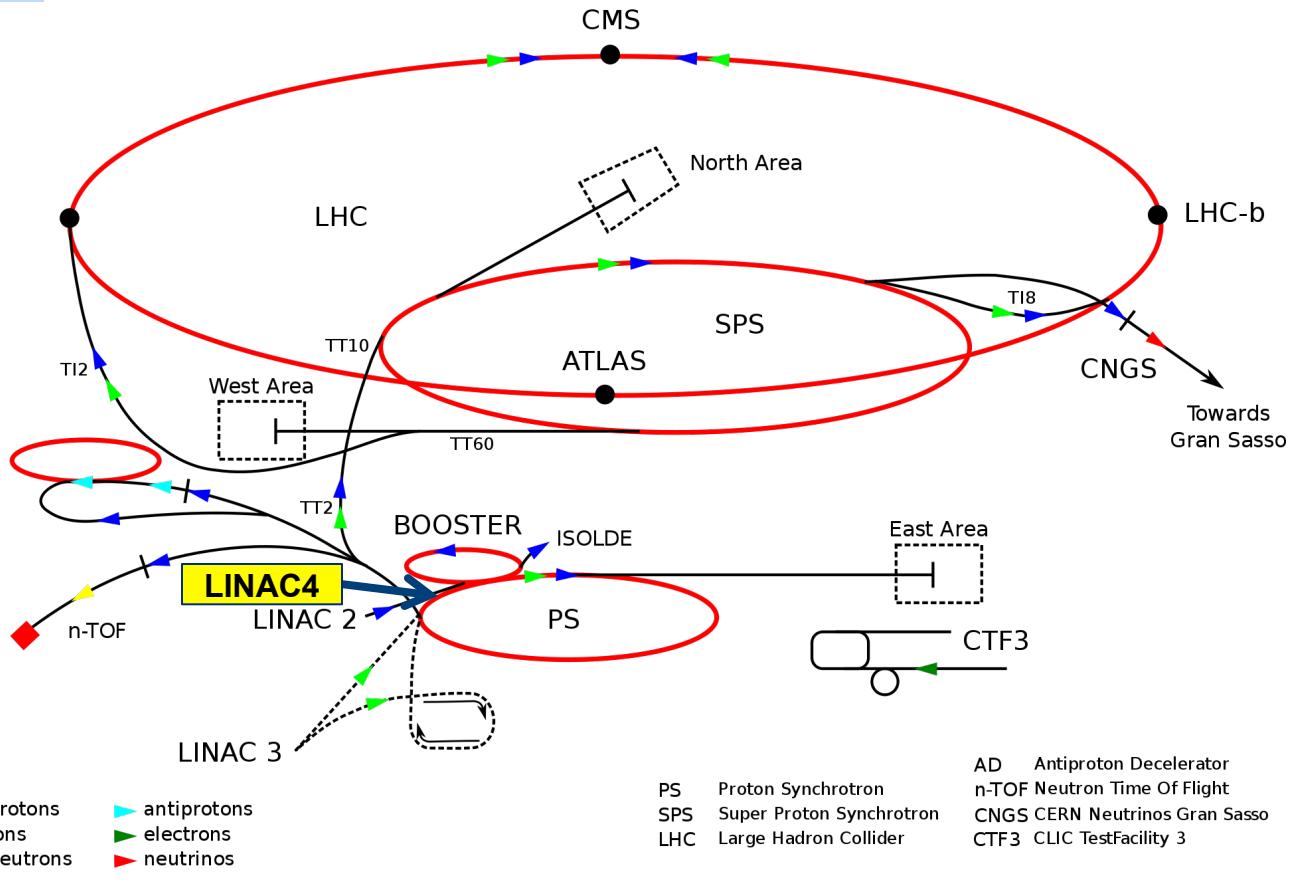
LHC ring
(2 x 17 controllers)



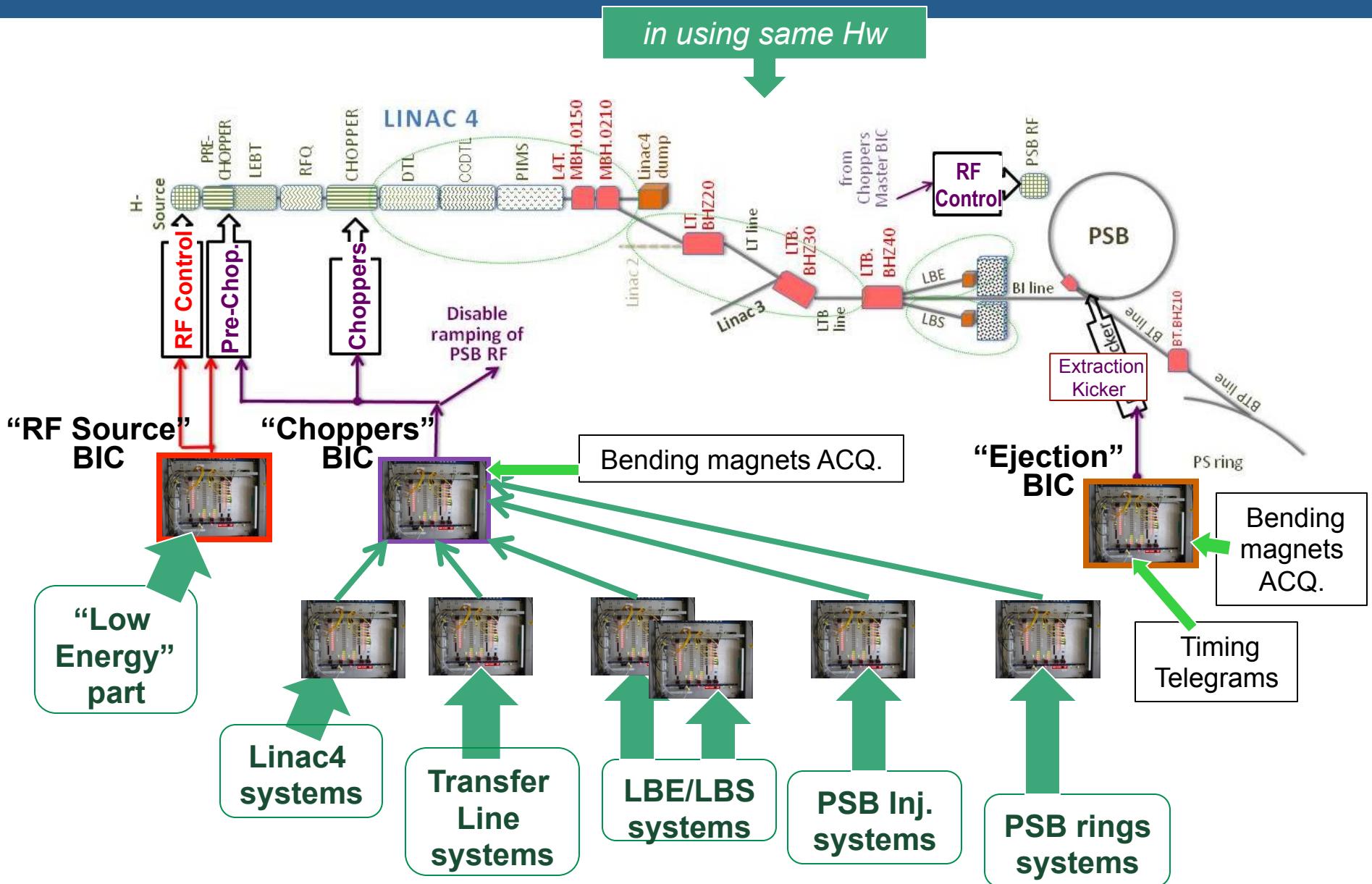
SPS to LHC Transfer lines
(14 c.)



SPS ring
(since 2006)



2013: BIS deployment to new Linac4 (and to Booster)



Fail Safe concept:

Must go to fail safe state whatever the failure

Safe: (Safety Integrity Level 3 was used as a guideline).

Must react with a probability of unsafe failure of less than 10^{-7} per hour and,
Beam abort less than 1% of missions due to internal failure
(2 to 4 failures per year).

Reliable: (whole design studied using Military and Failure Modes Handbooks)

Results from the LHC analysis are:

$$P(\text{false beam dump}) \text{ per hour} = 9.1 \times 10^{-4}$$

$$P(\text{missed beam dump}) \text{ per hour} = 3.3 \times 10^{-9}$$

Available:

Uninterruptable Powering (UPS)

Redundant Power Supply for Controller (i.e. VME crate)

Redundant Power Supply for Remote User Interface

BIS Performance

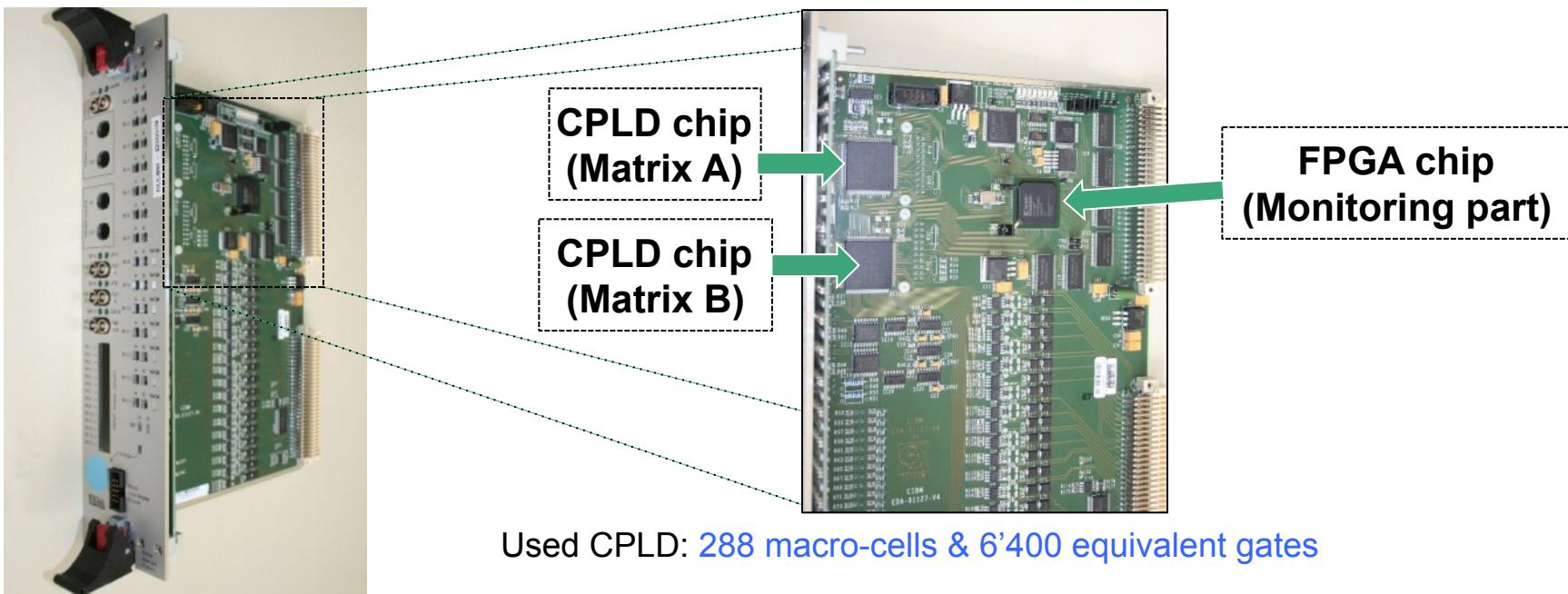
(2/3)

Critical process in Hardware:

- ◆ functionality into 2 redundant matrices
- ◆ VHDL code written by different engineers following same specification.

Critical / Non-Critical separation:

- ◆ Critical functionality always separated from non-critical.
- ◆ Monitoring elements fully independent of the two redundant safety channels.

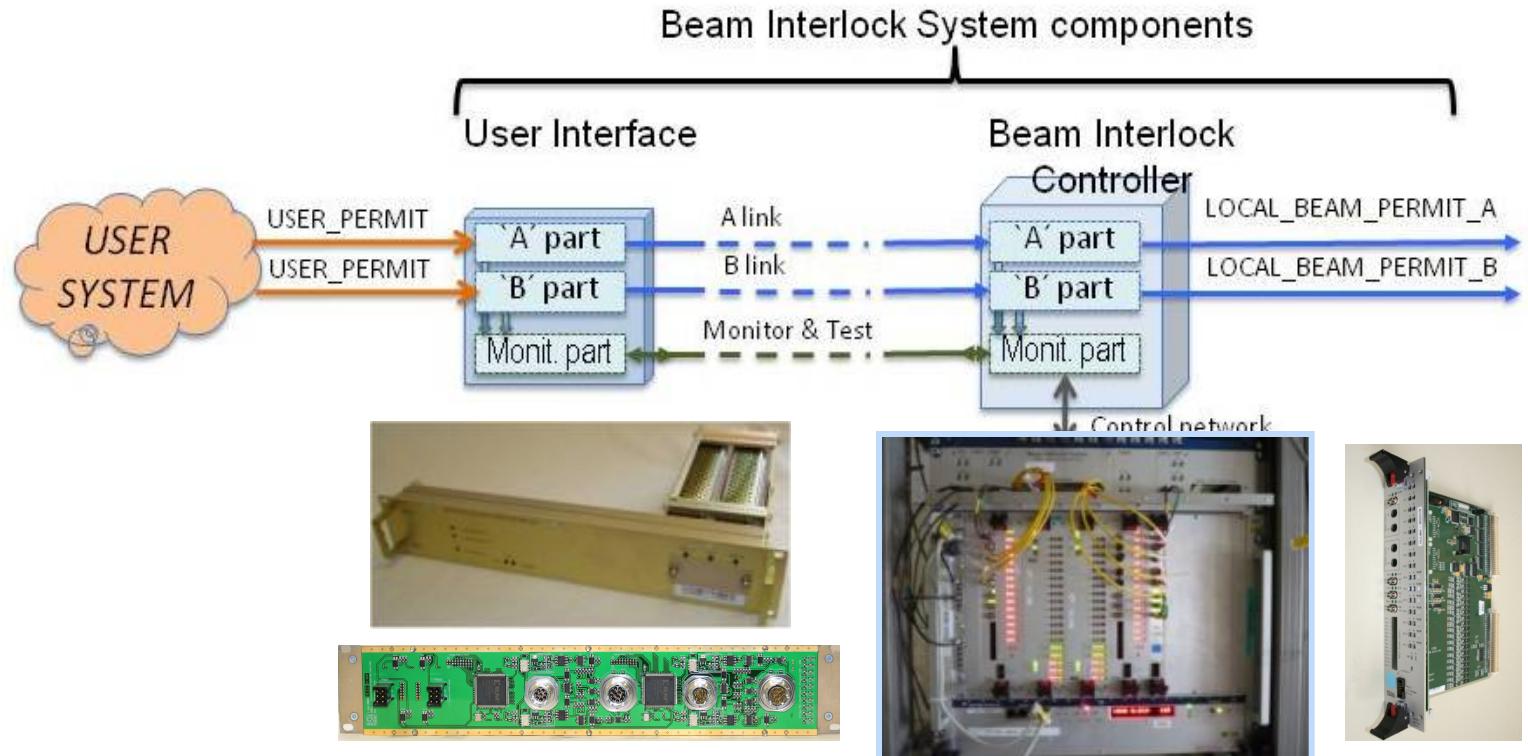


CPLD: Complex Programmable Logic Device FPGA: Field Programmable Gate Array

BIS Performance

(3/3)

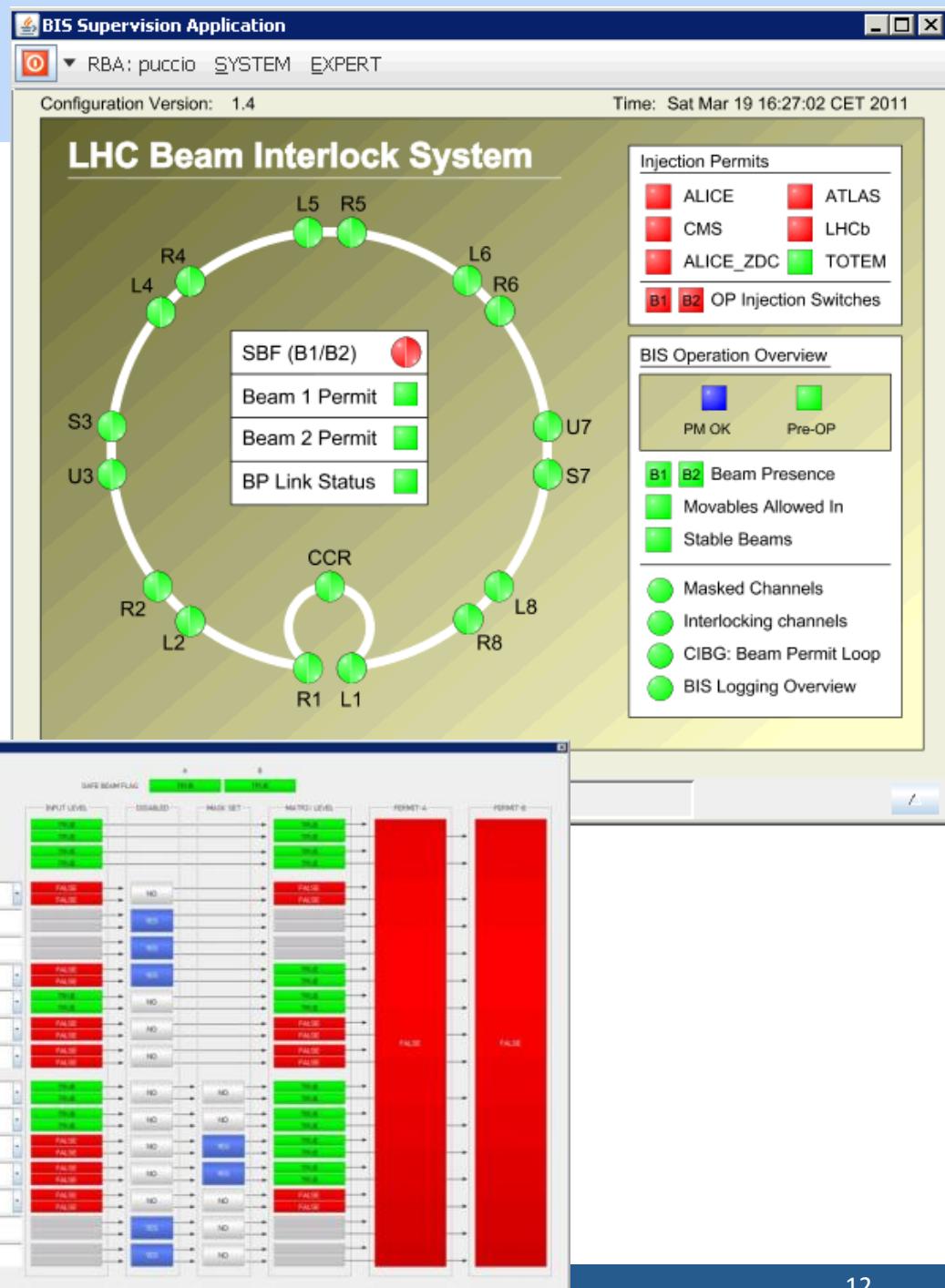
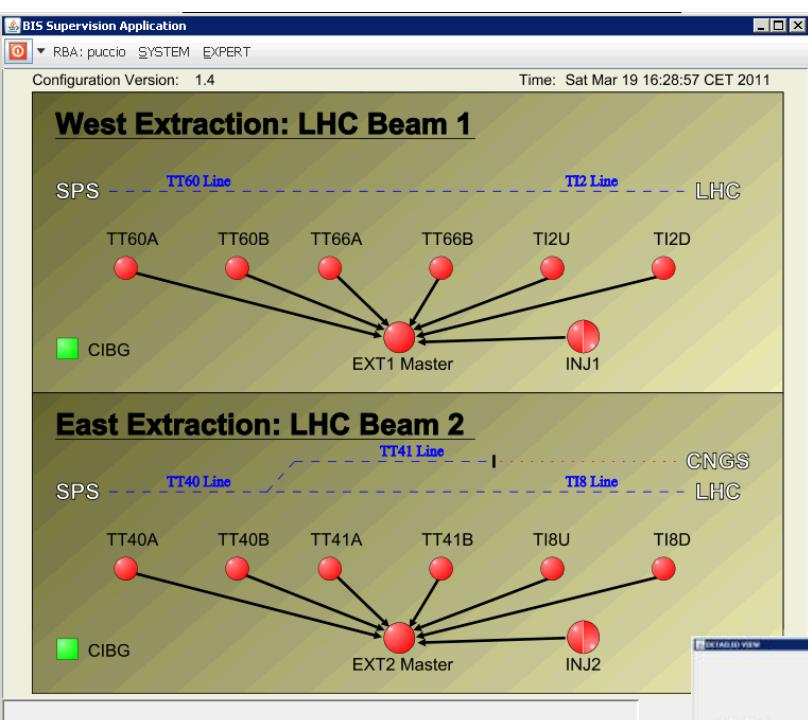
100% Online Test Coverage: Can be easily tested from end-to-end in a safe manner => recovered “good as new”



Fast: ~20μS reaction time from *User Permit* change detection to the corresponding *Local Beam Permit* change

Modular: (“Tree” or “Ring” topology) & (daisy chain of BIC to BIC possible)

Control Room GUIs



History Buffer

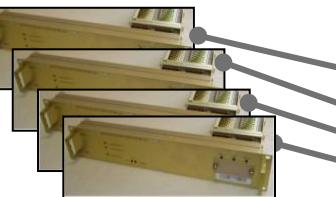
History Buffer Memory Map

FILTER: OPERATOR Logging USE SNAPSHOT

PERMIT	TIMESTAMP	DEVICE	DESCRIPTION
████	2012-06-06 16:02:06.152292	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); B T -> F
████	2012-06-06 16:02:06.152291	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); A T -> F
████	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: B T -> F
████	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: A T -> F
████	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B T -> F
████	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); A T -> F
████	2012-06-06 16:02:06.151002	CIB.BA6.TT60A	MARKER: 2 us
████	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: B F -> T
████	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: A F -> T
████	2012-06-06 16:02:06.14778	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); B F -> T
████	2012-06-06 16:02:06.14778	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); A F -> T
████	2012-06-06 16:02:06.14773	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B F -> T
████	2012-06-06 16:02:06.14773	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); A F -> T
████	2012-06-06 16:02:06.14774	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); B F -> T
████	2012-06-06 16:02:06.14774	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); A F -> T
████	2012-06-06 16:02:06.140292	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); B T -> F
████	2012-06-06 16:02:06.140291	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); A T -> F
████	2012-06-06 16:02:06.140278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B T -> F
████	2012-06-06 16:02:06.140278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); A T -> F
████	2012-06-06 16:02:06.140278	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); B T -> F
████	2012-06-06 16:02:06.140231	CIB.BA6.TT60A	LOCAL PERMIT: B T -> F
████	2012-06-06 16:02:06.140231	CIB.BA6.TT60A	LOCAL PERMIT: A T -> F
████	2012-06-06 16:02:06.140229	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); A T -> F
████	2012-06-06 16:02:06.140228	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); B T -> F
████	2012-06-06 16:02:06.135737	CIB.BA6.TT60A	LOCAL PERMIT: B F -> T
████	2012-06-06 16:02:06.135737	CIB.BA6.TT60A	LOCAL PERMIT: A F -> T
████	2012-06-06 16:02:06.135736	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B T -> F
████	2012-06-06 16:02:06.135734	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); B F -> T
████	2012-06-06 16:02:06.135734	CIB.BA6.TT60A	USER PERMIT: Ch 8(TT60 Converters currents); A F -> T
████	2012-06-06 16:02:06.135703	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); B F -> T
████	2012-06-06 16:02:06.135703	CIB.BA6.TT60A	USER PERMIT: Ch 10(MBB current); A F -> T
████	2012-06-06 16:02:05.825663	CIB.BA6.TT60A	USER PERMIT: Ch 14(FMCM_MST6177M); B F -> T
████	2012-06-06 16:02:05.825663	CIB.BA6.TT60A	USER PERMIT: Ch 14(FMCM_MST6177M); A F -> T
████	2012-06-06 16:02:05.800256	CIB.BA6.TT60A	USER PERMIT: Ch 13(FMCM_MSE6183M); B F -> T
████	2012-06-06 16:02:05.800256	CIB.BA6.TT60A	USER PERMIT: Ch 13(FMCM_MSE6183M); A F -> T
████	2012-06-06 16:02:05.238001	CIB.BA6.TT60A	SAFE BEAM FLAG: B F -> T
████	2012-06-06 16:02:05.238001	CIB.BA6.TT60A	SAFE BEAM FLAG: A F -> T
████	2012-06-06 16:02:05.238001	CIB.BA6.TT60A	SAFE BEAM FLAG: A T -> F
████	2012-06-06 16:01:47.388	CIB.BA6.TT60A	

████	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: B T -> F
████	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: A T -> F
████	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B T -> F
████	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); A T -> F
████	2012-06-06 16:02:06.151002	CIB.BA6.TT60A	MARKER: 2 us
████	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: B F -> T
████	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: A F -> T

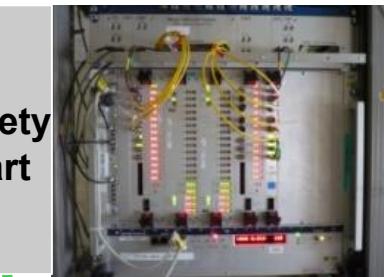
Equipment systems



User Interfaces

Safety part

Local Beam Permit

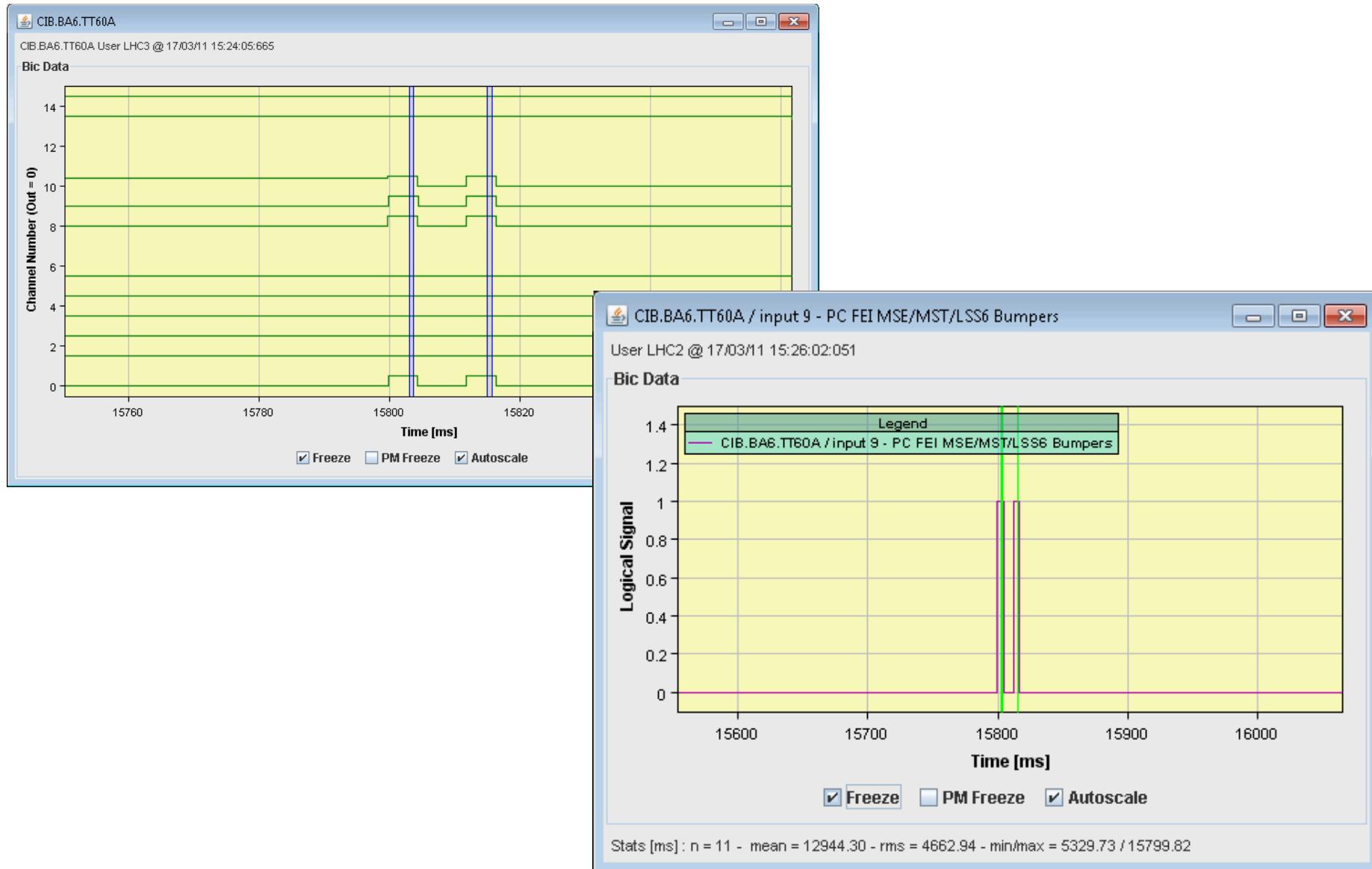


Beam Interlock Controller

Technical network

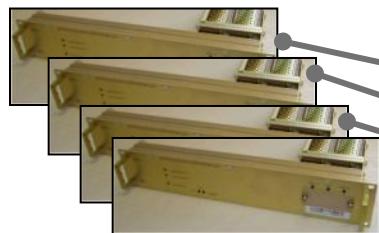
Mon. part

Timing views extracted from history buffer



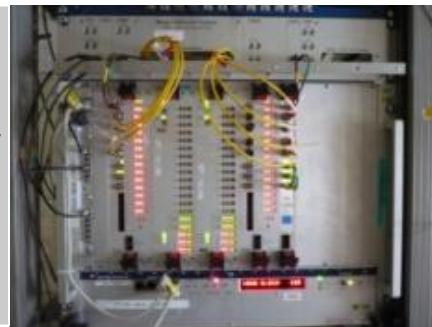
External signal(s) logged in history buffer

Equipment systems

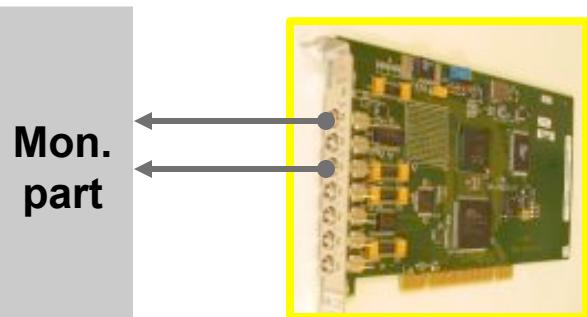


User Interfaces

Beam Interlock
Controller



Timing Receiver card



Safety
part

Mon.
part

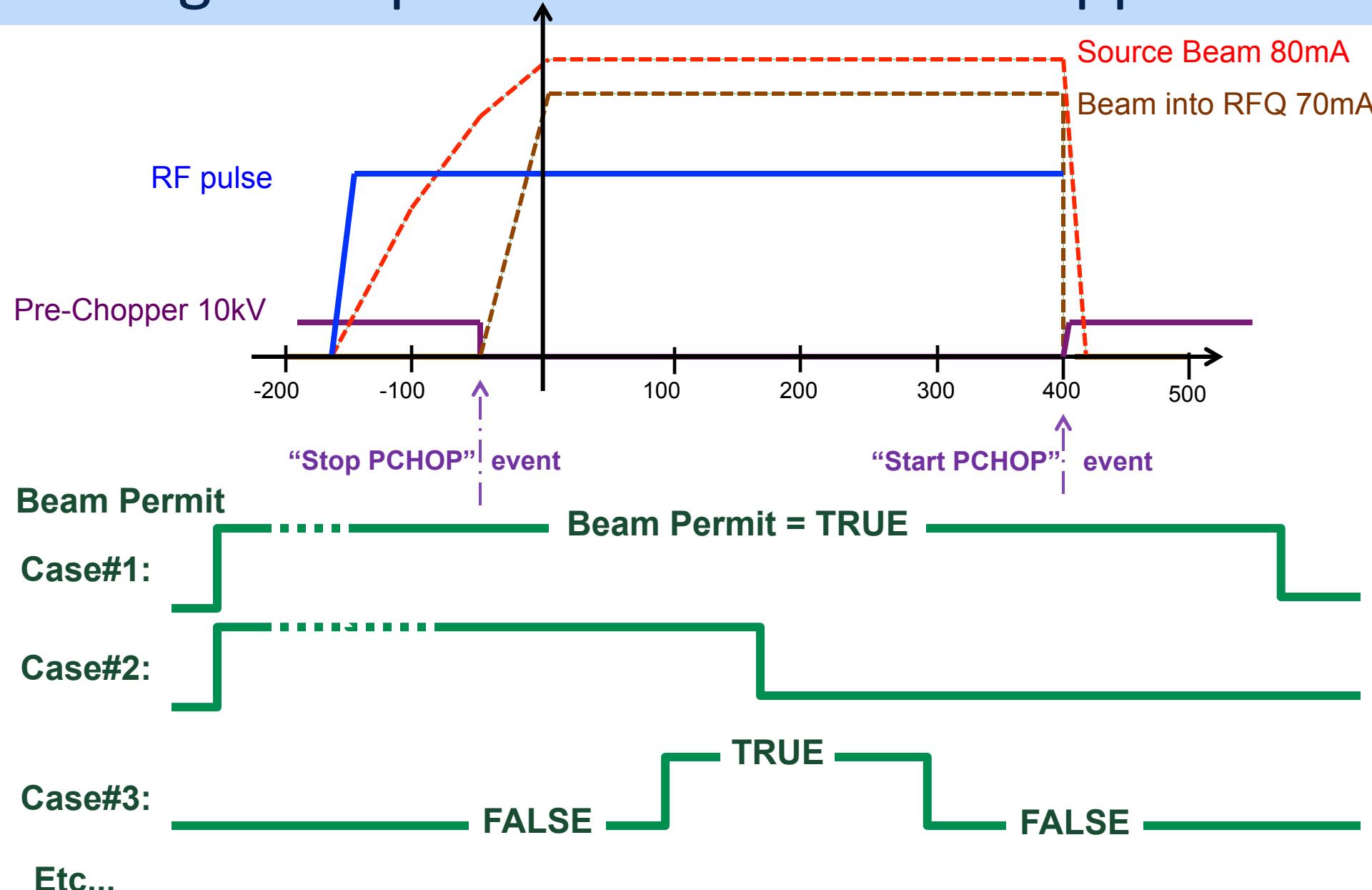
Local
Beam Permit

*External signals can be connected to
BIC Front-panel*

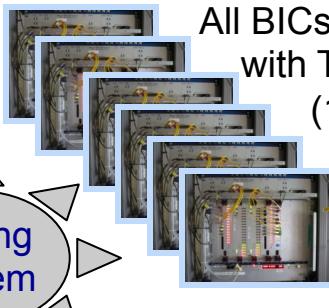
[red flag]	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: B T -> F
[red flag]	2012-06-06 16:02:06.15228	CIB.BA6.TT60A	LOCAL PERMIT: A T -> F
[green flag]	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); B T -> F
[green flag]	2012-06-06 16:02:06.152278	CIB.BA6.TT60A	USER PERMIT: Ch 9(MSE/MST currents); A T -> F
[green flag]	2012-06-06 16:02:06.151002	CIB.BA6.TT60A	MARKER: SPS Extraction event
[green flag]	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: B F -> T
[green flag]	2012-06-06 16:02:06.147781	CIB.BA6.TT60A	LOCAL PERMIT: A F -> T

extracted from the SPS Extraction BIC's history buffer

Timing example with Linac4 Pre-chopper



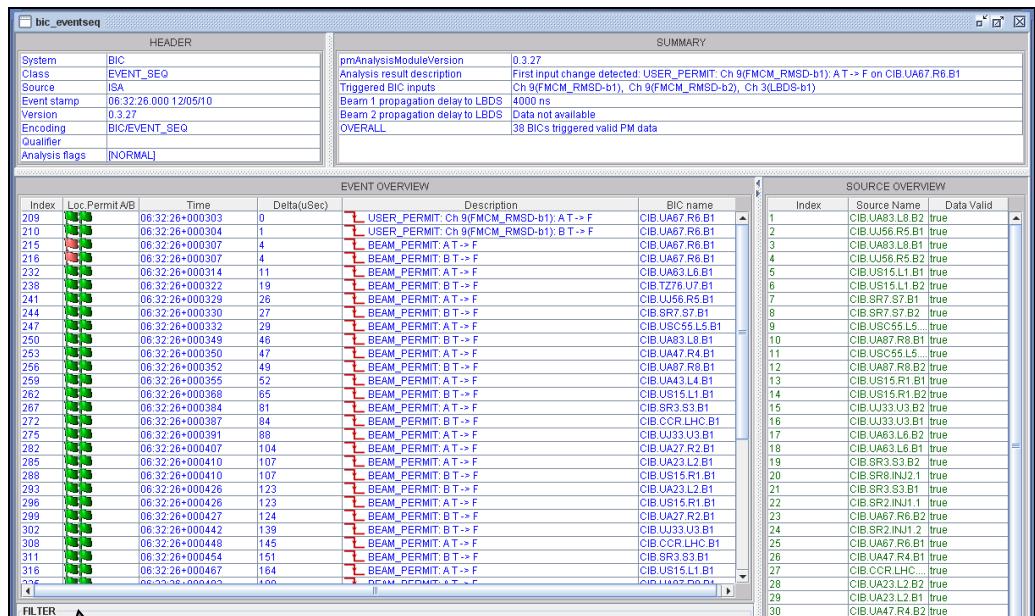
BIS = key element of Post-Mortem analysis



All BICs synchronised with Timing System (1µS accuracy)

History Buffer				Memory Map
History Buffer				Memory Map
History Buffer				Memory Map
FILTER...				
PERMIT	TIMESTAMP	DESCRIPTION	
.....	2009-06-11 11:23:32.196217	USER PERMIT: Ch 11(FMCM_MSE4183M): B T -> F	
.....	2009-06-11 11:23:32.196216	USER PERMIT: Ch 11(FMCM_MSE4183M): A T -> F	
.....	2009-06-11 11:23:32.189659	USER PERMIT: Ch 12(FMCM_RBH4.400107): B T -> F	
.....	2009-06-11 11:23:32.189658	USER PERMIT: Ch 12(FMCM_RBH4.400107): A T -> F	
.....	2009-06-11 11:23:32.185771	USER PERMIT: Ch 13(FMCM_RBH4.400309): B T -> F	
.....	2009-06-11 11:23:32.185769	USER PERMIT: Ch 13(FMCM_RBH4.400309): A T -> F	
.....	2009-06-11 11:23:32.142188	USER PERMIT: Ch 10(Bumpers currents): A T -> F	
.....	2009-06-11 11:23:32.142188	USER PERMIT: Ch 10(Bumpers currents): B T -> F	
.....	2009-06-11 11:23:32.142186	USER PERMIT: Ch 9(MSE septum current): B T -> F	
.....	2009-06-11 11:23:32.142185	USER PERMIT: Ch 9(MSE septum current): A T -> F	
.....	2009-06-11 11:23:32.141884	LOCAL PERMIT: B T -> F	
.....	2009-06-11 11:23:32.141882	LOCAL PERMIT: Ch 8(TT40 converters currents): B T -> F	
.....	2009-06-11 11:23:32.141879	LOCAL PERMIT: A T -> F	
.....	2009-06-11 11:23:32.141877	USER PERMIT: Ch 8(TT40 converters currents): A T -> F	
.....	2009-06-11 11:23:32.141002	MARKER: 2 us	
.....	2009-06-11 11:23:32.141002	TIME: Event Received	
.....	2009-06-11 11:23:32.137668	LOCAL PERMIT: B F -> T	
.....	2009-06-11 11:23:32.137668	USER PERMIT: Ch 10(Bumpers currents): B F -> T	
.....	2009-06-11 11:23:32.137627	USER PERMIT: Ch 9(MSE septum current): B F -> T	
.....	2009-06-11 11:23:32.137627	USER PERMIT: Ch 9(MSE septum current): A F -> T	
.....	2009-06-11 11:23:32.137209	USER PERMIT: Ch 8(TT40 converters currents): B F -> T	
.....	2009-06-11 11:23:32.137209	USER PERMIT: Ch 8(TT40 converters currents): A F -> T	
.....	2009-06-11 11:23:32.130289	USER PERMIT: Ch 10(Bumpers currents): A T -> F	
.....	2009-06-11 11:23:32.130288	USER PERMIT: Ch 10(Bumpers currents): B T -> F	
.....	2009-06-11 11:23:32.130185	USER PERMIT: Ch 9(MSE septum current): B T -> F	
.....	2009-06-11 11:23:32.130185	USER PERMIT: Ch 9(MSE septum current): A T -> F	

History Buffers

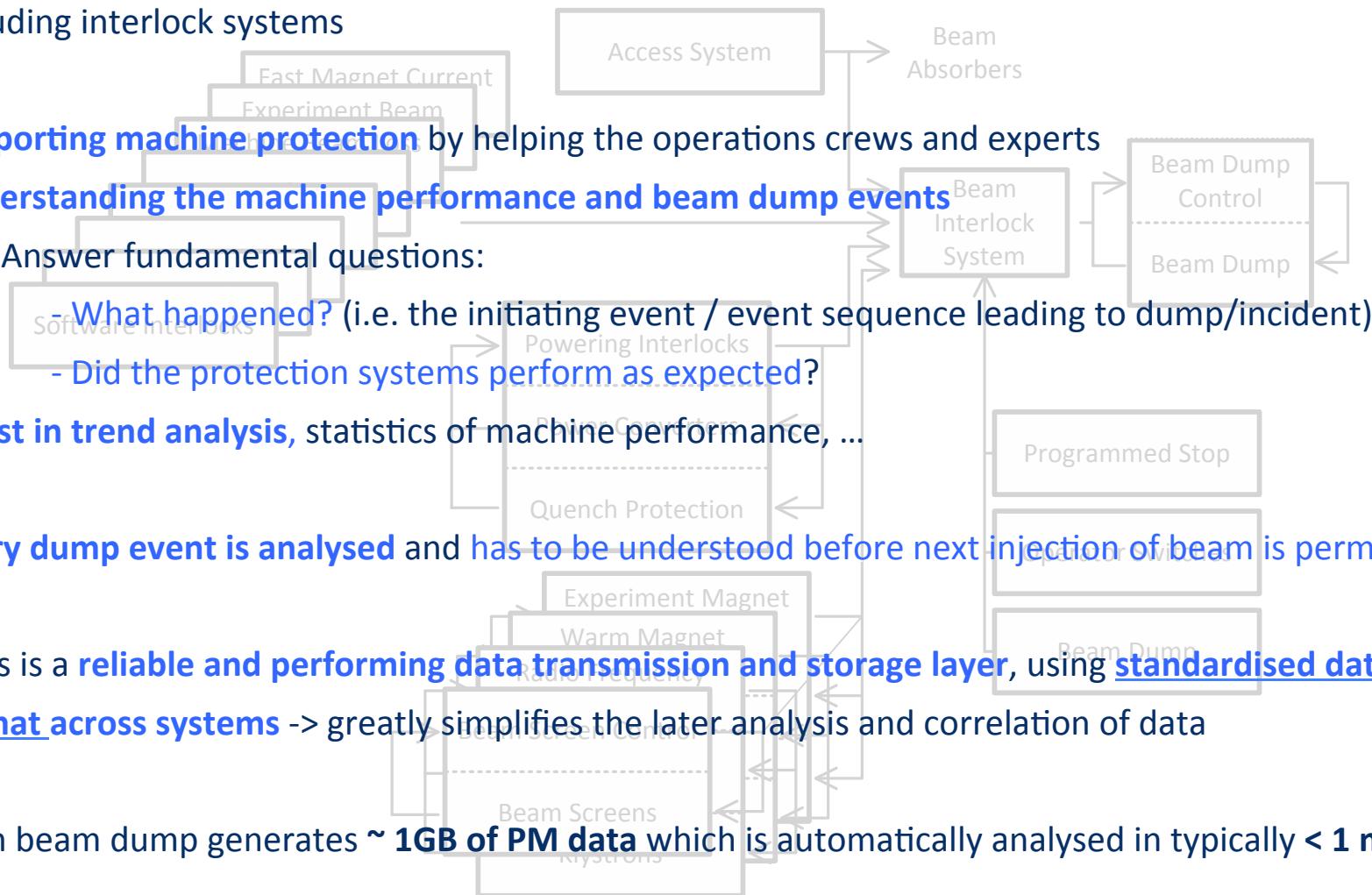


Thanks to different gathered HBs:
 => Identify of source of beam dump
 => Reconstruct sequence of events that has led to the beam dump

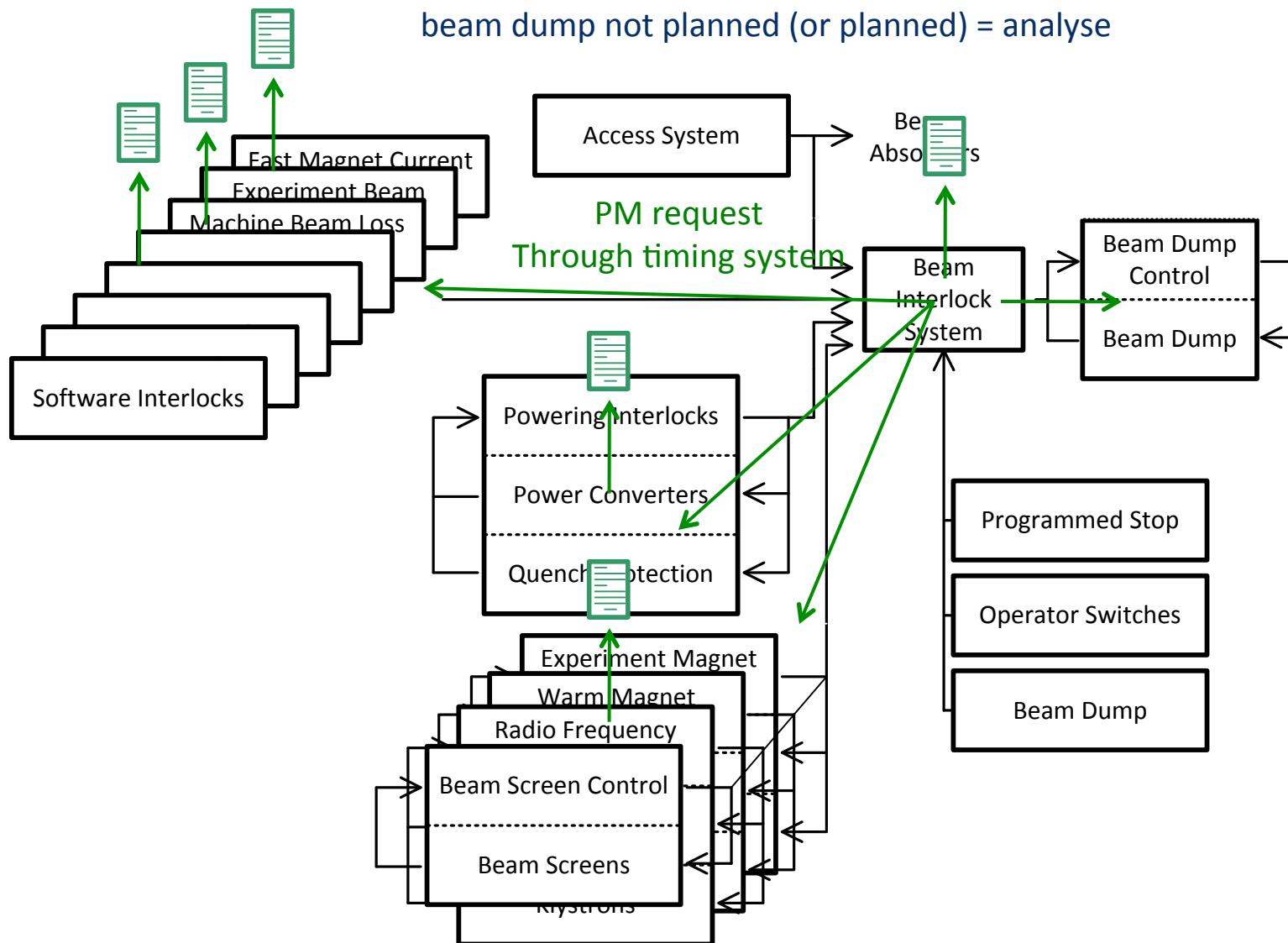
has led to the beam dump

Post-Mortem system

- **Automated post-operational analysis** of transient data recordings from LHC equipment systems, including interlock systems
- **Supporting machine protection** by helping the operations crews and experts
- **Understanding the machine performance and beam dump events**
- and Answer fundamental questions:
 - What happened? (i.e. the initiating event / event sequence leading to dump/incident)
 - Did the protection systems perform as expected?
- **Assist in trend analysis**, statistics of machine performance, ...
- **Every dump event is analysed** and has to be understood before next injection of beam is permitted
- Basis is a **reliable and performing data transmission and storage layer**, using standardised data format across systems -> greatly simplifies the later analysis and correlation of data
- Each beam dump generates ~ **1GB of PM data** which is automatically analysed in typically < 1 min



Post-Mortem layout





PM Server Architecture

GLOBAL : GPM1 : 18.06.2010 06:33:18 (1276835598311142900)

Final analysis is finished

Session confirmation Modules graph Results

Dump context

Event timestamp: 2010.06.18 06:33:18 CEST
Acc mode: BEAM SETUP
Beam mode: INJECTION PROBE BEAM
Energy: 450120 [MeV]
Intensity B1: 0 [e¹⁰ charges]
Intensity B2: 0 [e¹⁰ charges]
SMP B1 / SMP B2: PRESENT, SAFE / SAFE

Event sequence

Event Category: PROGRAMMED_DUMP
Event Classification: SINGLE_SYSTEM_DUMP
Event Sequence: First input change detected: USER_PERMIT: Ch 1(Programable Dump b1): A T -> F on CIB.CCR.LHC.B1
Triggered BIC inputs: Ch 1(Programable Dump b1), Ch 3(LBDS-b1), Ch 3(LBDS-b2)
SCEvents: No power converter events found

Machine protection features

Event Description: Machine Protection features OK, safe for next injection
Highest Beam Losses:
Magnet Quenches: No magnet quenches found
nQPS Triggers: No nQPS events found

BIC IPOC: ✓ FMCN ISA: ✓ PIC IPOC: ✓
XPOC B1: ✓ XPOC B2: ✘
Safe for injection ?: ✓ PM Overall: ✓

Comments

User:
Input your comment for session confirmation:

Confirm Discard Release SIS

Console

06:42:19 - IOC_EXT_ISA FINISHED
06:42:19 - New results have been received from the module FGC_EXT_ISA
06:42:19 - Final analysis is finished
06:42:19 - FFC_EVT_ISA data ready
06:42:19 - New analysis session progress: Final analysis is finished

Running tasks

06:56:14 - Ignoring IOC PM event: [IOC] 1276836971480238525
06:57:47 - Ignoring IOC PM event: [IOC] 1276837065080238525
07:53:10 - Ignoring IOC PM event: [IOC] 1276840387880238525

11:54:51 - FFT windowing OFF

PM buffers



LHC
Logging

Analysis of global events...



An example of event sequence

Event Timestamp: 12-JUN-11 07.17.00.656290 AM

Beam Energy: 3500040

Mps Expert Comment : Quench of RD2.L1 magnet

(+ due to suspected imbalance as well RQ4 some 17 sec. after the dump...)

Dump clean.

BIC/EVENT_SEQ >> Version: 0.4.10 Responsible: Ivan Romera Romirez

HEADER			
System	BIC		
Class	EVENT_SEQ		
Source	ISA		
Event stamp	07:17:00.654 12/06/11		
Version	0.4.10		
Encoding	BIC/EVENT_SEQ		
Qualifier	Analysis flags [NORMAL]		
		SUMMARY	
pmAnalysisModuleVersion	0.4.10		
Analysis result description	First USR_PERMIT change: Ch 12-PIC_MSK: AT -> F on CIB.US15.L1.B1		
Triggered BIC inputs	Ch 12-PIC_MSK(L1.B1), Ch 5-PIC_UNM(L1.B1), Ch 12-PIC_MSK(L1.B2), Ch 5-PIC_UNM(L1.B2), Ch ...		
Beam 1 propagation delay to LBDS	61000 ns		
Beam 2 propagation delay to LBDS	64000 ns		
OVERALL	38 BICs triggered valid PM data		

EVENT OVERVIEW			
Index	Loc_Permit A/B	Time	Delta(uSec)
154		07:17:00+656215	0
155		07:17:00+656215	0
156		07:17:00+656215	0
157		07:17:00+656215	0
158		07:17:00+656215	0
159		07:17:00+656215	0
160		07:17:00+656215	0
162		07:17:00+656216	1
230		07:17:00+656279	64
231		07:17:00+656279	64
236		07:17:00+656281	66
238		07:17:00+656281	66
248		07:17:00+656281	66
257		07:17:00+656282	67
267		07:17:00+656284	69
271		07:17:00+656284	69
336		07:17:00+656331	116
337		07:17:00+656331	116
346		07:17:00+656332	117
347		07:17:00+656332	117
395		07:17:00+656343	128
396		07:17:00+656343	128
400		07:17:00+656344	129
401		07:17:00+656344	129
647		07:17:00+656675	460
649		07:17:00+656678	463
661		07:17:00+656687	472
665		07:17:00+656691	476

SOURCE OVERVIEW		
Index	Source Name	Data Valid
1	CIB.UA83.L8.B2	true
2	CIB.UJ56.R5.B1	true
3	CIB.UA83.L8.B1	true
4	CIB.UJ56.R5.B2	true
5	CIB.US15.L1.B1	true
6	CIB.US15.L1.B2	true
7	CIB.SR7.57.B1	true
8	CIB.SR7.57.B2	true
9	CIB.USC55.L5...	true
10	CIB.UA87.R8....	true
11	CIB.USC55.L5...	true
12	CIB.UA87.R8....	true
13	CIB.US15.R1.B1	true
14	CIB.US15.R1.B2	true
15	CIB.UJ33.U3.B2	true
16	CIB.UJ33.U3.B1	true
17	CIB.UA63.L6.B2	true
18	CIB.UA63.L6.B1	true
19	CIB.SR3.53.B2	true
20	CIB.SR8.INJ2.1	true
21	CIB.SR3.53.B1	true
22	CIB.SR2.INJ1.1	true
23	CIB.UA67.R6....	true
24	CIB.SR2.INJ1.2	true
25	CIB.UA67.R6....	true
26	CIB.CCR.LHC.B1	true
27	CIB.UA23.R6.B4	true
28	CIB.UA23.L2.B2	true
29	CIB.CCR.LHC.B2	true
30	CIB.UA47.R4....	true
31	CIB.UA23.L2.B1	true
32	CIB.UA43.L4.B2	true
33	CIB.UA43.L4.B1	true
34	CIB.T276.U7.B2	true
35	CIB.T276.U7.B1	true
36	CIB.SR8.INJ2.2	true

FILTER

Beam_Permit_Loop Beam_Permit Local_Permit User_Permit User_Permit_Glitch Software Mask Masked_Permit
 Disabled_Permit Channel_Enable Test Power Self_Test Time Safe_Beam_Flag Marker Injection_BICs
 Channel_A Channel_B Beam_1 Beam_2 Generator

Triggered BIS Inputs:

Ch 12-PIC_MSK(L1.B1),
 Ch 5-PIC_UNM(L1.B1),
 Ch 12-PIC_MSK(L1.B2),
 Ch 5-PIC_UNM(L1.B2),
 Ch 5-PIC_UNM(R1.B2),
 Ch 4-BLM_UNM(L6.B2),
 Ch 4-BLM_UNM(L6.B1),
 Ch 11-BLM_MSK(L6.B2),
 Ch 11-BLM_MSK(L6.B1),
 Ch 8-BPMs L&R syst.'A' (R6.B2),
 Ch 8-BPMs L&R syst.'A' (R6.B1),
 Ch 10-BPMs L&R syst.'B' (L6.B1)
 Ch 10-BPMs L&R syst.'B' (L6.B2)
 Ch 3-LBDS-b2(R6.B2),
 Ch 3-LBDS-b1(L6.B1),
 Ch 4-Vacuum b1b2(R1.B2),
 Ch 4-Vacuum b1b2(R1.B1),
 Ch 1-Vacuum b2(L1.B2),
 Ch 1-Vacuum b1(L1.B1)

Summary

Beam Interlock System is by design: safe, reliable, fast, modular....

+ has embedded features for monitoring and testing interlock process,

Together with Timing system, Post-Mortem and GUI applications:

- Provide clear and useful information to Operation
- Minimize machine downtime



Thank you for your attention



Spare

BIS Feature

“Flexible”:

thanks to Input Masking

Within a fixed partition, half of *User Permit* signals could be remotely masked

Masking depends on an external condition:

the **Setup Beam Flag**

- generated by a separate & dedicated system (Safe Machine Parameters)
- distributed by Timing

*Masking automatically removed when
Setup Beam Flag = FALSE*



BIS User Systems: LHC Vs. Linac4

version of 01.08.2009

User Systems	LHC ring																Σ	INJ.		Abbrev.	
	L1	R1	L2	R2	U3	S3	L4	R4	L5	R5	L6	R6	U7	S7	L8	R8	CCR	b1	b2		
1 Collimation (Environmental Param.)			1,1	1,1	1,1	1,1			1,1	1,1	1,1		1,1	1,1	1,1	1,1		10	10	1	COLL_ENV
2 Collimation (Motor positions)	1,1	1,1	1,1	1,1	1,1				1,1	1,1	1,1		1,1	1,1	1,1	1,1		11	11	1	COLL_MOT
3 Vacuum system ("sector valves")	1,1		1,1	1,1	1,1		1,1	1,1	1,1	1,1	1,1		1,1	1,1	1,1	1,1		12	12		VAC
4 PIC for essential circuits	1,1	1,1	1,1	1,1	2,			1,1	1,1	1,1	1,1	1,1	1,2		1,1	1,1		16			PIC_UNM
PIC for auxiliary circuits	1,1	1,1	1,1	1,1	2,			1,1	1,1	1,1	1,1	1,1	1,2		1,1	1,1		16			PIC_MSK
5 BLM at aperture limitations	1,1		1,1		1,1			1,1		1,1		1,1		1,1	1,1			8			BLM_UNM
BLM in arcs	1,1		1,1		1,1			1,1		1,1		1,1		1,1	1,1			8			BLM_MSK
6 Fast Magnet current Change Monitors	1,			2,		3,					1,		1,1		3,			10	1	1	FM xxxx
7 WIC (Warm Magnets Interlock)	1,	1,	1,					1,	1,	1,				1,	1,			1,			WIC
8 Screens		1,		1,1				1,1			1,	1,1	1,					4	5		BTV
9 RF & Transverse Damper					1,1													2	2		RF
10 Beam excursion (BPM)																		1	1		BPM
11 LHC Beam Dumping System										1,	1,							1	1	1	LBDS
12 Beam Aperture Kicker							1,1											1	1		MKA
13 Injection Kickers																		1	1	1	MKI
14 TCDQ																		1	1		TCDQ
15 LHC Access Safety System								1,		1,								1	3		LASS
16 LHC Control Room (Operator Buttons)																		1	1	1	CCC
17 Programmed Beam Dump (via Timing)																		1	1		PROG
18 LHC Safe Machine Parameters																		1	1		SMP
19 Fast Beam current Change Monitors								1,1										1	1		FBCM
20 ATLAS (Detector part)			1,															1	1	1	ATL_DET
21 LHCf (Detector part)		1,																1			LHCf_DET
22 ALICE (Detector part)				1,														1	1	1	ALI_DET
23 CMS (Detector part)										1,								1	1	1	CMS_DET
24 TOTEM (Detector part)										1,								1	1	1	TOT_DET
25 LHCb (Detector part)										1,								1	1	1	LHCb_DET
26 ATLAS (Magnets)			1,															1			ATL_MAG
27 ALICE (Magnets)					1,													1			ALI_MAG
28 CMS (Magnets)										1,								1			CMS_MAG
29 LHCb (Magnets)																		1	1		LHCb_MAG
30 ATLAS (movable devices)				1,1														1	1		ATL_MOV
31 TOTEM (movable devices)																		1	1		TOT_MOV
32 LHCb (movable devices)																		1			LHCb_MOV
33 ALICE-ZDC (movable device)																	0	0	1	ALI_ZDC	
34 MSI Convertor Sum Fault																	0	0	1	MSI_SUM	
User Systems	L1	R1	L2	R2	U3	S3	L4	R4	L5	R5	L6	R6	U7	S7	L8	R8	CCR	b1	b2	Abbrev.	
Individual beam connections (Unmaskable) max = 3,3	1,1	1,1	1,1	2,1	1,1	0	1,1	1,1	1,1	1,1	2,1	1,2	1,1	0,0	1,1	1,1	50	51	14	13	
Both beams connections (Unmaskable) max = 4	3	4	4	3	3	1	2	2	3	4	3	1	3	1	4	4	87	215		connections	
Individual beam connections (Maskable) max = 3,3	1,1	1,1	1,1	2,1	1,1	0	1,1	1,1	1,1	1,1	2,1	1,2	1,1	0,0	1,1	1,1	50	51	14	13	

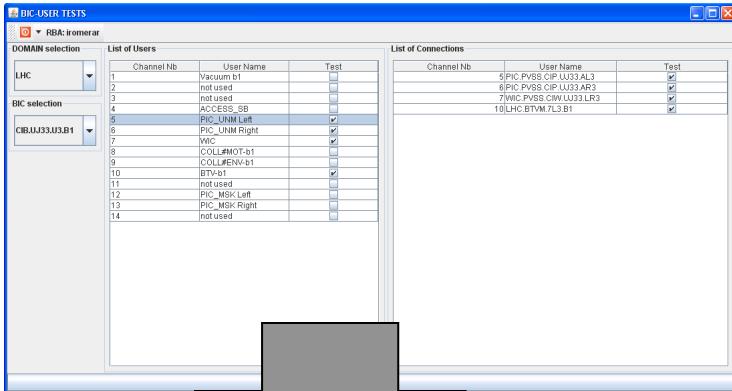
SIS
Source HV
Pre-chopper
L4 Beamstopper Out/Moving
L4 BeamstopperIn
Chopper
L4 Low-Energy WD
No Inhibit (Operator)
L4 Low-Energy Vacuum Valves
AQN L4L.MQF3910
AQN L4L.MQD4010
AQN L4L.MQF4110

SIS
Ex. Conditions (full pulse)
L4 RF
BLMs L4+TL (low loss)
BLMs L4+TL (high loss)
L4 WD before BHZ20 (high loss)
L4 WD before BHZ20 (low loss)
L4 Vacuum Valves + L4T.WGS.0101

Low Energy part
after Chopper

BIS: Operational Checks

Pre-Operation checks (launched by Beam Sequencer)



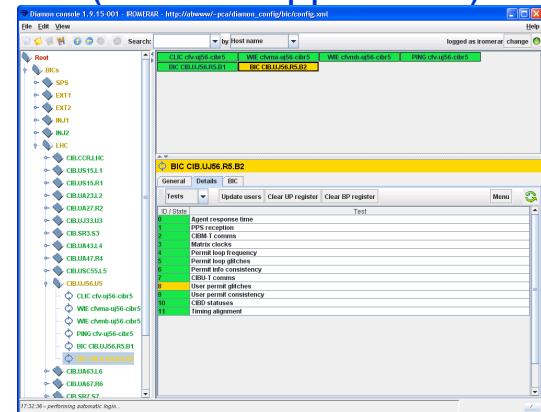
configuration verification
and integrity check

fault diagnosis
and
monitoring

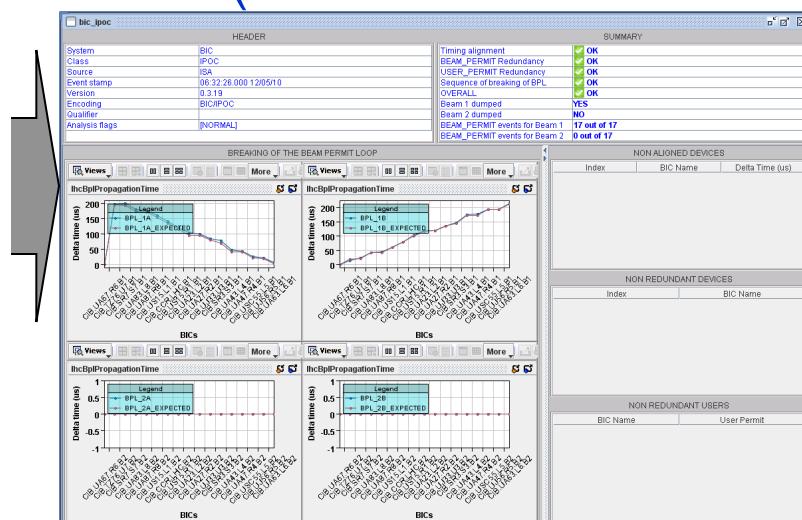
In order to ensure
that safety is not
compromised,
the verification is
carried out in three
stages

response
analysis

During Operation
(DiaMon application)



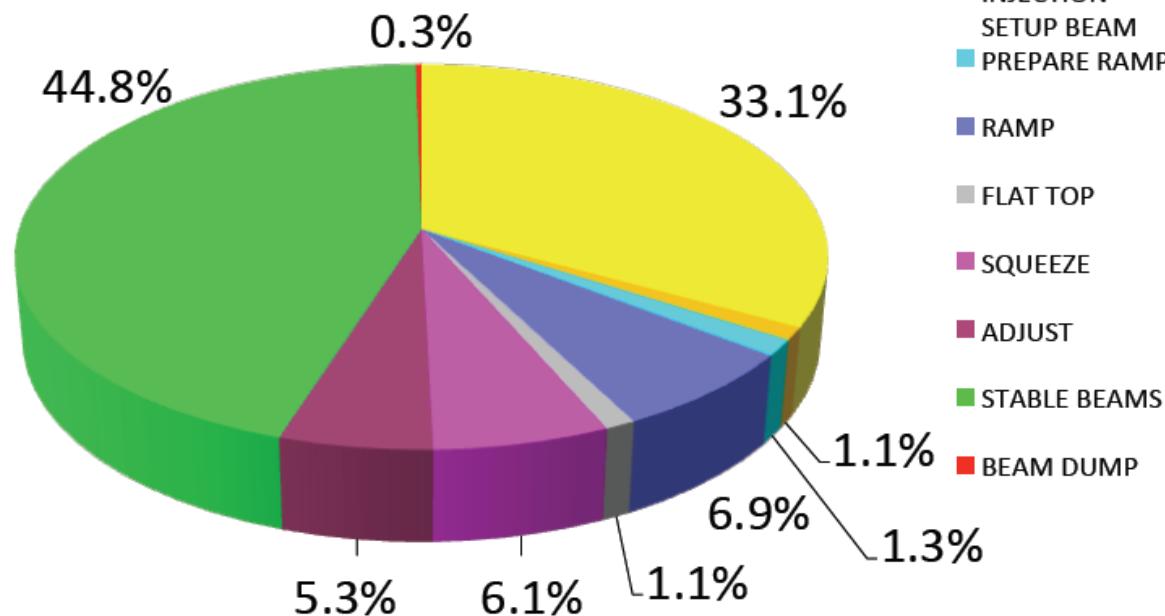
Post-Operation checks
(included in Post Mortem analysis)



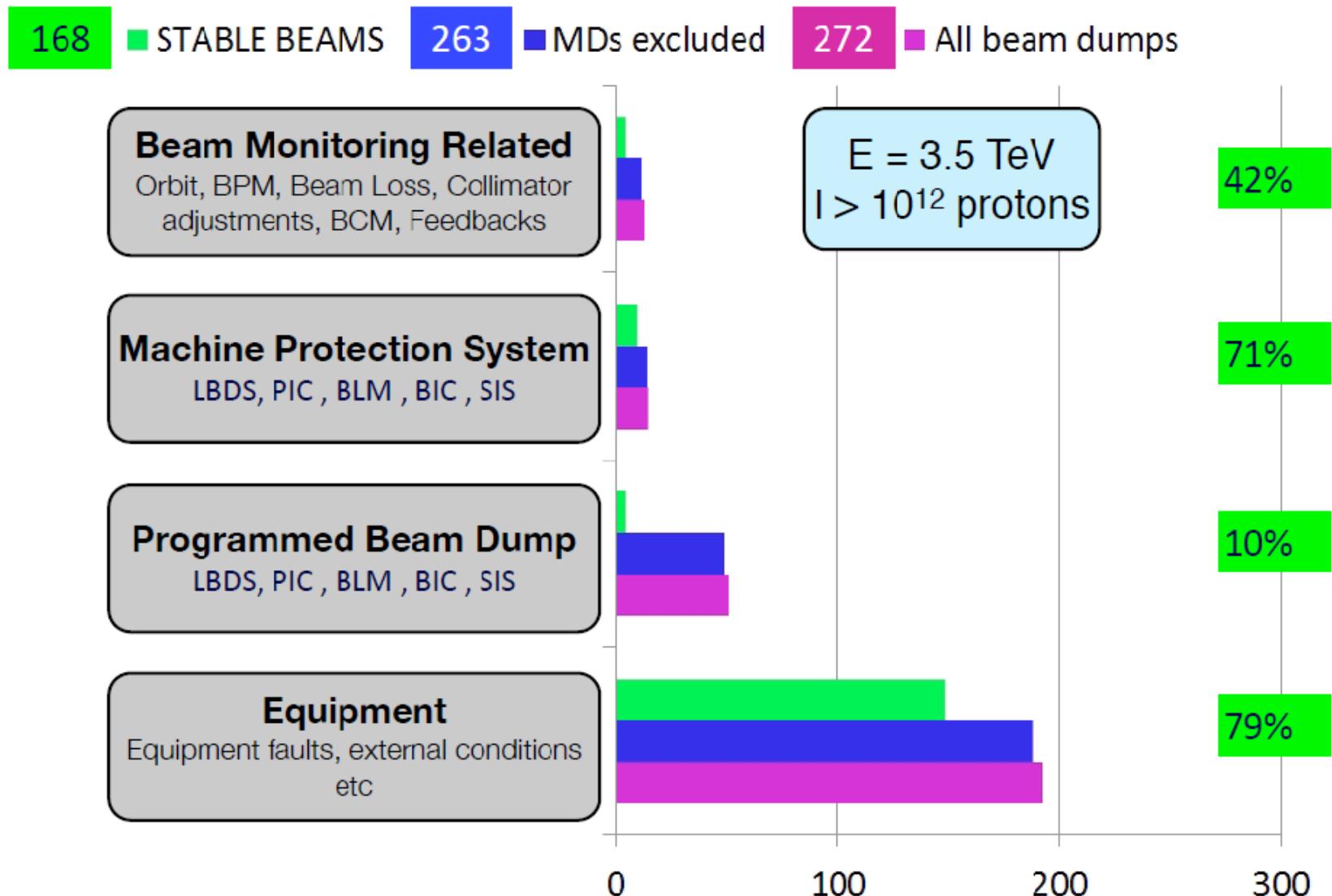
Beam Dumps in 2011 Proton Run

- Number of Beam Dumps in 2011 p-p Run: **482**
- Number of Non-Programmed Beam Dumps: **375 (78%)**
- Number of Non-Programmed Beam Dumps in Stable Beams: **168 (35%)**

Non-programmed dumps in 2011 p-p Run



2011 Proton Run: Beam Dump Causes



Safe Machine Parameters Layout

