

Development of Microwave Kinetic Inductance Detectors and a newly-developed Readout System for LiteBIRD

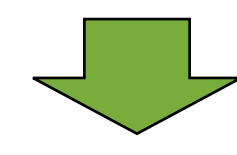
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1. Introduction

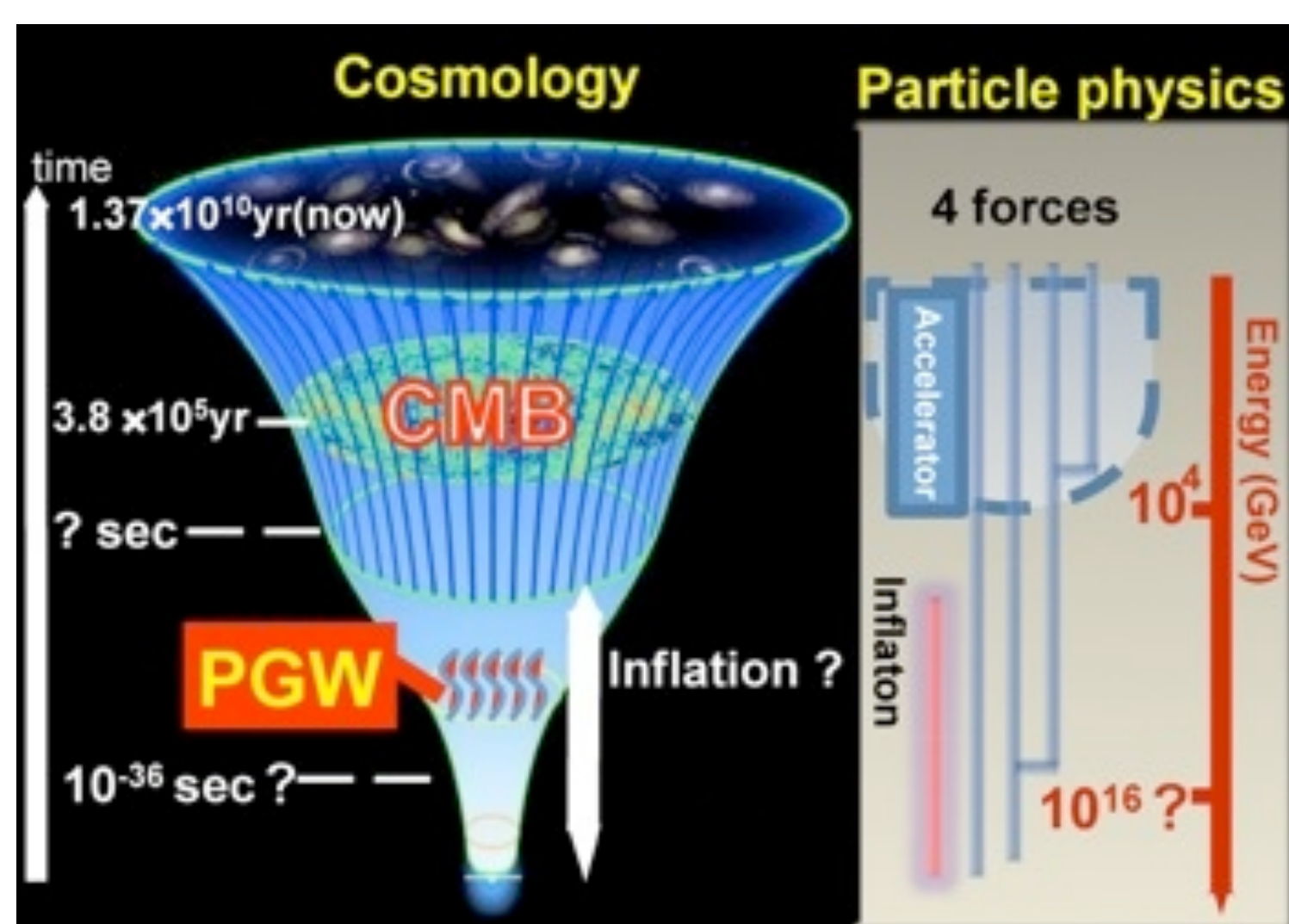


- Primordial gravitational waves (PGW) were supposed to be generated during inflation.
- imprinted a **B-mode polarization pattern** on cosmic microwave background (CMB)
- precise measurement of B-mode can identify an inflation model

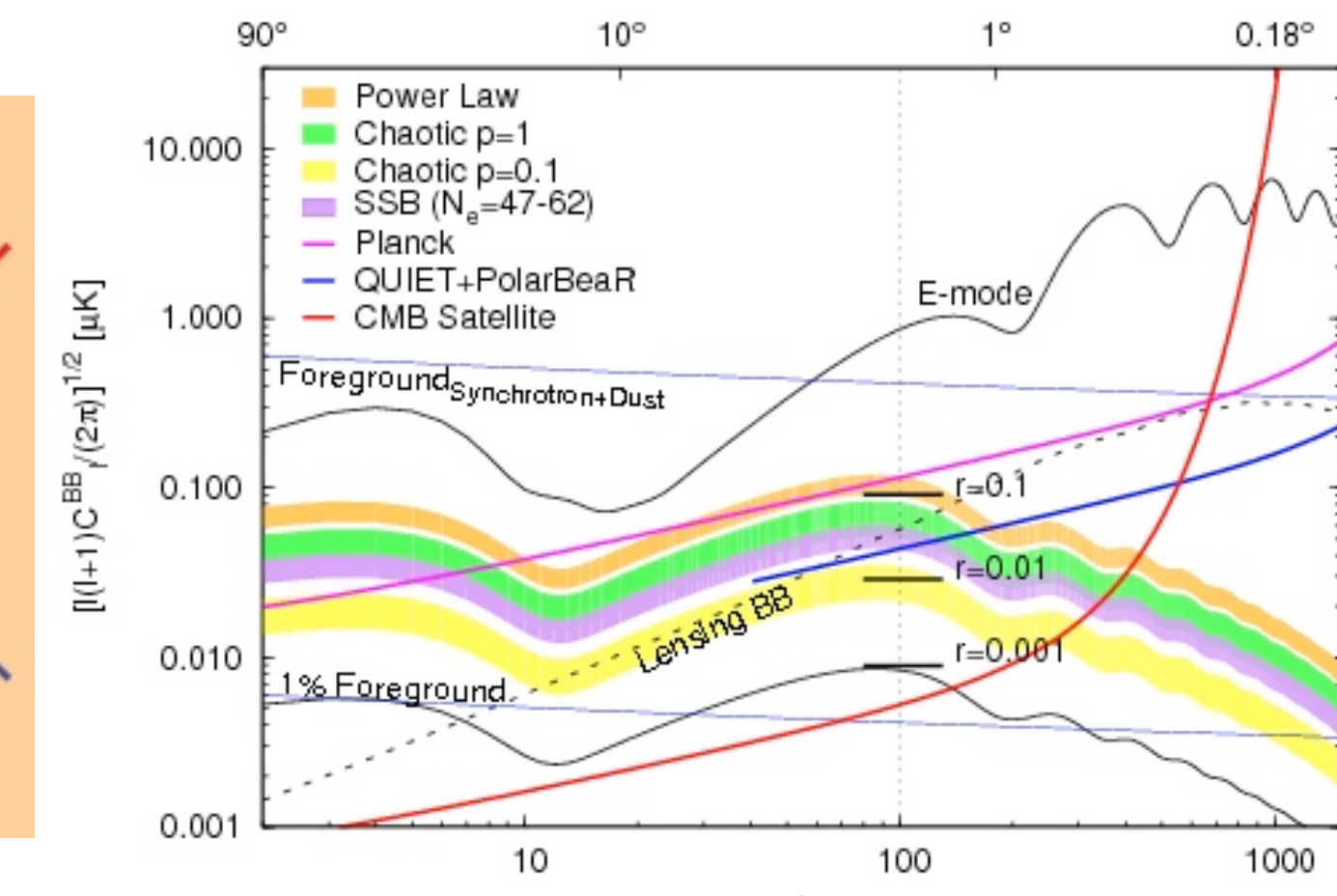
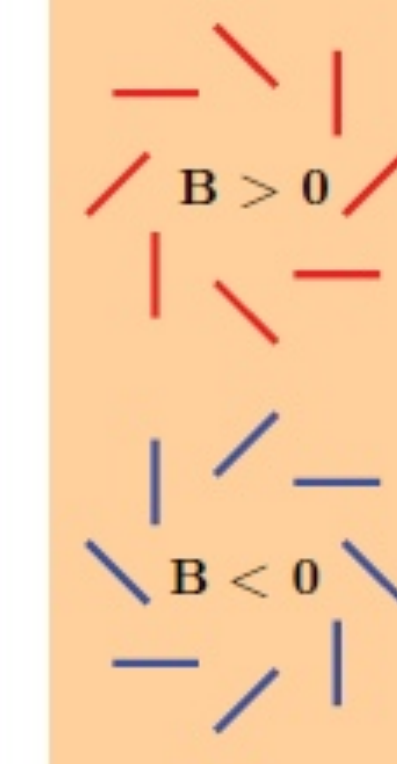
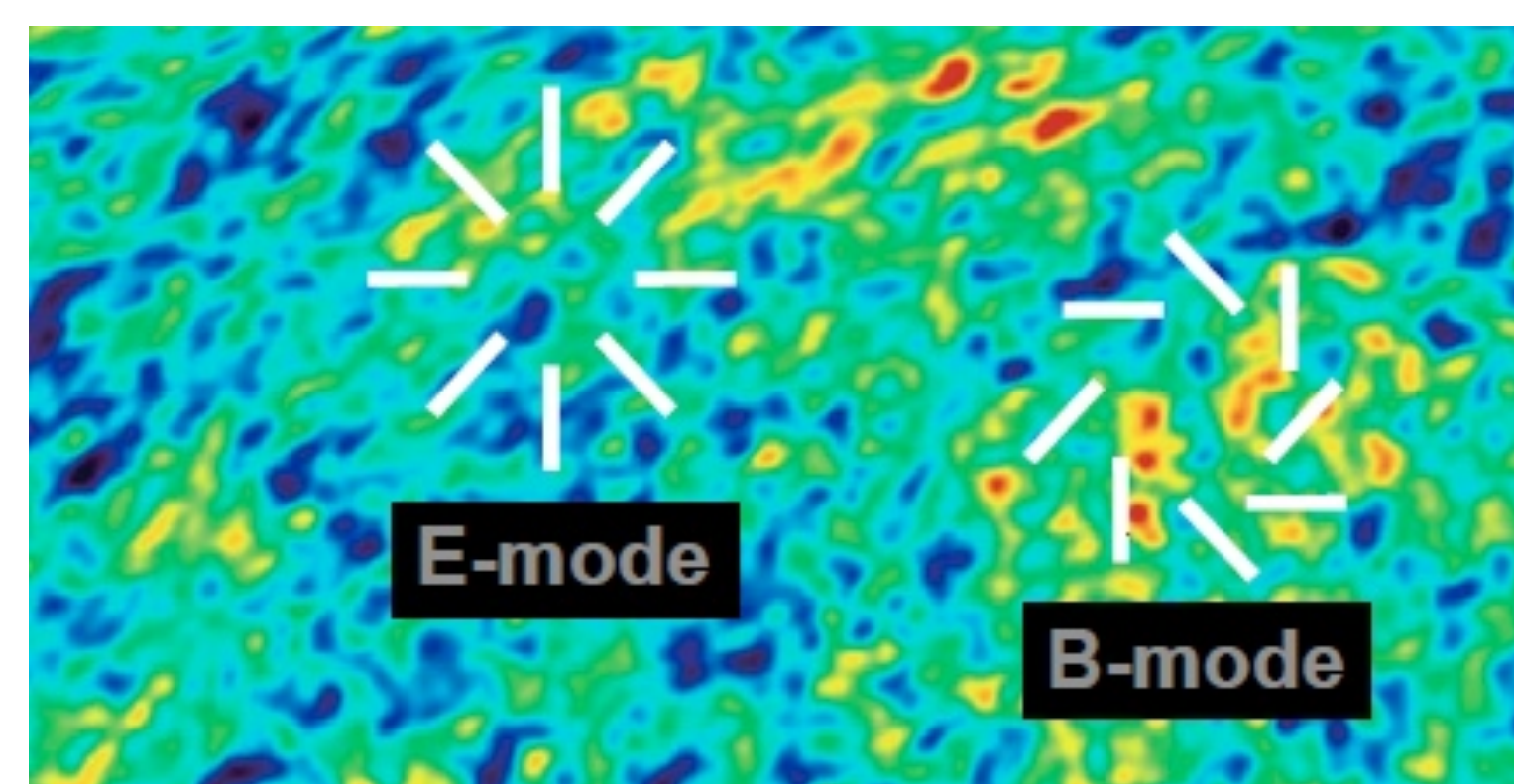


LiteBIRD: being designed to detect CMB B-mode polarization pattern

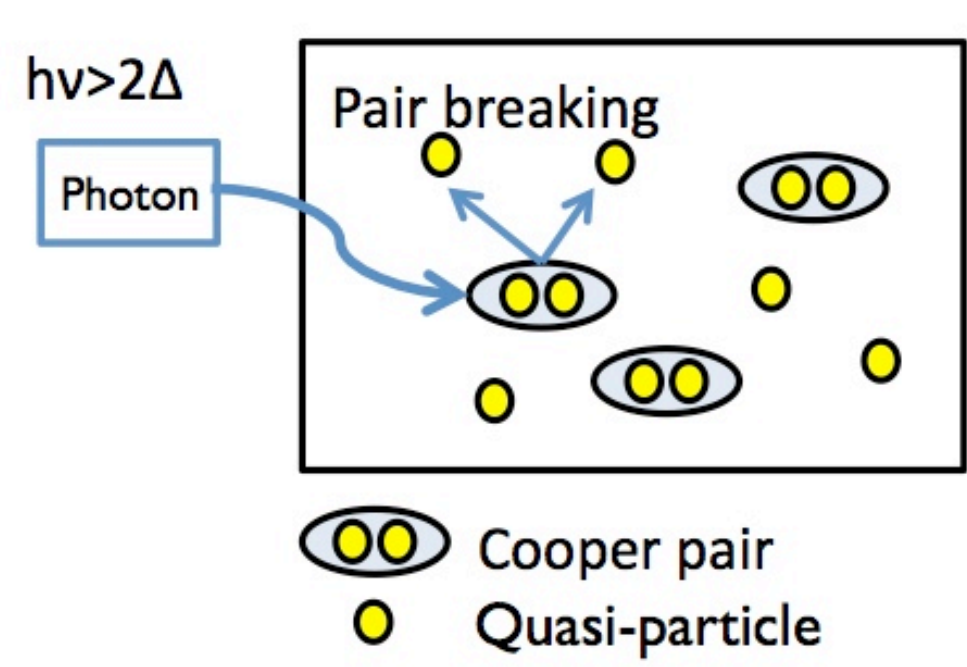
- ← need about **2,000 high sensitive detectors with wide frequency band (50 - 250 GHz)** and **high multiplexing factor** due to limiting cooling power



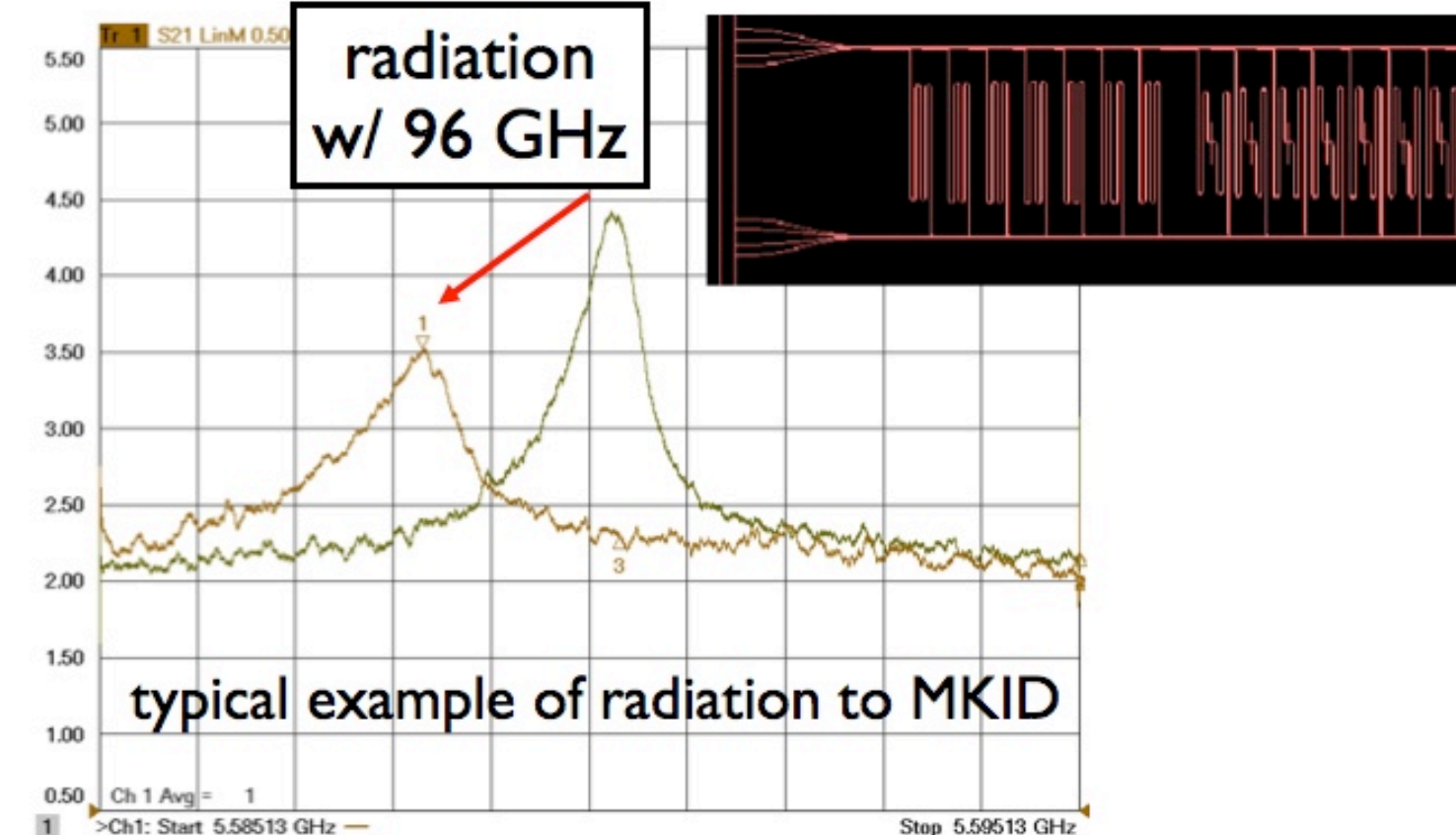
MKIDs are one of the candidates as suitable detectors!



2. Microwave Kinetic Inductance Detectors (MKIDs)

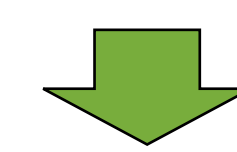


Breaking Cooper Pair
Change Inductance



• MKIDs

- consist of a feed line and resonators
- Cooper pairs are broken and quasi-particles are generated when a resonator is irradiated by millimeter-wave radiation

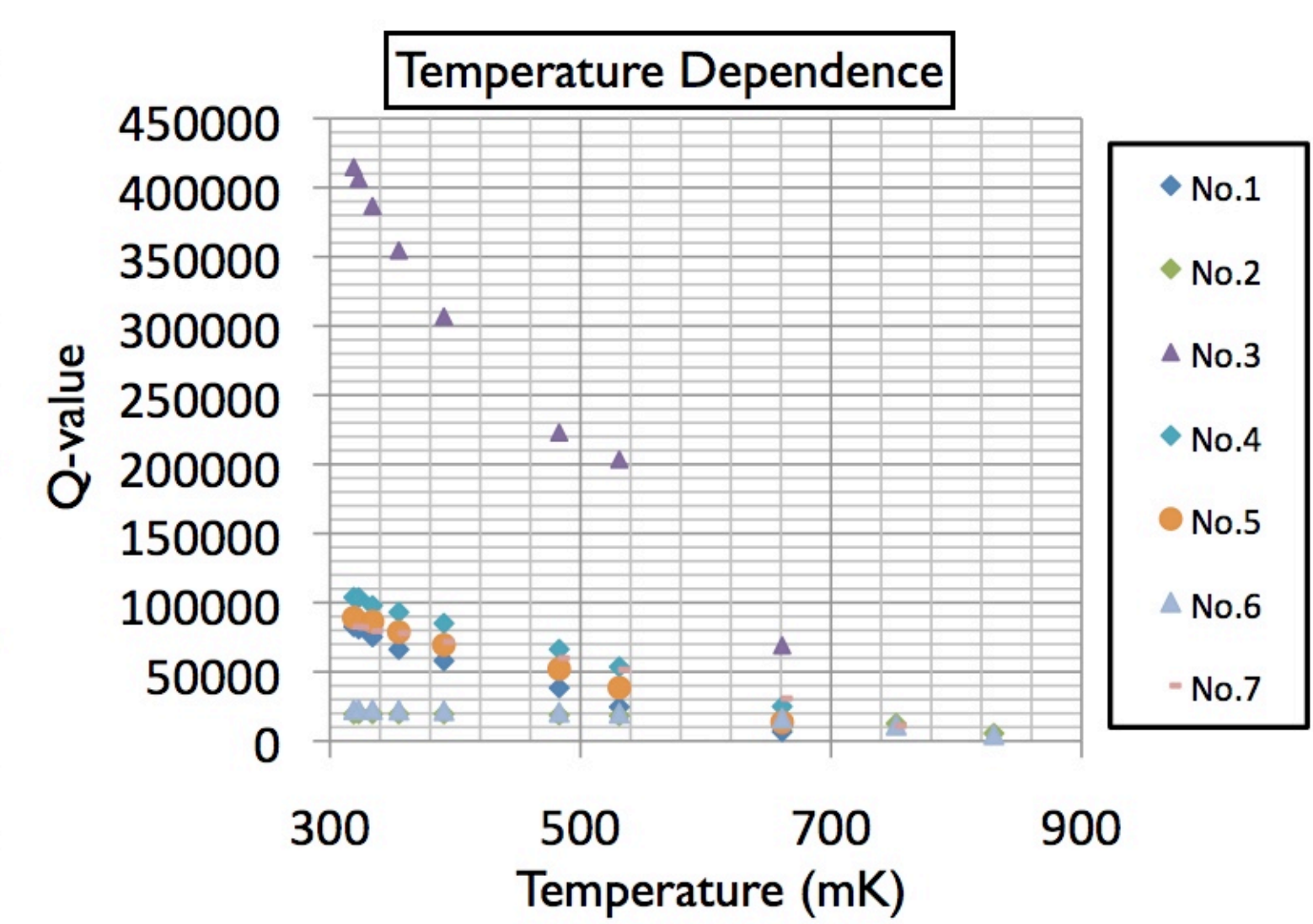
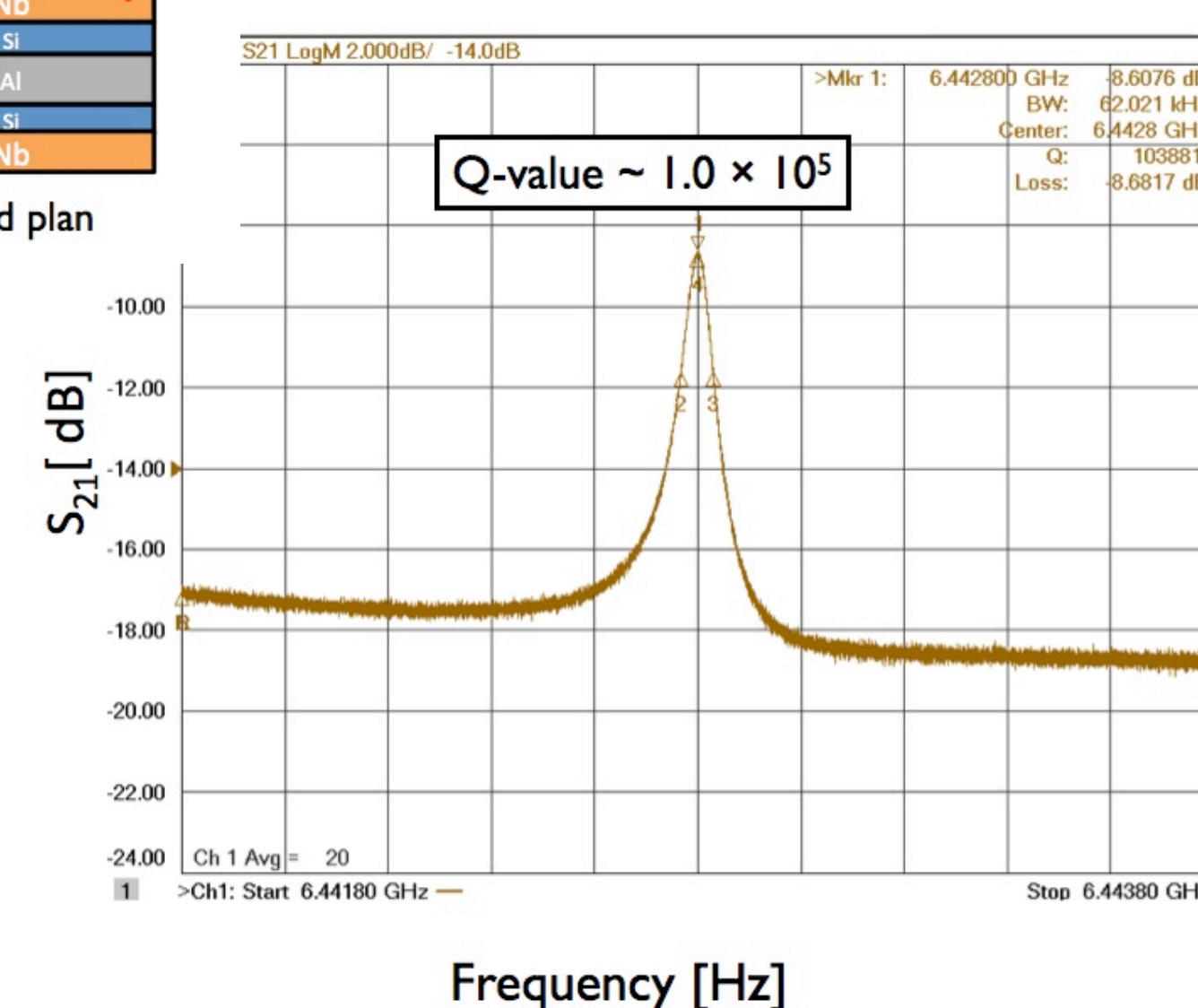
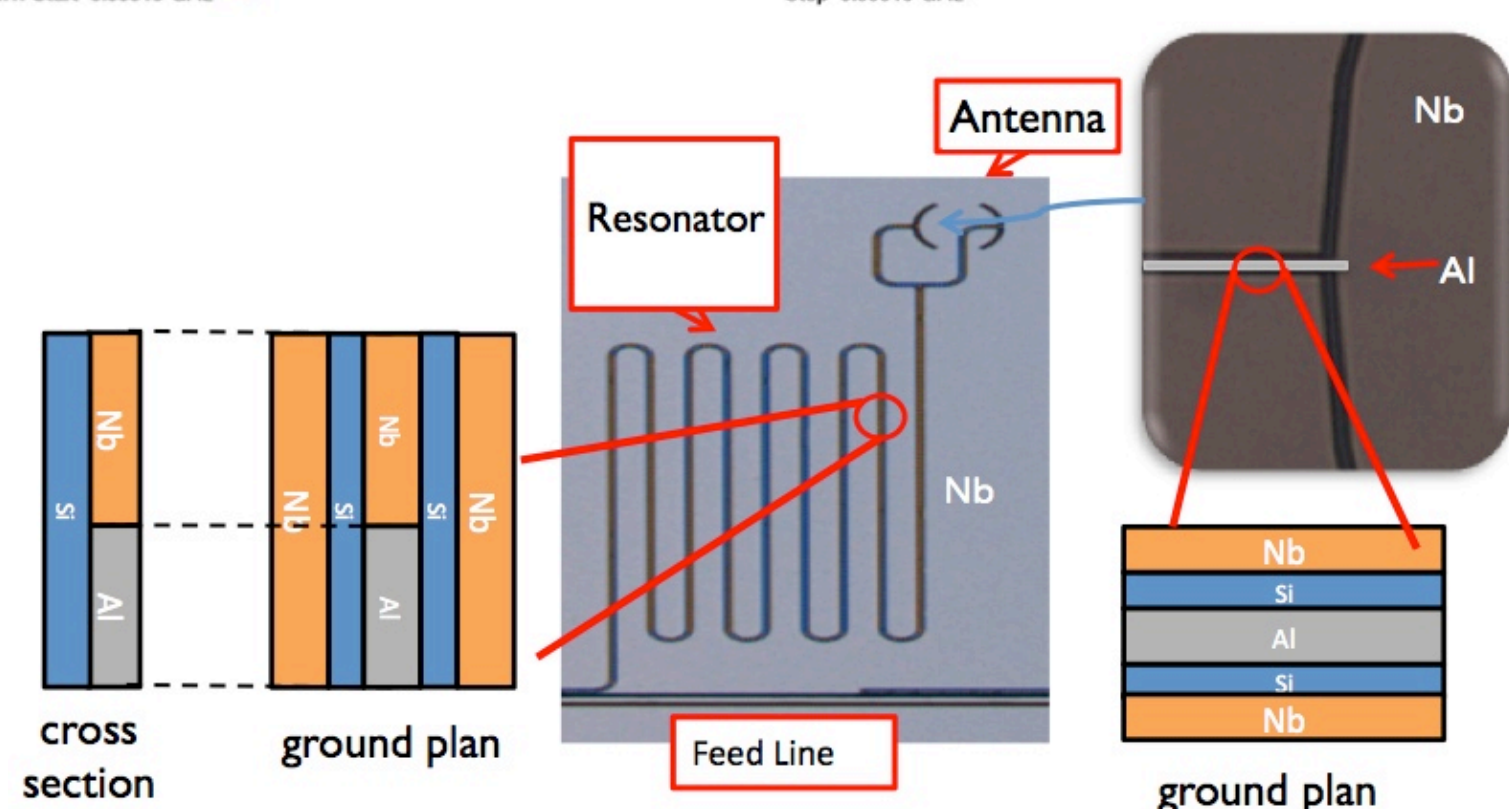


Resonant frequency changes.

- alters transmission **amplitude and phase** of the on-resonant microwave
- enables us to detect the millimeter-wave radiation

• Making high sensitive MKIDs

- Nb-Al hybrid CPW MKID
- a niobium thin layer is used for feed line and resonators, and aluminum is used for resonators near the antenna

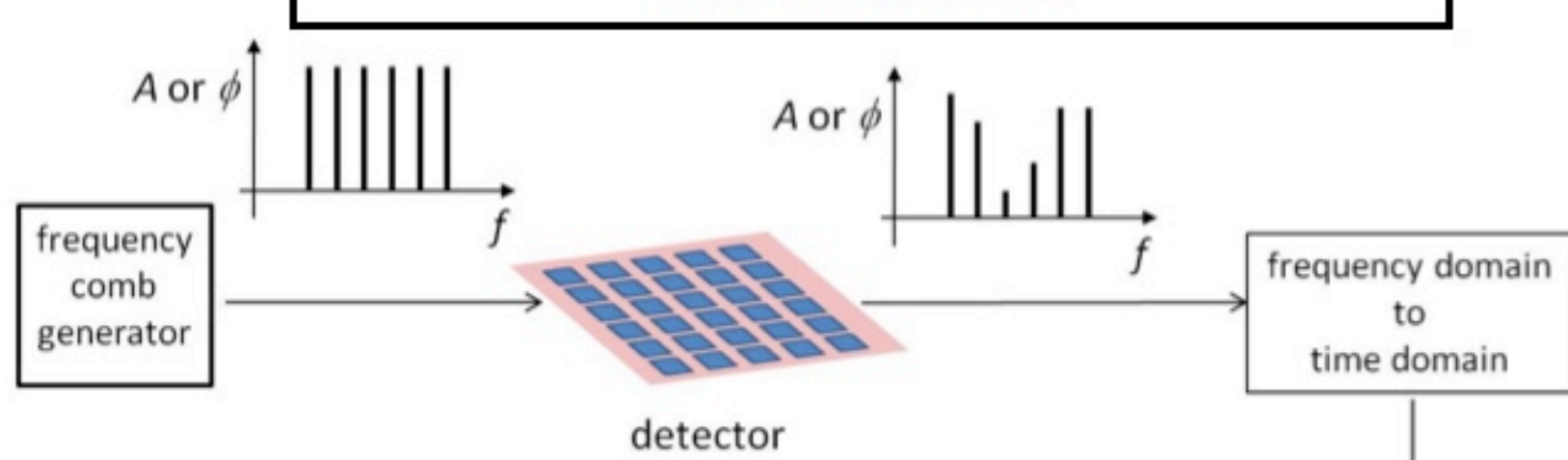


• Checking performance of Al-Nb hybrid MKIDs

- performed the experiment with the sorption cryostat
- achieved the Q-value of $\sim 4.0 \times 10^5 @ 0.3 \text{ K}$
- investigated the dependence of resonant peak on temperature, and confirmed that Q-value of a resonant peak decreases in inverse proportion to temperature

3. Readout System

Schematic View of Readout System for LiteBIRD

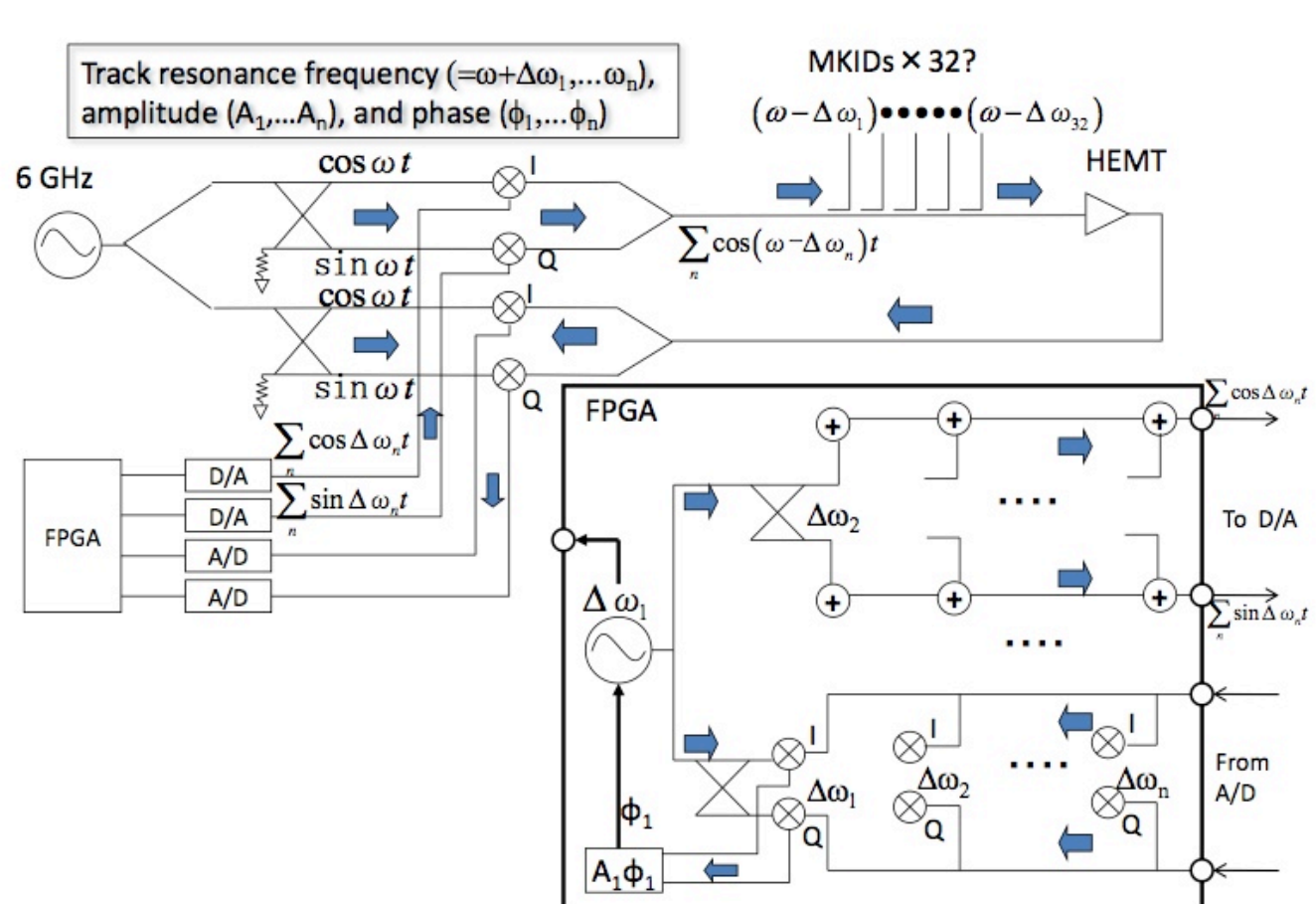


• Readout System for LiteBIRD

- read out multi-channels with one cable
→ prevent heat inflow (favorable for low temperature)
- sampling rate is faster than detector response ($\gg 10 \text{ kHz}$)
- observation without dead time
- readout rate (data transfer to offline) $> 10 \text{ kHz}$
→ perform **common mode suppression** to reduce noise

• R & D Status of Readout System

- develop 2-channel FPGA-based readout system
- debug the readout logic with comparing data and numerical simulation results. This simulation reproduces data well.
→ **We can understand noise characterization sufficiently.**
→ aim for low noise system to satisfy required noise level ($1.95 \times 10^{-6} \text{ rad} \cdot \sqrt{\text{sec}}$)



Xtream DSP Development Kit-IV (Xilinx FPGA Vertex-4)

Data from Readout System Numerical Simulation

