8TH INTERNATIONAL CONFERENCE ON POSITION SENSITIVE DETECTORS



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8th International Conference on Position Sensitive Detectors

Glasgow, Scotland, 1st to the 5th of September

Polarimetry: The Missing Piece of the Puzzle

Imaging: Chandra

Timing: RXTE



The only polarized source already known



Positive measurement: of X-ray polarization of the Crab Nebula without pulsar contamination (by lunar occultation. Weisskopf et al., 1978). $P = 19.2 \pm 1.0 \%$ $\theta = 156.4^{\circ} + 1.4^{\circ}$

But this is only the average measurement.The structure is much more complex!

The principle of detection



GEM electric field



X photon (E)

pixel

5 μs

VLSI

αE

gain

A custom CMOS analog chip is at the same time the pixelized charge collecting electrode and the amplifying, shaping and charge measuring frontend electronics of Micropattern Gas Detectors (MPGD) or other suitable charge multiplier



 Christensen, F. E. et al. X-ray calibration of the SODART flight telescope. Proc. SPIE 3113, 69–78 (1997).

Three ASIC generations of increasing size, reduced pitch and improved functionality have been realized



The collecting anode/read-out VLSI chip

First ASIC

prototype

asynchronous, fast, low noise

pixel electronics dimension: 80 μm x 80 μm in an hexagonal array, <u>comprehensive</u> of preamplifier/shaper, S/H and routing (serial read-out) for each pixel number of pixels: 2101

CANNELL MILL LET

~3.5 μs shaping time 100 e- ENC 100 mv/fC input sensitivity 20 fC dynamic range



Last technological step: a 0.18 μm CMOS VLSI



Matrix organization: 300 x 352 pixels width=300x50µm=15mm, height=352x43.3µm=15.24mm The chip integrates more than 16.5 million transistors. It has a15mm x 15mm active area of 105'600 pixels organized in a honeycomb matrix

> 470 pixels/mm² Pixel noise: 50 electrons ENC Total power dissipation ~ 0.5 Watt

Detector assembly



1 - The GEM glued to the bottom of the gas-tight enclosure2 - The large area ASIC mounted on the control motherboard

Large effective gas gain around 1000 @450V in Ne(50%)-DME(50%) (at least 70 V less than in our standard 90 μm pitch GEM)

GEM specs

pitch: 50 μm holes innter Ø: 35 μm



The matching of readout and gas amplification (GEM) pitch allows getting optimal results and to fully exploit the very high granularity of the device

Tracks reconstruction

1) The track is recorded by the PIXel Imager

2) Baricenter evaluation

3) Reconstruction of the principal axis of the track: maximization of the second moment of charge distribution

4) Reconstruction of the conversion point: major second moment (track length) + third moment along the principal axis (asymmetry of charge release)

5) Reconstruction of emission direction: pixels are weighted according to the distance from conversion point.





Track morphology and angle reconstruction





Imaging capability



55Fe source Ne(50%)-DME(50%)



Holes: 0.6 mm diameter, 2 mm apart.

Imaging and spectroscopic capability

Argon (50%)-DME(50%)

Baricenter position





Holes: Ø 0.5 mm pitch 1 mm

Residual modulation



S/N distribution, scatter plot of the two principal axes of the cluster charge and residual modulation, obtained with 55Fe source in Ne(50%)-DME(50%)



The level of integration, compactness and operational simplicity of this device is comparable to solid state detectors

Semitransparent Photocathode



Drift gap = 1 mm Transfer gap = 1mm GEM thickness = 50 μm GEM pitch = 50 μm

Pros:

- Simple
- High gain
- High geometrical efficiency

Cons:

- Low thickness \rightarrow Low Q.E.
- Extra diffusion in the gas layer above the GEM

Reflective Photocathode



Drift gap = 1 mm Transfer gap = 1mm GEM thickness = 50 mm GEM pitch = 50 mm

Pros: • Thick film \rightarrow high Q.E. (10-20%)

Cons:

- More complicated to build
- Special gold coating on the GEM
- Low geometrical efficiency (in our case 50%)
- Lower gas gain

Csl photocathode



Single photon operation

Single photon event topology

Semitransparent Photocathode

"Self-portrait" of the GEM amplification structure

Intrinsic resolution of the read-out system

′**σ ~ 4** μ**m**

1 electron primary charge

Semitransparent Photocathode

First test in collaboration with Space Science Laboratory, Berkeley

The events

At low rate the detector records single photon events in window mode (<1K channels/frame, frame rate up to 10KHz).

Thanks to the high granularity, at high rate the detector can resolve up to few hundreds of photons/frame in full frame readout mode (1KHz frame rate, up to ~200KHz photon rate)

MCP 12.5µm aperture 15µm pitch

The USAF1951 3-Bar Resolving Power Test Chart

Full detector image

Line pairs/mm = $2^{n+(m-1)/6}$ X (mm) = $2^{-n-(m-1)/6}$ Es. n=6, m=1, X= $2^{-6}=1/64=15.6\mu$ m n=6, m=2, X= $2^{-(6+1/6)}=1/72=14\mu$ m n=6, m=6, X= $2^{-(6+5/6)}=1/114=8.7\mu$ m n=7, m=1, X= $2^{-7}=1/128=7.8\mu$ m

MCP 10µm aperture 12µm pitch

MCP 4µm aperture 5.5µm pitch

The CMOS counting ASIC

The chip integrates more than 259 million transistors.

It has 480k pixels organized in a honeycomb matrix of 600 columns×800 lines corresponding to an active area of 24mm (600x40μm) ×27.7mm (800x34.64μm)

Each pixel is connected to a charge-sensitive shaping amplifier followed by a discriminator and a 15-bit shift register.

A self-calibration circuit is implemented in each pixel to reduce unavoidable DC offset variations from pixel to pixel \rightarrow a global threshold can be applied to the whole matrix.

Each pixel column can be individually configured for:

- counting the number of events during a given time slot or
- providing, with an external clock, a timestamp to the event or the time over threshold

Work program

This chip has been or will be used as the basic building block of several detector configurations:

- coupled to gas ionization chamber
- coupled to charge multipliers (MCP, GEM ...)
- coupled to solid state semiconductors (CdTe, CZT,)

Thin ,High Pressure, Parallel Plate Ionization Chamber

- Gas Pressure:12 bar
- Gas Filling Xe(95)-CO2(5), W=25 eV
- Gas Gap:700 microns
- Entrance Window:1.5 mm Beryllium
- Working Mode:counting
- Illumination:X Rays 5-20keV
- Signal charge/photon:300-800 electrons
- Conceptually identical to a solid state pixel device, but with gas as absorbing medium

Working in gas ionization counting mode (G=1) at 8 keV

The simplest possible detector concept: just a pixelized collection plane and a window No gas system, no complex bump bonding Ideal solution for photon energy around 10keV

> Collaboration with Oxford Instruments Analytical Oy (Finland)

IMAGE GALLERY Imaging at 8 keV in pulse ionization mode Source: copper tube @ 10 kV high voltage

IMAGE GALLERY High resolution image of high contrast object

IMAGE GALLERY High resolution image of a high contrast biological sample

IMAGE GALLERY High resolution image of low contrast biological sample

Head of anchovy X-ray Tube @ 20kV 5.4 keV Cr line

	ХроІ	MEDIPIX2	PIXIE
Technology:	CMOS 0.18 μm	CMOS 0.25 μm	CMOS 0.18 μm
Туре:	analog	digital (counting)	digital (counting+ ToT+Time stamp)
Area:	15x15=225mm ² (1.1X)	14x14=196mm ² (1X)	24x28=672mm ² (3.4X)
Pixel no.:	105.600 (1.6X)	65.536 (1X)	480.000 (7.3X)
Pixel density:	470/mm ² (1.4X)	330/mm² (1X)	720/mm ² (2.2X)
Pixel noise:	50 electrons ENC	110 electrons ENC	50 electrons ENC
Read-out scheme:	asynchronous, synchronous	synchronous	synchronous
Read-out trigger:	self-trigger, internal, external	internal, external	internal, external
Read-out mode:	single pixel, window, full frame (8-16 nodes)	full frame (1 node)	Full frame (up to 200 nodes)
Global threshold:	2000 el. (unadjusted)	1000 el. (adjusted)	200 el. (auto- adjusted)
Frame rate:	10 kHz	1 kHz	5 kHz
Event rate:	~10 ⁵ /s (10 ² ev. / frame)	~ 10 ⁹ /s	> 10 ⁹ /s
Resolution:	~ 1μm (analog int.)	~15µm (55/√12)	~11µm (38/√12)
Metal fraction:	90%	13%	47%

Conclusions

Analog and counting pixel ASICs of <u>very large area</u> and <u>high granularity</u> have been designed, fabricated, packaged and used. These chips have been and will be the basic building block of several detector configurations when coupled to:

- charge multipliers (MCP,GEM,...)
- solid state semiconductors (CdTe, CZT,)
- gas ionization chambers working in pulse counting mode

The *high granularity and low noise* of the read-out plane of the XPOL analog chip and the possibility to reconstruct the centroid of the single electron avalanche from UV photon conversion, has allowed to reach ultra-high intrinsic spatial resolution: 4μ m in gas and 1.6μ m in vacuum.

The possibility to work in ionization mode with high pressure gas fillings coupled to the PIXIE counting chip has been demonstrated and high resolution images at high events rate have been acquired.

Depending on type of charge supplier, pixel and die size, electronics shaping time, analog vs. digital read-out, counting vs. integrating mode, many applications can be envisaged for this class of detectors.

Spares slides

From gas to solid state

Collaboration with ACRORAD (Japan) & AJAT Oy (Finland)

Large area CdTe Schottky type (27.7 x 24.2 x 0.5 mm) (ACRORAD)

Electrode structure:

- -AIN
- Au-Ni-Au
- Pt
- Cdte
- In
- Ti

Pixel pitch CdTe crystal = 80 μm ASIC = 40μm

Only <u>1 out of 4</u> pixel is bump bonded to the crystal (AJAT)

Extremely low dark current @ operating voltages (400-700V)

Stability under HV obtained by switching HV off for a few seconds every half an hour.

IMAGE GALLERY Integrated circuit on a plastic package High and very low contrast structures visible on the same image 35 kV Cr-tube

IMAGE GALLERY Large area ASIC in ceramic package High and extremely low contrast structures visible on the same image 35 kV Cr-tube

IMAGE GALLERY

High resolution imaging of a biological sample Statistics acquired in 800 ms, typical mammography exposure time Average pixel counting rate: 15 kHz - Cr-tube @25 kV

Reflective Photocathode

Drift gap = 1 mm Transfer gap = 1mm GEM thickness = 50 µm GEM pitch = 50 µm

Pros: • Thick film \rightarrow high Q.E. (10-20%)

Cons:

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- Special gold coating on the GEM
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Csl photocathode

0.18 µm ASIC features

- Peaking time: 3-10 μs, externally adjustable;
- Read-out clock: up to 10MHz;
- Frame rate: up to 10 kHz in self-trigger mode (event window);
- Read-out mode: asynchronous or synchronous;
- Trigger mode: intenal, external or self-trigger;
- Self-trigger threshold: 2000 electrons;
- Full-scale linear range: 30000 electrons;
- Parallel analog output buffers: 1, 8 or 16;
- Access to pixel content: direct (single pixel) or serial (8-16 clusters, full matrix, region of interest);
- Fill fraction (ratio of metal area to active area): 92%

The counting ASIC features

 User-selectable data acquisition mode (per column): counting or event timing;

Shutter to control event capture time slots;

Continuous total power dissipation: < 3000 mW

Pixel response to negative input charge

Pixel response to positive input charge

Autocalibration

Problem: in the case of very small pixels, i.e. pixels with extremely low parasitic capacitance, CSA/shaping amplifier & discriminators offsets become more important than noise.

Solution: on chip autocalibration circuit. A single sequencer is used to calibrate simultaneously all pixels in < 10ms.

Implementation (30% of the pixel cell area):

- 5-bit current DAC & autocal code storage register
- 120fF MOSFET & analog switches for CSA and discr
- some combinational logic

Autocal algorithm: a successive approximation algorithm.

The 5-bit DAC is a binary-weighted current source realizing the function lout = (B4*1/2 + B3*1/4 + B2*1/8 + B1*1/16 + B0*1/32) * Iref

The DAC output current lout is added to the output current of the first stage of the discriminator equivalent to a programmable offset voltage at the discri input.

– The discr threshold is set to Vref (=AGnd).

– All bits are successively tried starting with the MSB (B4). If the discr output is high, B4 is returned to 0 (otherwise B4 is set to 1) and the related value is stored, then the procedure continues with next bit (B3), and so on.

– At the end the discr switching point is AGnd ±0.5LSB.

- Then the discr threshold level and the CSA gain are returned back to normal,