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performance of 2nd generation CALICE ASICs (HARDROC, MICROROC, SKIROC & SPIROC)

In the framework of CALICE, EUDET and AIDA programs, technological prototypes for ILC calorimetry have been developed. They rely on highly integrated readout ASICs to perform signal readout, auto-trigger and energy measurement over several millions of channels. Ultra-low power is achieved thanks to power pulsing, which must maintain calorimetric performance. The chips developed for the various types of calorimeters (RPCs, Micromegas, Si diodes or SiPMs) have now been tested extensively on test bench and test beam and the most salient features will be presented.

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