

## **Minutes VME BOC ROD meeting 04-09-2012**

### **BOC**

- No particular issues in the HW BOC-ROD interface: all the lines have been tested extensively (Wuppertal)
- Firmware allows interfacing the FEI4 with and without optoboard

### **ROD**

- Preliminary ROD→BOC→FEI4→BOC-ROD firmware tests with electrical connections only (Bologna)
- SLINK preliminary interface running at 80MHz integrated into the firmware
- DDR module for Virtex to be used is still to be defined: some components are under tests but each requires a specific firmware. Need to converge to a unique type of commercial DDR

### **CERN ATLAS pixel-lab crate**

- ROD, BOC and TIM cards available in the crate
- TDAQ software updated
- Firmware for BOC and ROD to be loaded and tested
- Clock from the TIM card to be distributed to BOC and ROD: a test is foreseen