

Charge to the Committee:

The read out driver (ROD) for the Cathode Strip Chamber (CSC) will be upgraded to cope with a higher Level-1 accept rate of at least 100 kHz. The intent is to install the new RODs during the upcoming long shutdown (LS1) and have them ready when data taking operations resumes in late 2014. The SLAC and UC Irvine groups are taking a lead role in the upgrade of the RODs and are pursuing a design based on the ATCA architecture. The purpose of this review is to critically examine the proposed technical design together with the costs and schedule for this project. The issues we would like the committee to evaluate include:

1. Does the proposed design meet the ATLAS requirements?
2. What is the schedule? Does it meet the ATLAS schedule for installation and commissioning?
3. What are the schedule risks?
4. What are the milestones required to track and monitor progress?
5. What is the total construction cost and profile? How much of that cost is expected to come from the US ATLAS Operations Program budget?
6. What are the cost risks?
7. If necessary, how will those schedule and cost risks be mitigated?
8. What long term effort is needed to maintain and operate the RODs?