

Recent Results of the 3D-Stripixel Si Detectors

Zheng Li¹, D. Bassignana², Wei Chen¹, Shuhuan liu^{1,3}, David Lynn¹, G.
Pellegrini²

¹ Brookhaven National Laboratory, Upton, NY 11973, USA

² Centro Nacional de Microelectrónica (IMB-CNM-CSIC), Campus Univ.
Autónoma de Barcelona, 08193 Bellaterra, Barcelona (Spain)

³ Permanent address: Xian Jiaotong University, Xian, China

Contents

Part I: Full 3D Stripixel Detector Designs

1. 1 New Prototype Generation-A Single-Sided double-Strip Detector
- 1.2 Optimization of design- Device Simulation
- 1.3 Layout and Fabrication of the Detector

Part II: Detector Characteristics Measurements

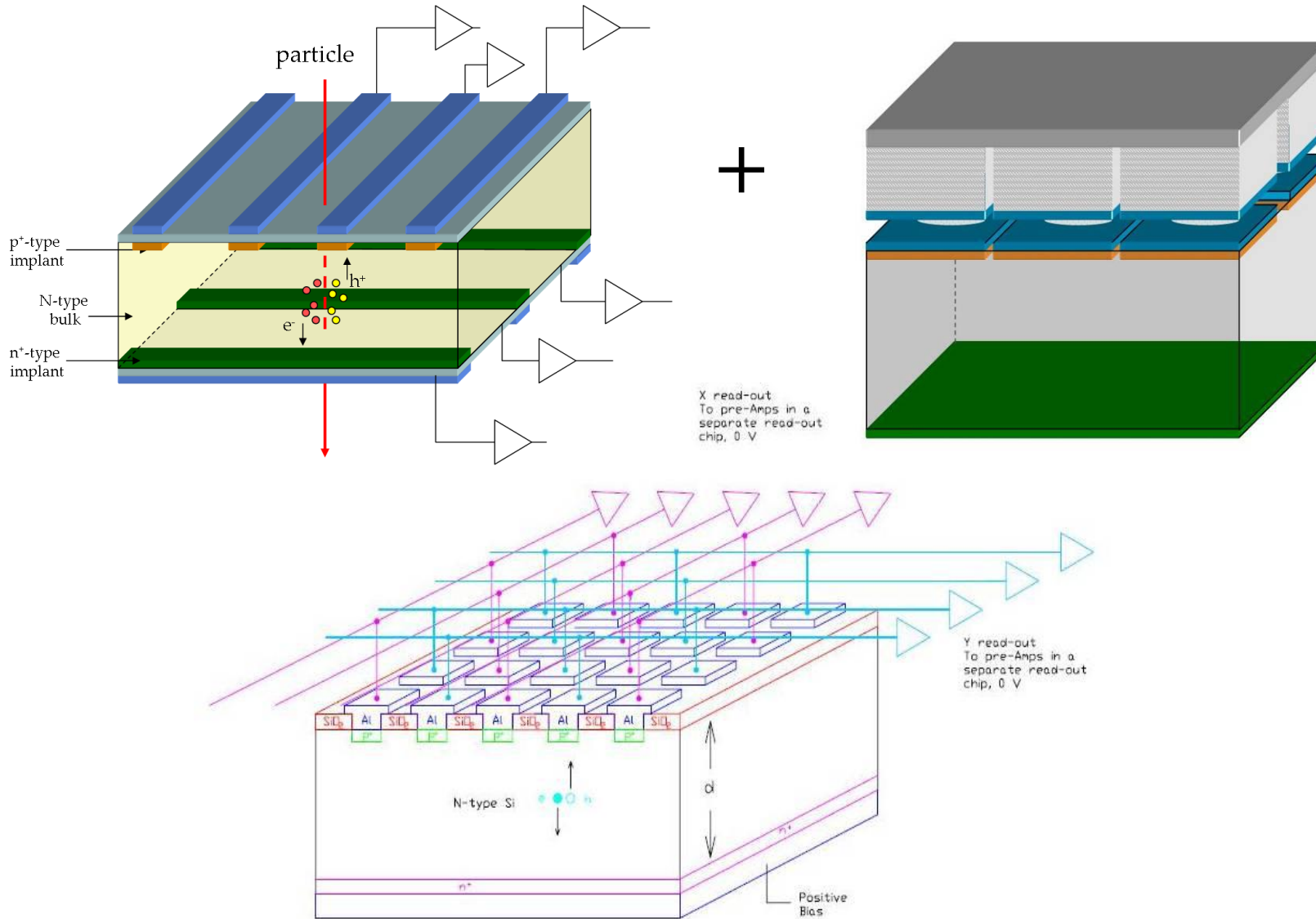
- 2.1 Electrical Characteristics
- 2.2 TCT Measurements
- 2.3 Detector Responses to Laser Measurements
With Alibava DAQ System
 - CCE Characteristics
 - 2D Position Sensitivity

Part III: Summary

Part I:
Full 3D Stripixel Detector
Designs

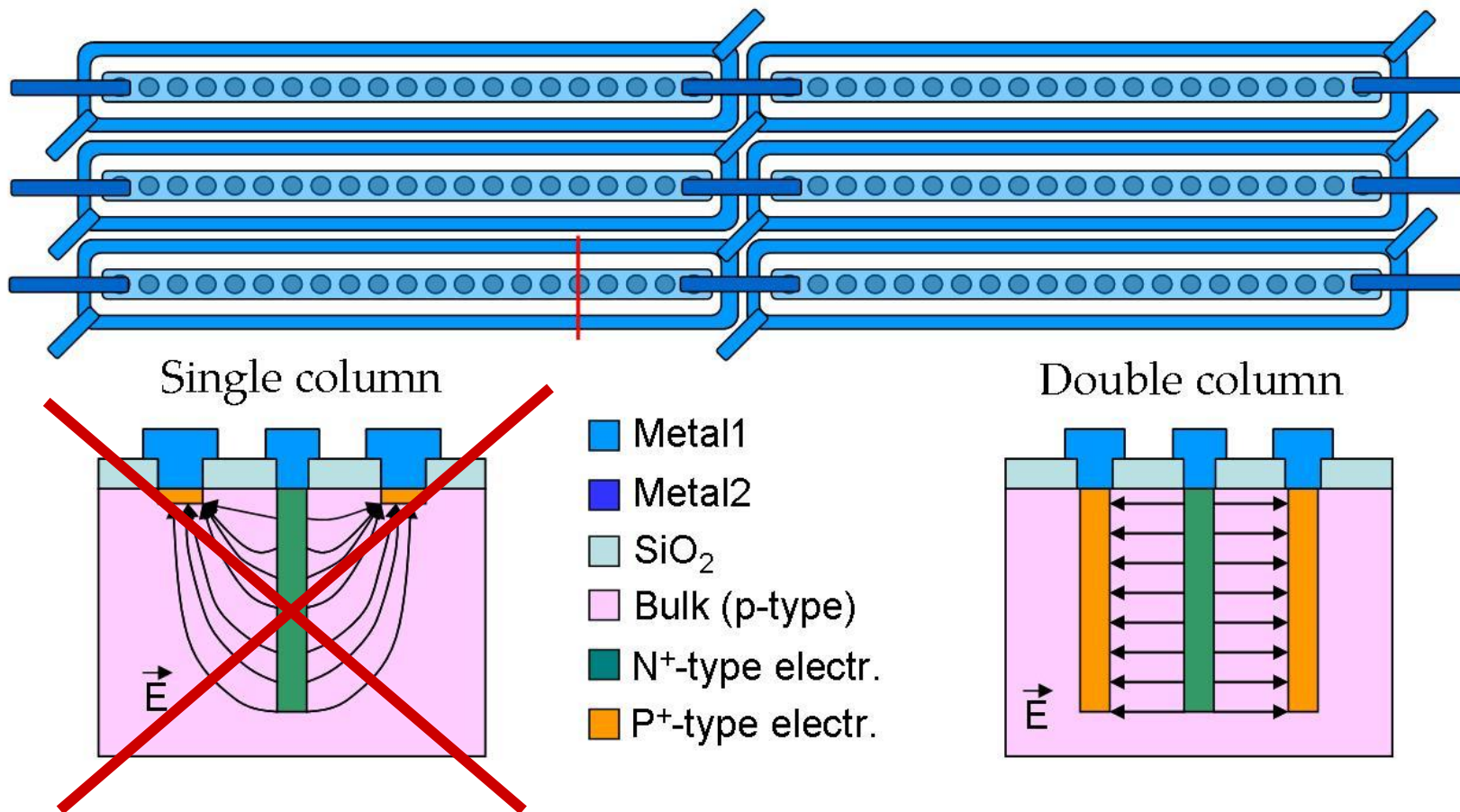
Stripixel concept: planar stripixel* at the BNL

* Z.Li, *Novel silicon stripixel detector: concept, simulation, design, and fabrication*, Nucl. Instr. and Meth. A 518 (2004) 738-753.



Stripixel concept: 3D stripixel* (BNL +IMB-CNM)

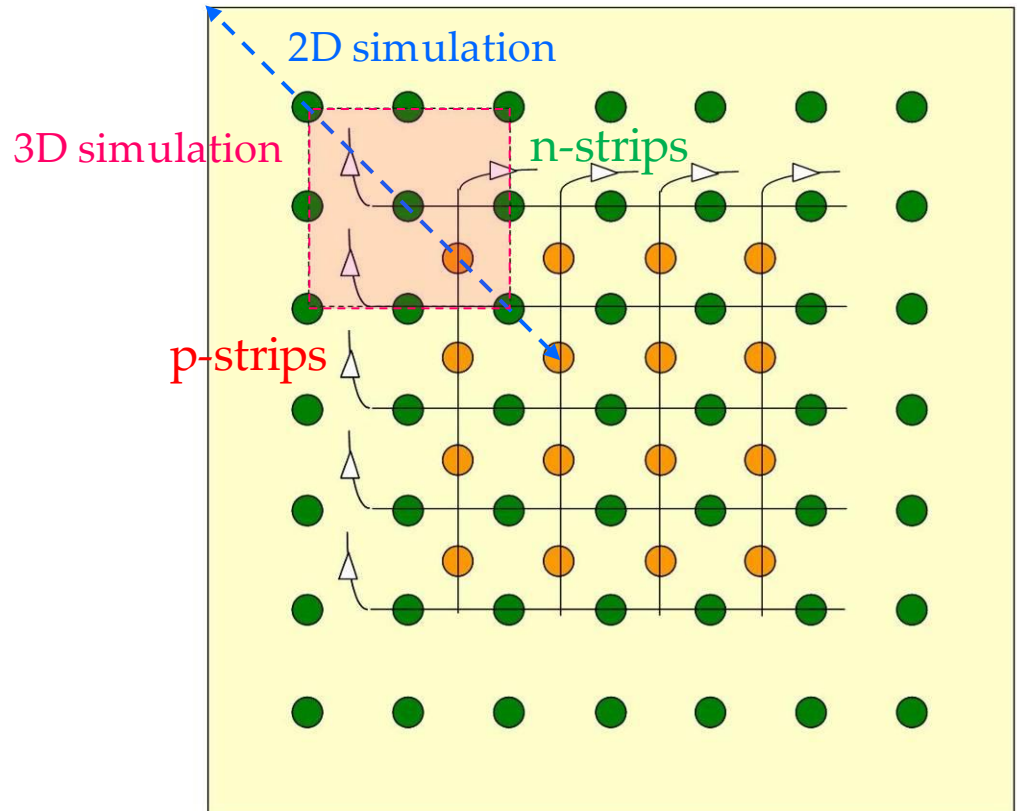
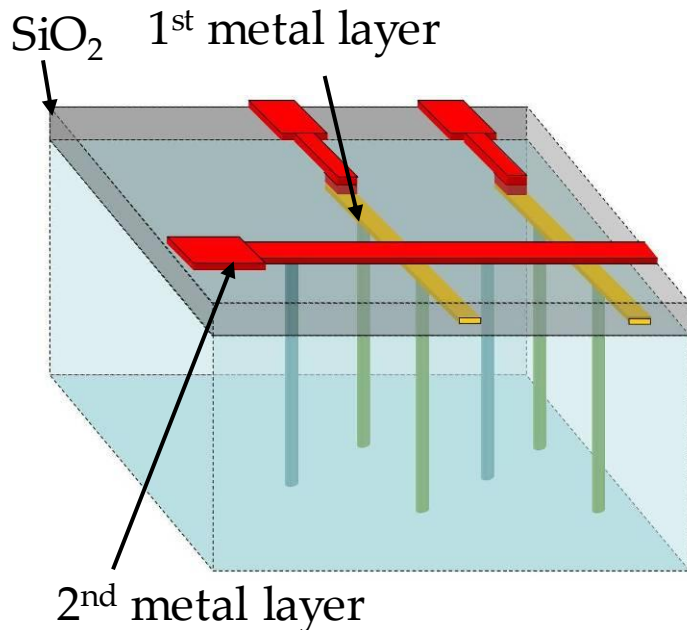
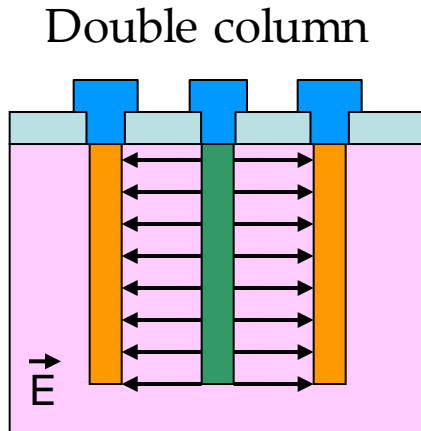
* Z.Li et al., *Development, simulation and processing of new 3D detectors*, Nucl. Instr. and Meth. A 583 (2007) 139-148.



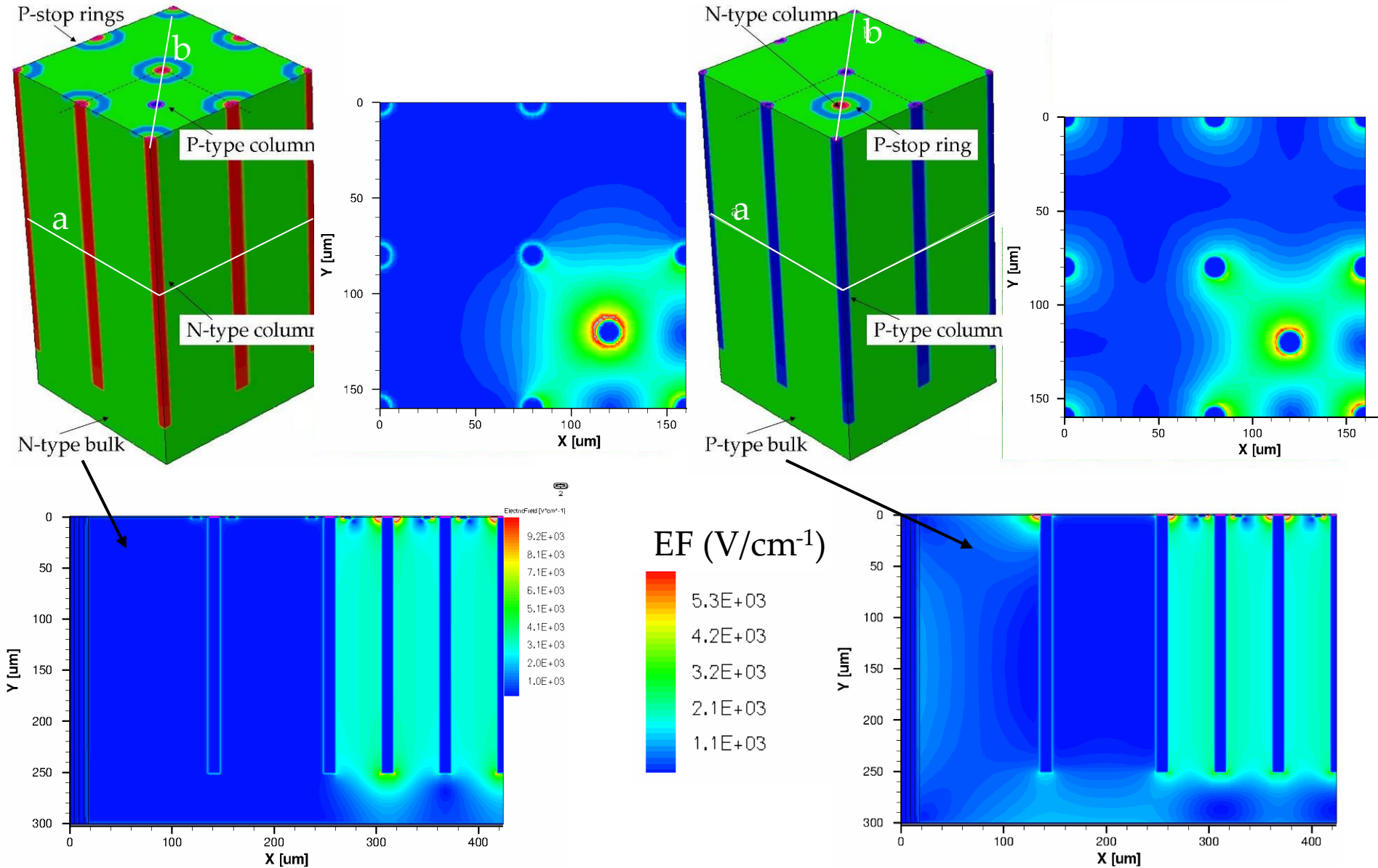
1.1 New 3D-Stripixel prototype generation: a single-side double-strip detector

2D position sensitive

- Single sided
- Double columns (**n-columns** connected as **n-strips**
p-columns connected as **p-strips**)
- Dual metal technology
- **n-strips** collect e's, **p-strips** collect h's



1.2 Optimization of the design: Sentaurus simulations



Edge defects model from E.P. Nochis et al, Nucl. Instr. and Meth. A 574(3) (2007) 420–424.

Real prototypes

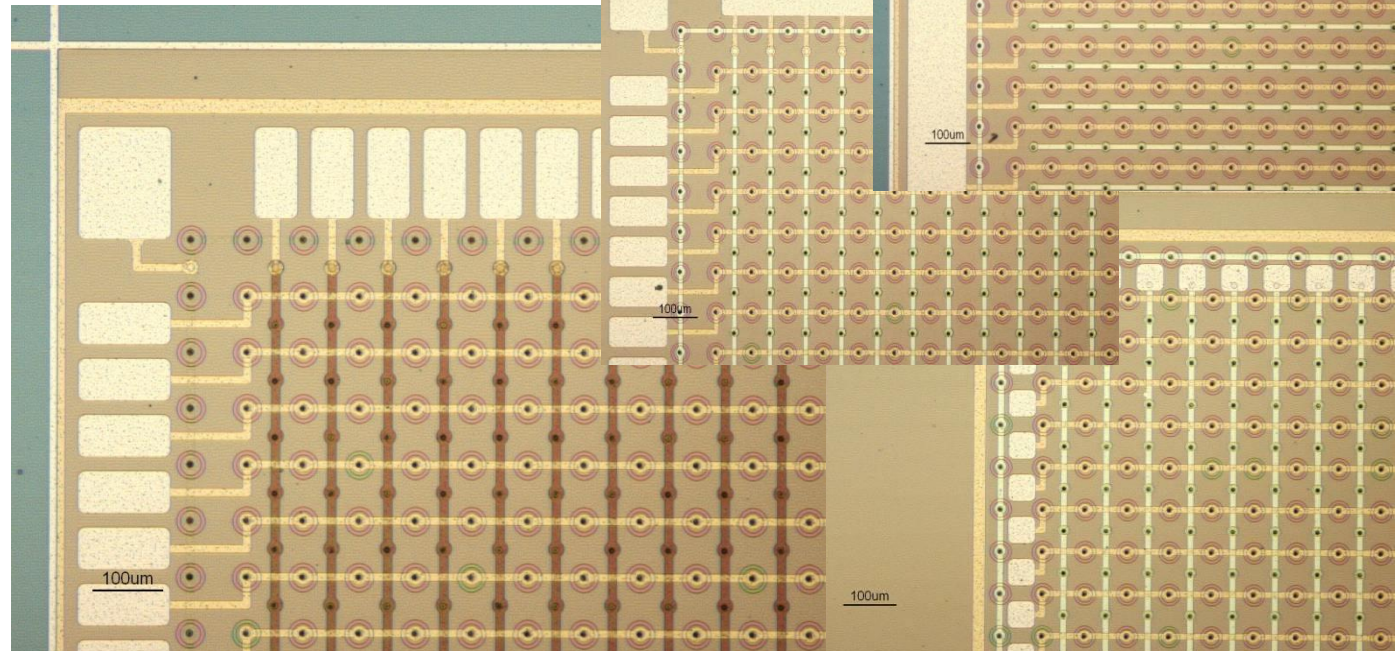
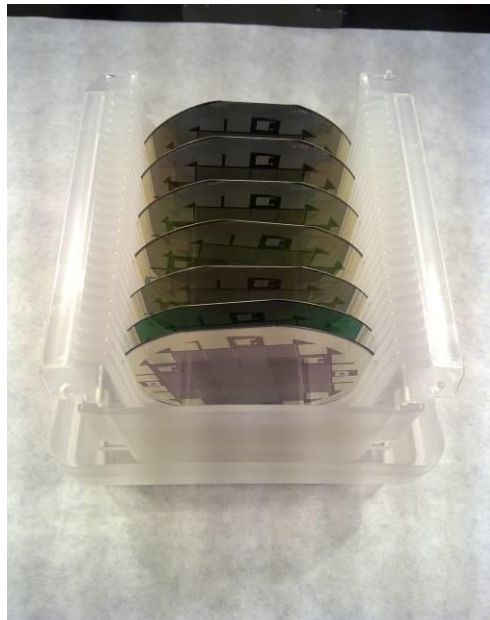
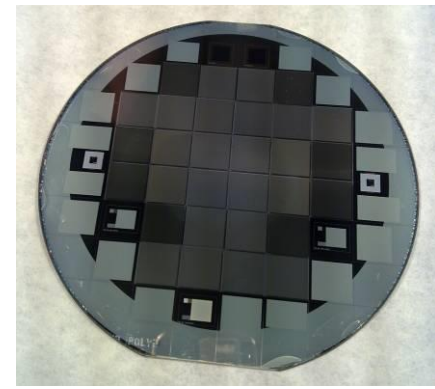
6 wafers 300 μm thick, 1 wafer SOI 20 μm thick

Detector structures:

- 127 p+-type strips and 128 n+-type strips
- pitch 80 or 160 μm , double metal or polysilicon
- pitch 80 or 160 μm , double metal, edgeless

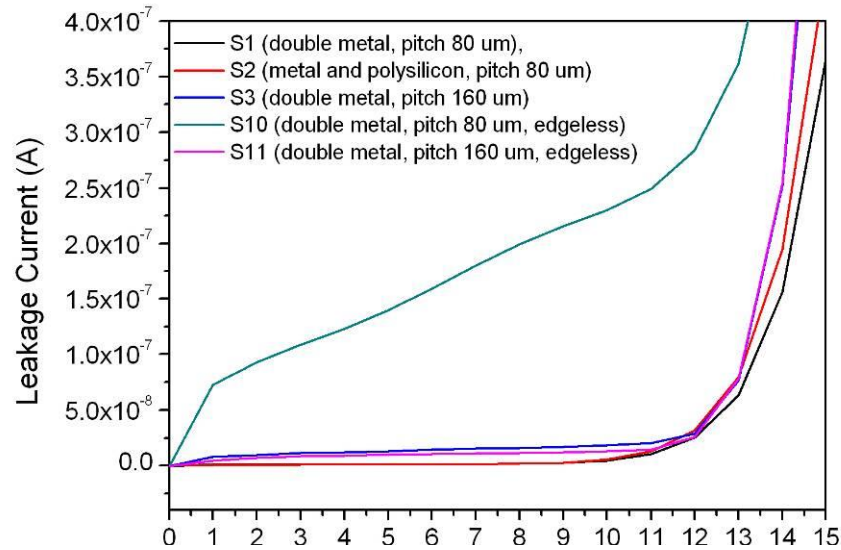
Test structures:

- 1D microstrip detectors (n or p strips shorted)



Part II:
Detector Characteristics
Measurements

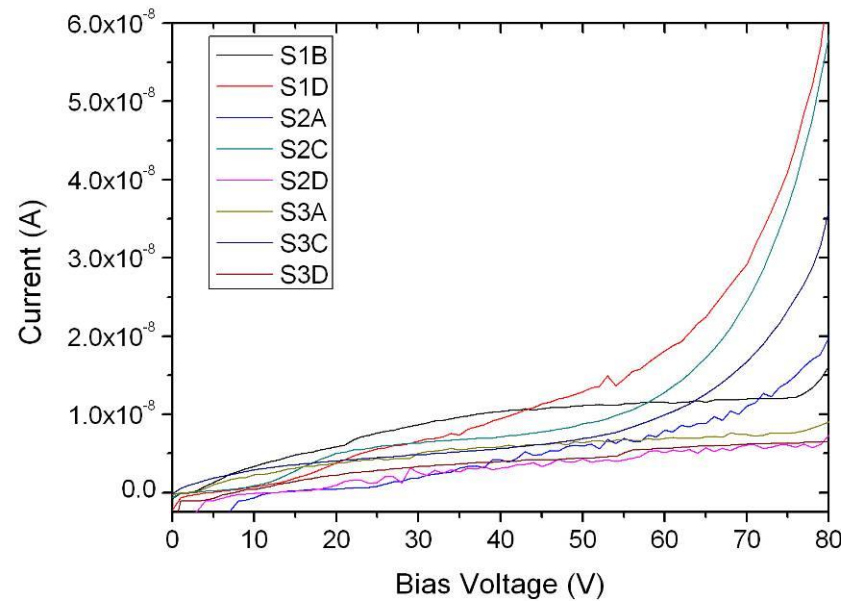
2.1 Electrical characterization



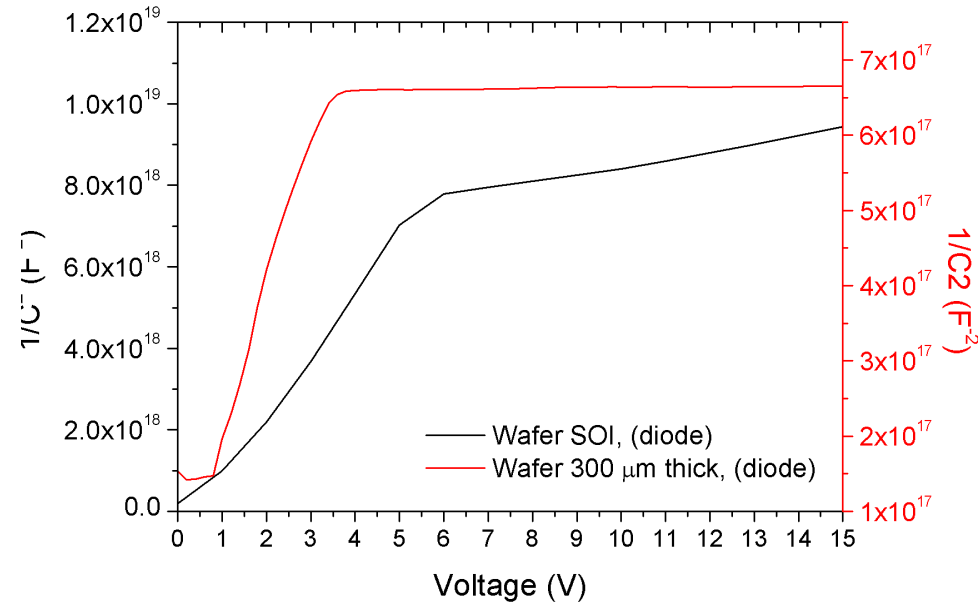
$I_{\text{leakage}} \sim 102 \text{ nA}$ (wafer 300 μm)
 $\sim \text{few nA}$ (wafer SOI)

$C_{\text{bulk}} \sim 8 \text{ pF}$ (wafer 300 μm)
 $\sim 0.4 \text{ pF}$ (wafer SOI, 20 μm)

$V_{\text{dep}} \sim 3.5 \text{ V}$ wafer 300 μm thick
 $\sim 5.5 \text{ V}$ wafer SOI, 20 μm thick

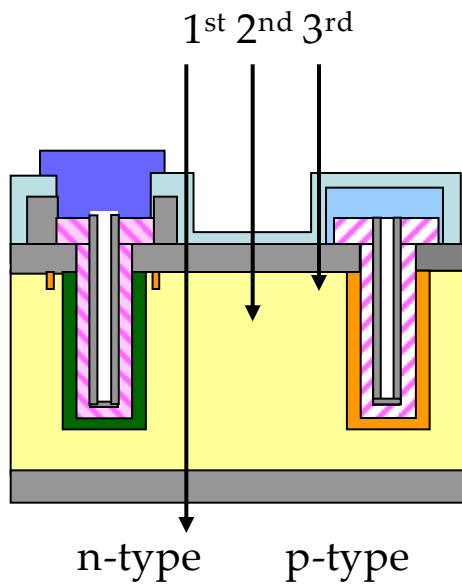


Diode structure



2.2 TCT measurements: setup

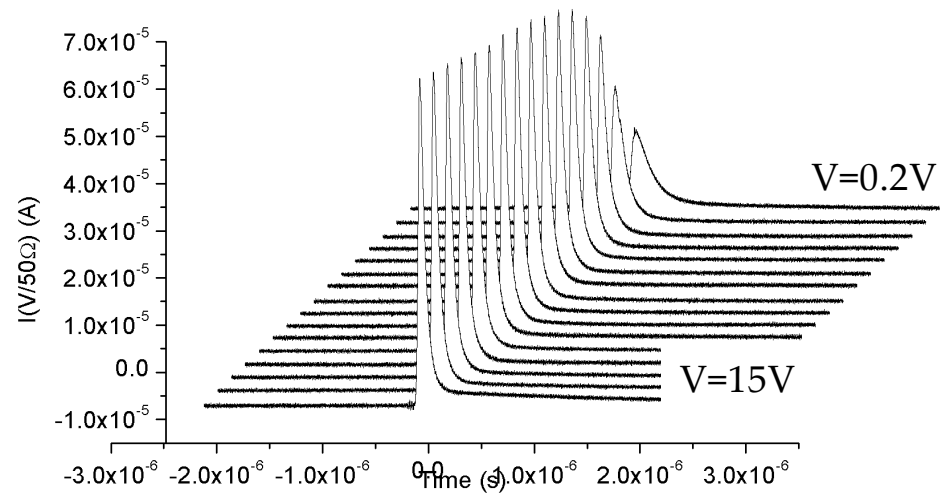
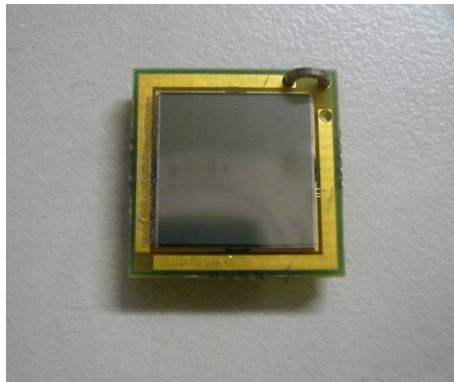
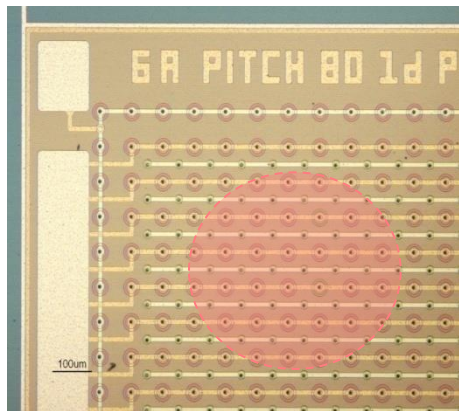
Carried out in the BNL (NY, USA) laboratories



Laser setup	Wavelength λ (nm)	Intensity (V)	Width (ns)	Period (μ s)	Penetration depth (μ m)
1 st	1060	10	10	20	> 300 μ m
2 nd	830	10	10	20	15
3 rd	660	10	10	20	5

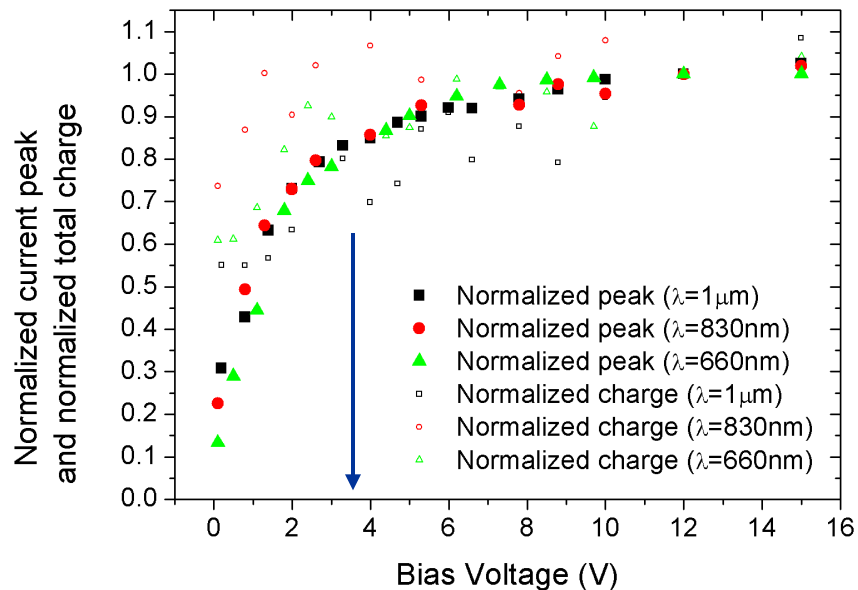
Laser beam (spot = 1mm)

Parallel strips

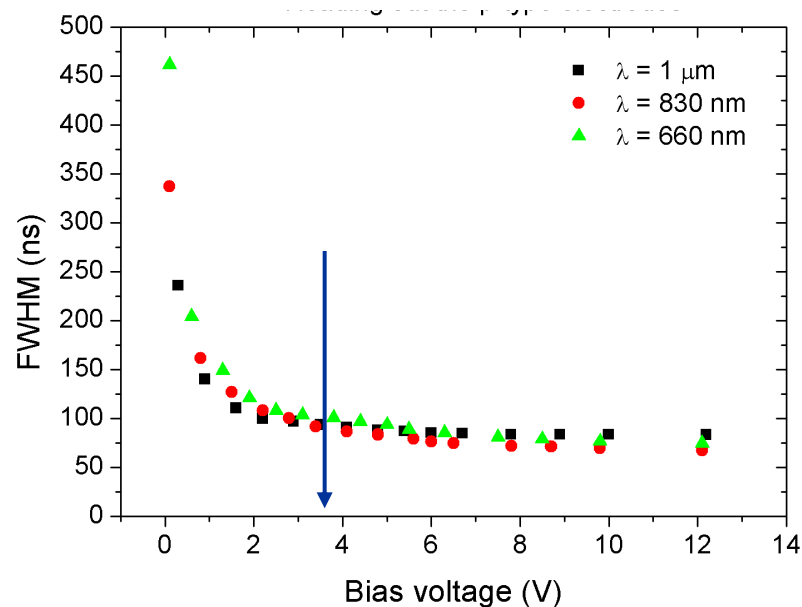
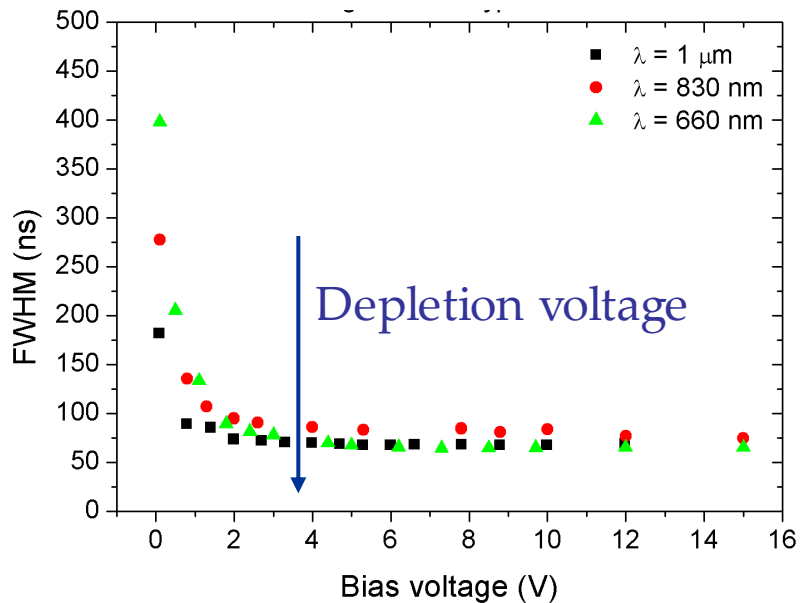
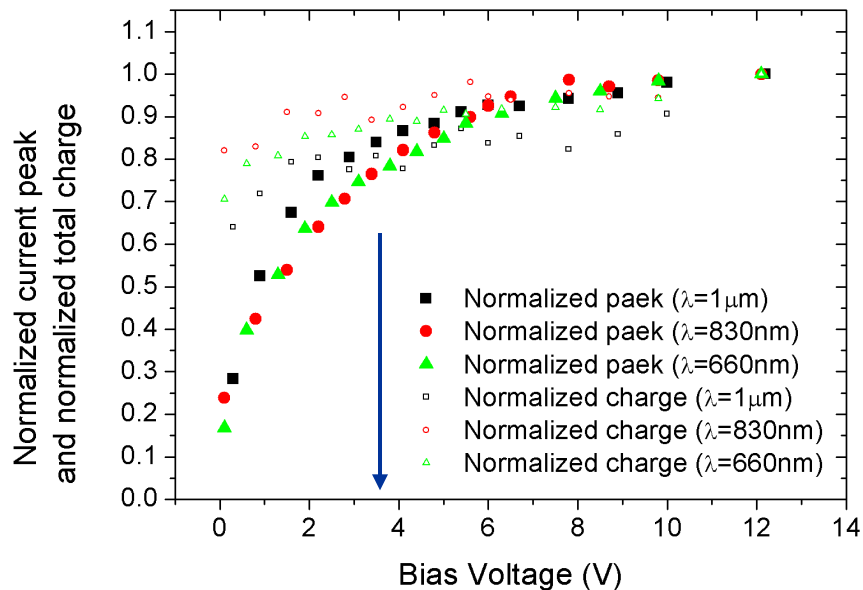


2.2 TCT measurements: laser from the front side

Reading out the n-type electrodes



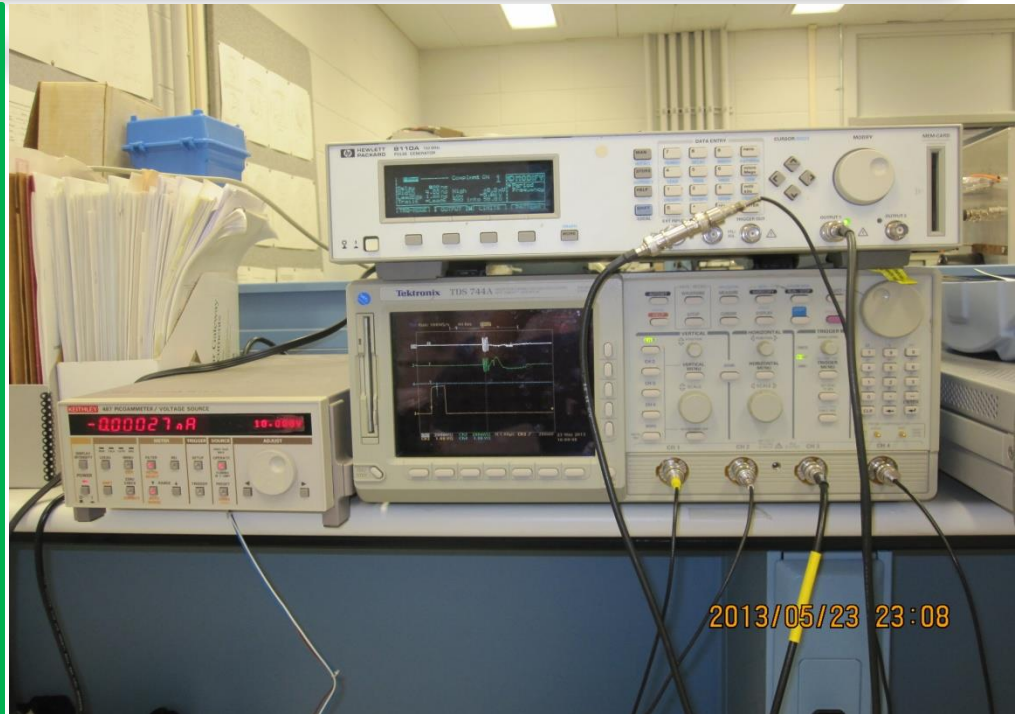
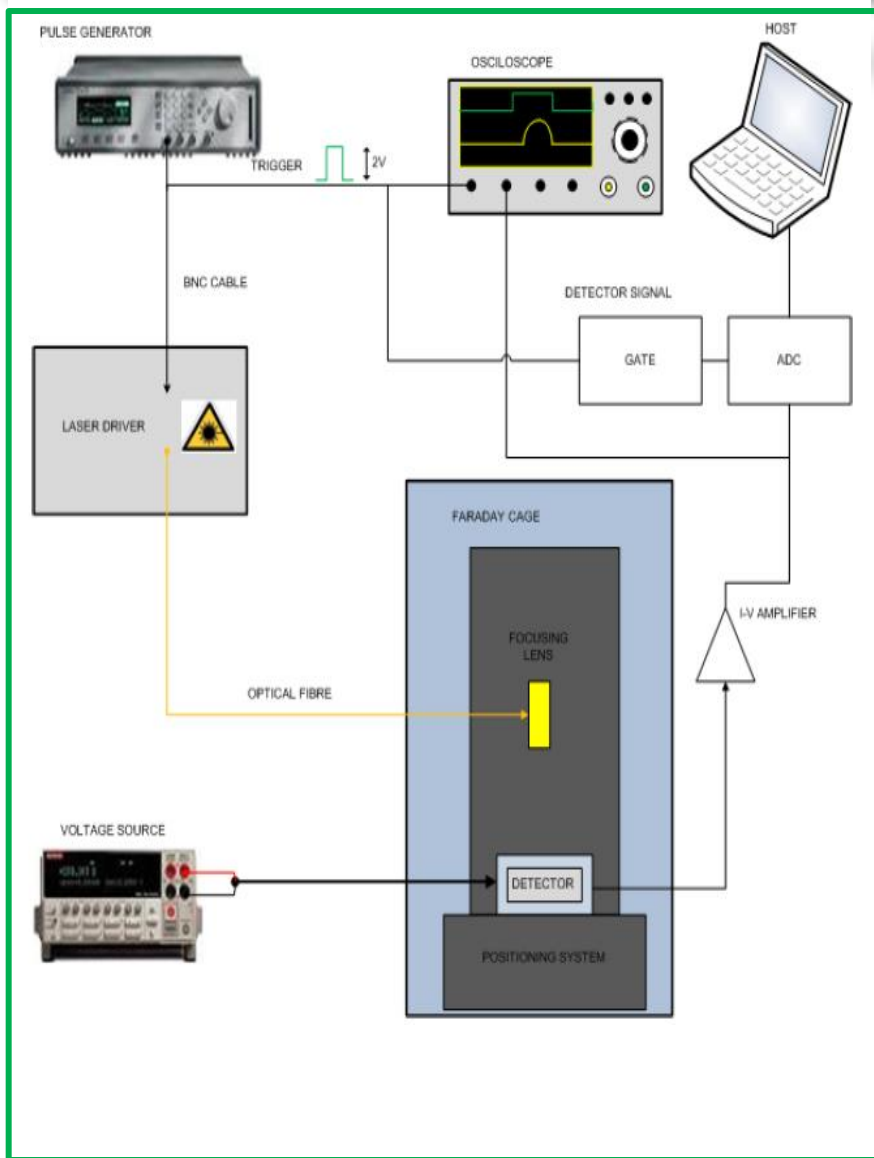
Reading out the p-type electrodes



2.3 Detector Responses to Laser Measurements With Alibava DAQ System

- **System Introduction**
- **CCE measurement**
- **2D position sensitivity**

System Introduction



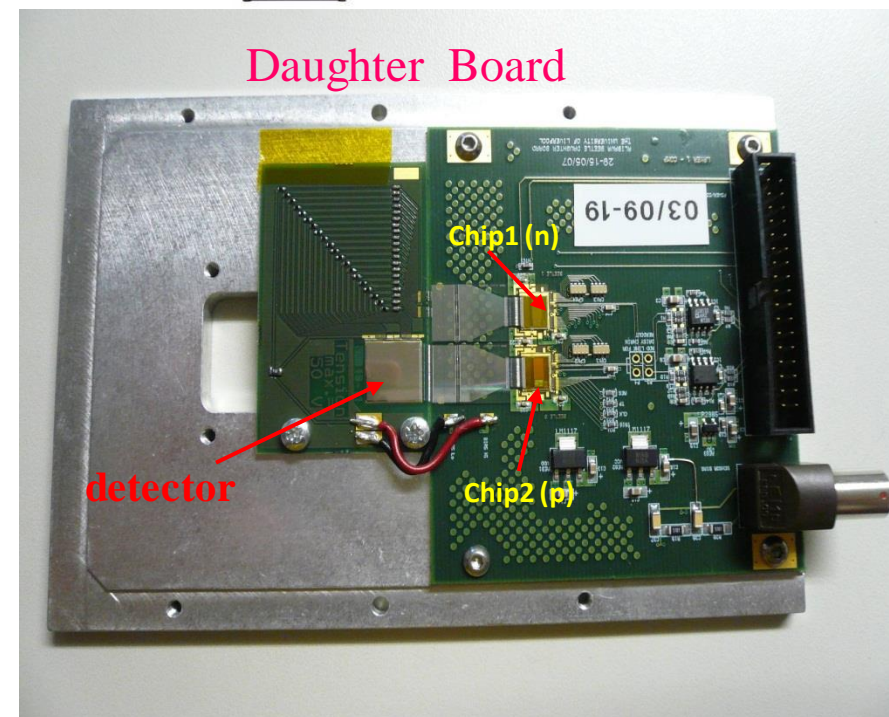
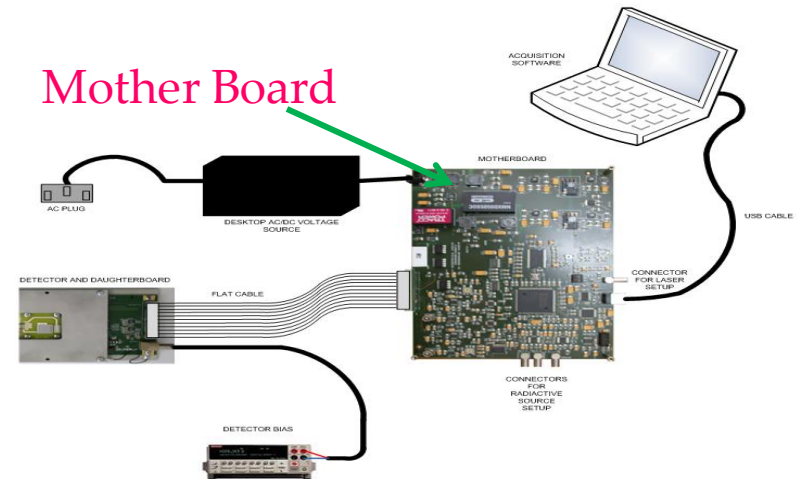
Hardware Parts-A dual board system

➤ Mother Board:

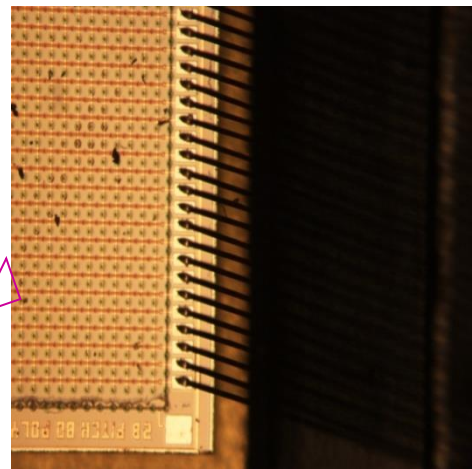
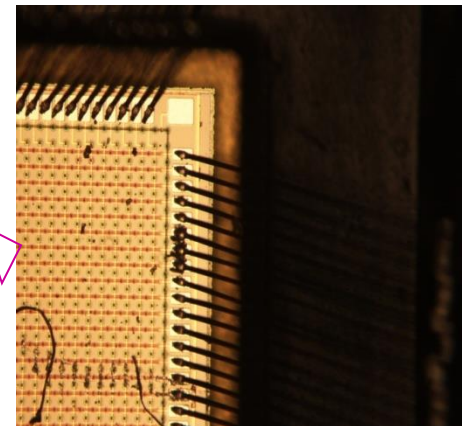
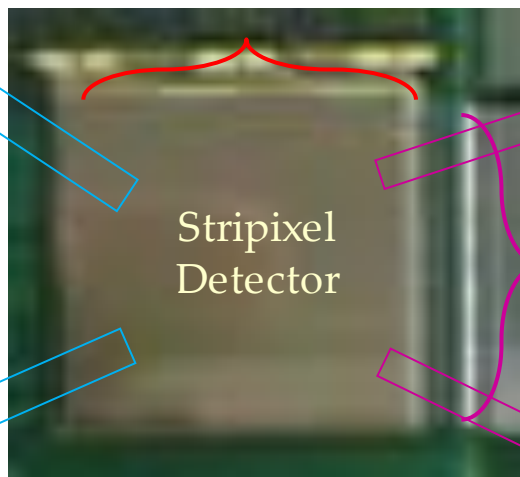
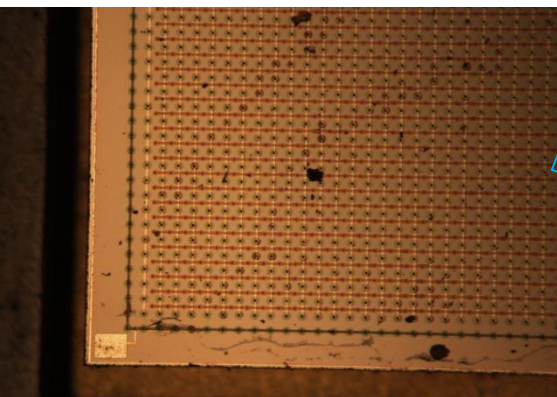
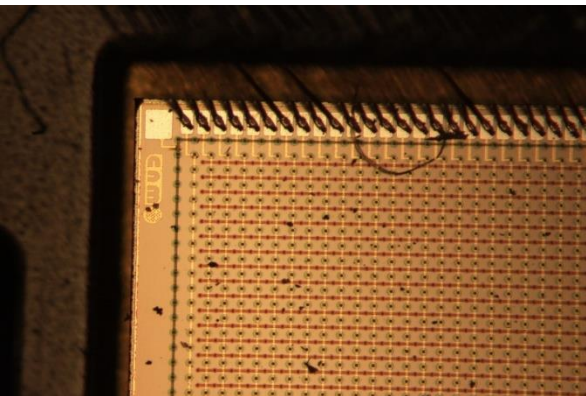
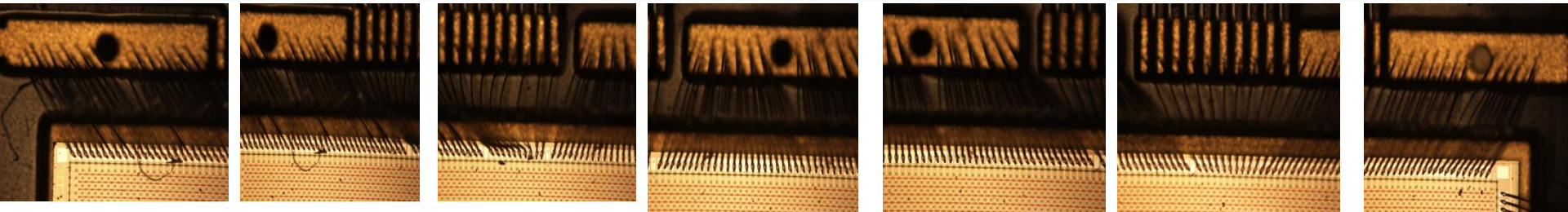
- Analogue data processing
- Trigger output generation
- Hardware part control
- USB communication

➤ Daughter board

- Two Beetle readout chips
- Fan-ins and detector support to interface the detectors

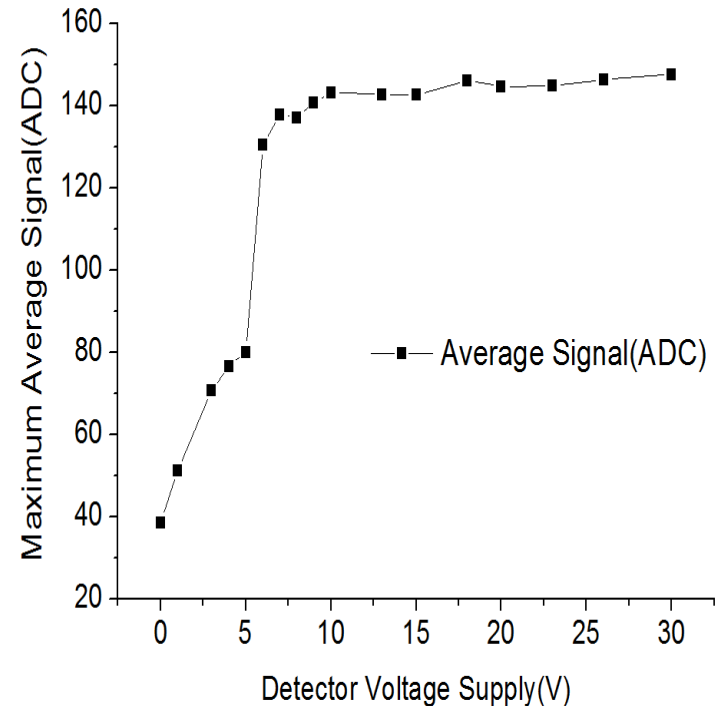
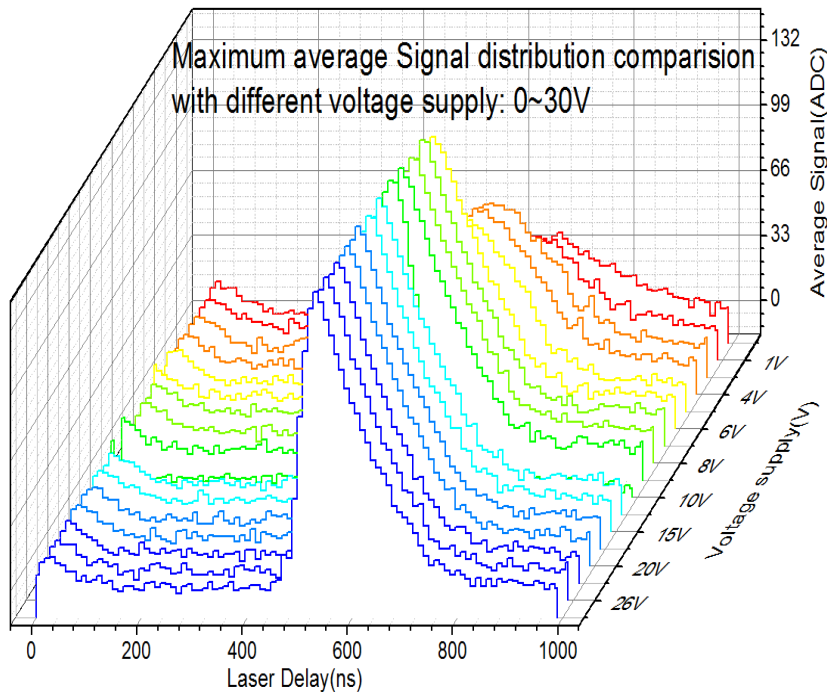


Hardware Parts-Stripixel Detector Bonding



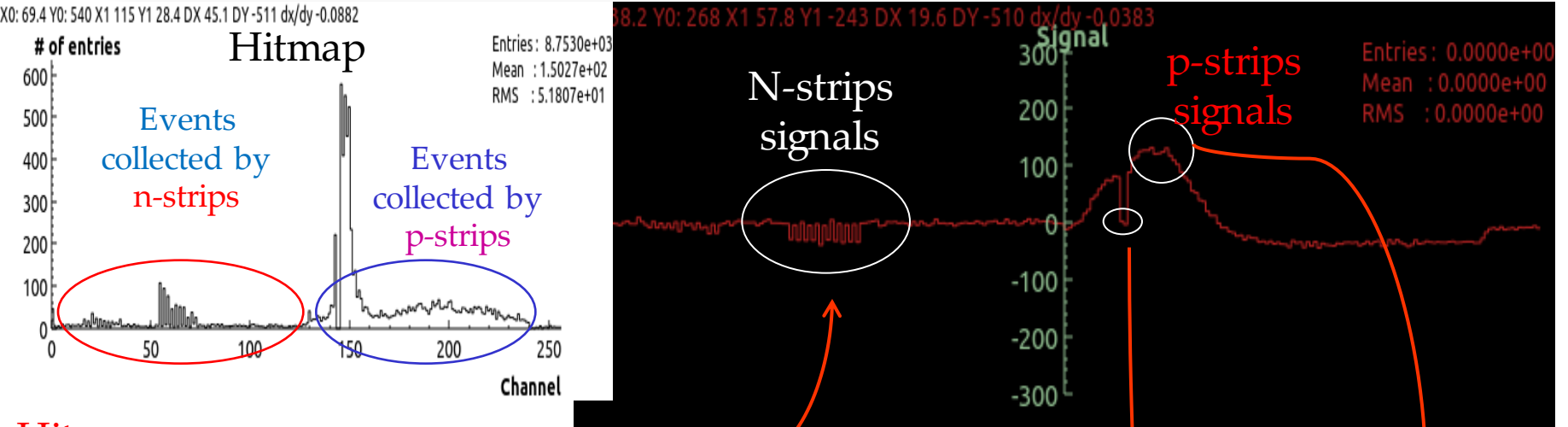
CCE Characteristics

Detector CCEs distributions vis detector voltage supply



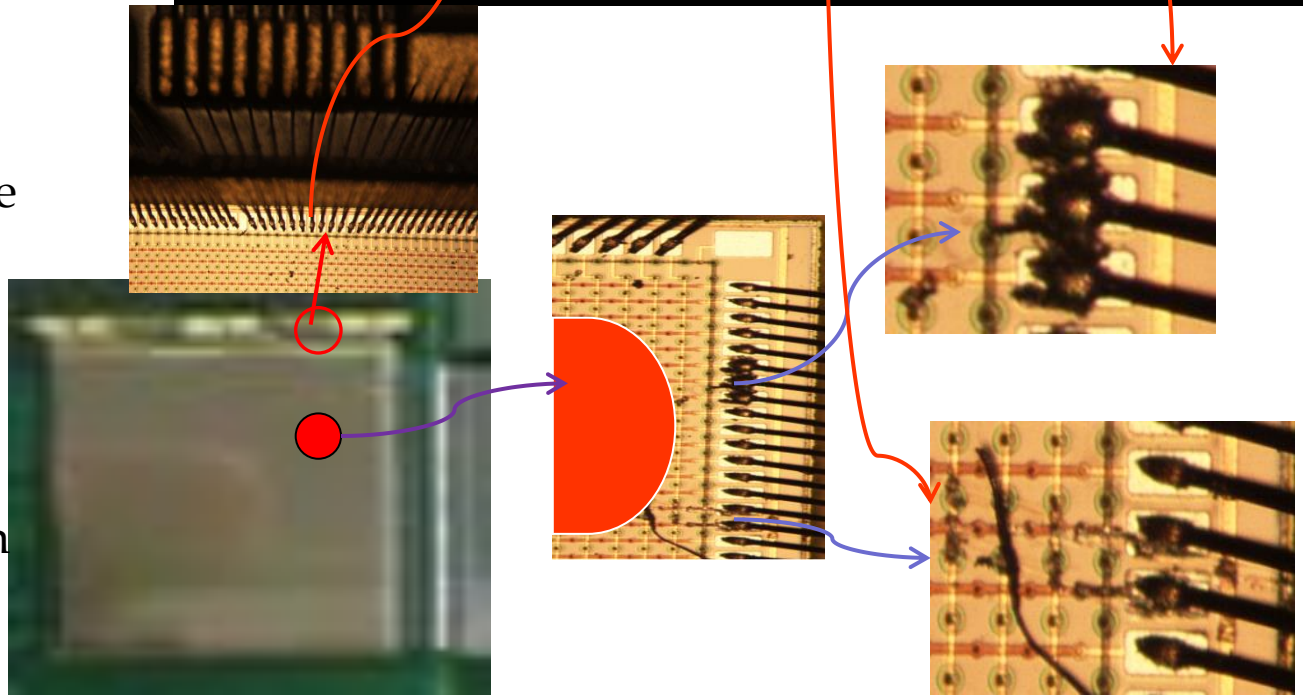
$$V_{\text{Bias}} \approx 0.5 * V_{\text{(Detector Voltage Supply)}} \longrightarrow V_{\text{dep.}} \approx 5V$$

2D Position Sensitivity



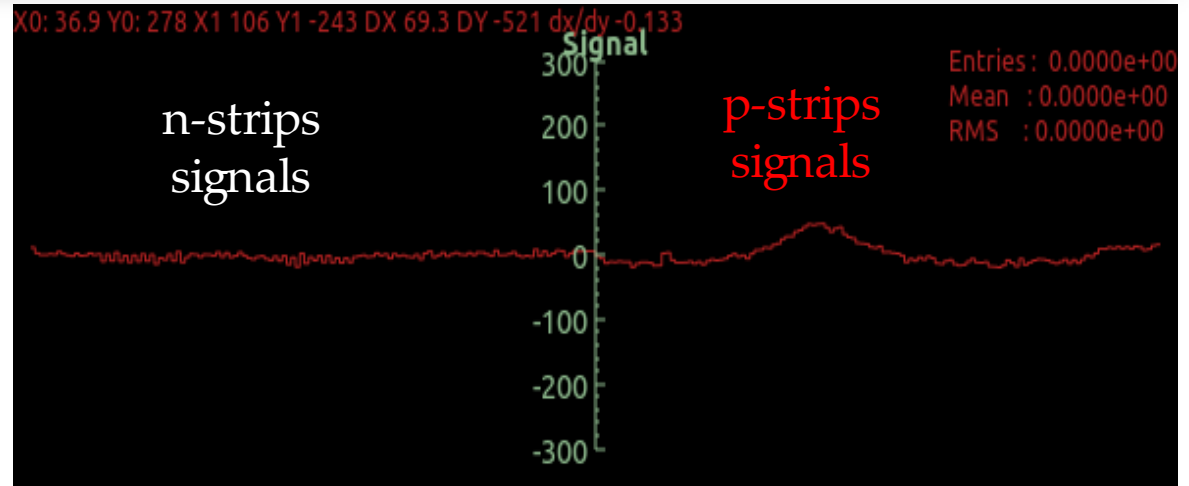
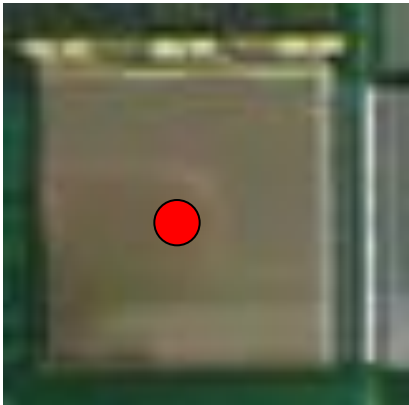
Hitmap:
 distribution of laser exciting events are collected by n or p type strips detected by front-end readout chips .

Event Signal:
 Amplitude distribution of event signal(ADC) for one laser injection.

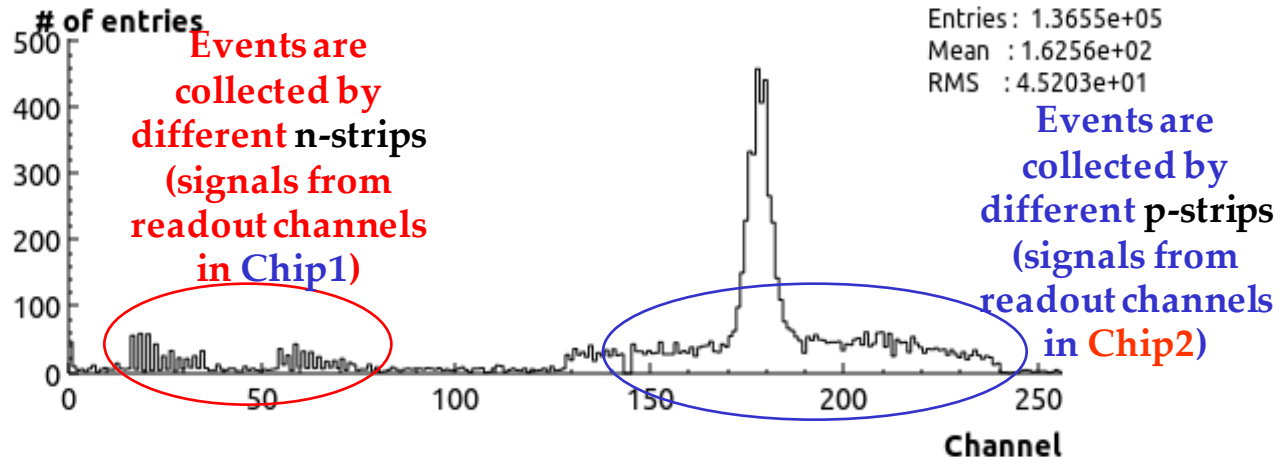


2D Position Sensitivity

**Laser injection
position :**
**near detector
center**

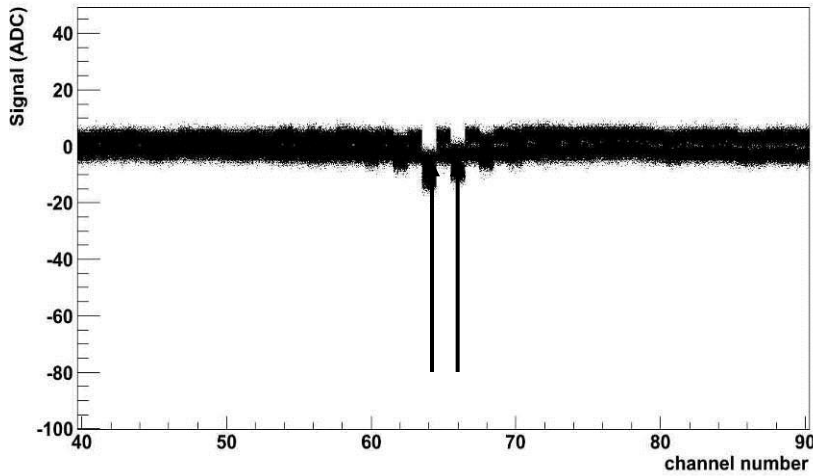


X0: 62.7 Y0: 377 X1 43.5 Y1 280 DX -19.2 DY -96.7 dx/dy 0.198

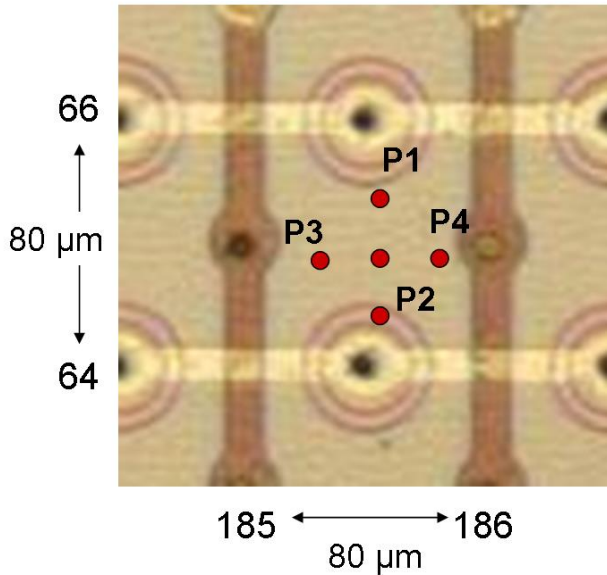
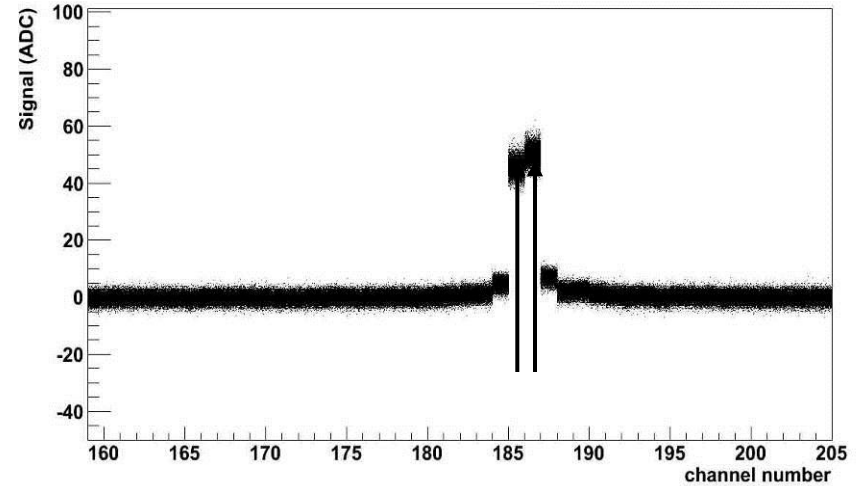


Data with more-focus laser ($5\ \mu\text{m}$ spot, $\lambda=1060\ \text{nm}$), Measurements done in CNM with the same detector and daughter board

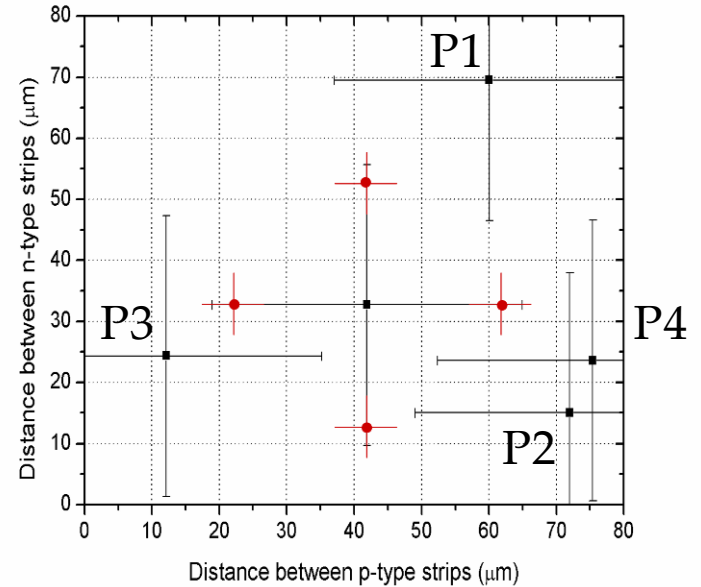
Signal vs n-type channels



Signal vs p-type channels



Spatial resolution
 $\text{pitch}/\sqrt{12}$
 $\sim 23\ \mu\text{m}$



2D sub-pixel position resolution have been seen

Part III: Summary

With BNL TCT , Alibava DAQ systems and other semiconductor detector measurement instruments, the key parameters including the depletion voltage, leakage current, and CCE characteristics of the newly designed and fabricated full 3D stripixel detectors are successfully tested.

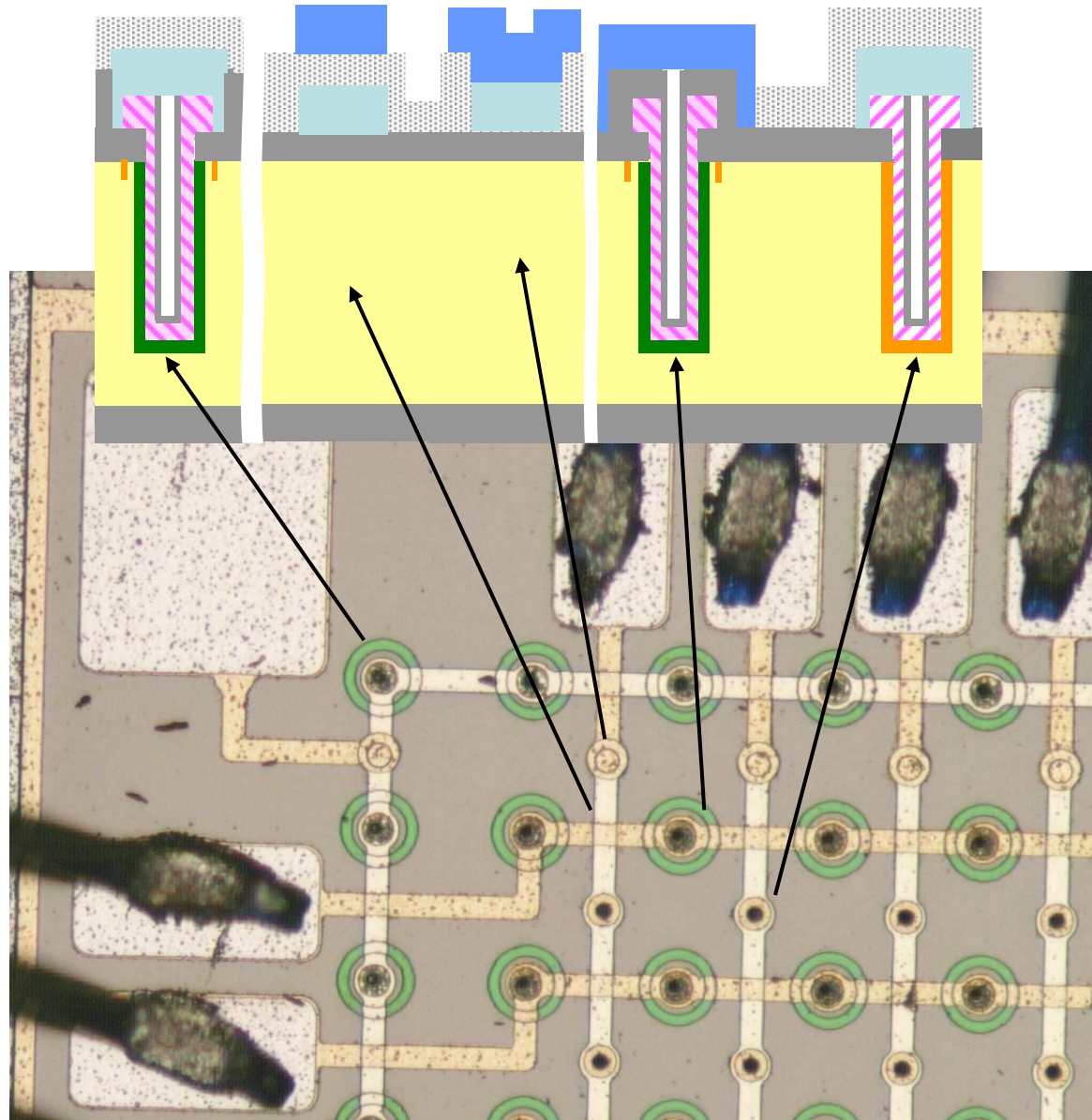
(1) The full depletion voltage of the 3D stripixel detectors is about 3-4 volts from CV and TCT measurements, and about 5 volts from ALIBAVA CCE measurement

(2) 2D position sensitivity of laser illumination have been detected from both TCT and ALIBABA CCE measurements

(3) Measurements using a laser with a more-focused spot have shown 2D sub-pixel position resolution

1.3 Fabrication process: double metal technology

- Si
- Poly1
- Poly2
- p-type diff.
- n-type diff.
- SiO₂
- SiO₂
- Metal 1
- Metal 2



1.3 Fabrication process:

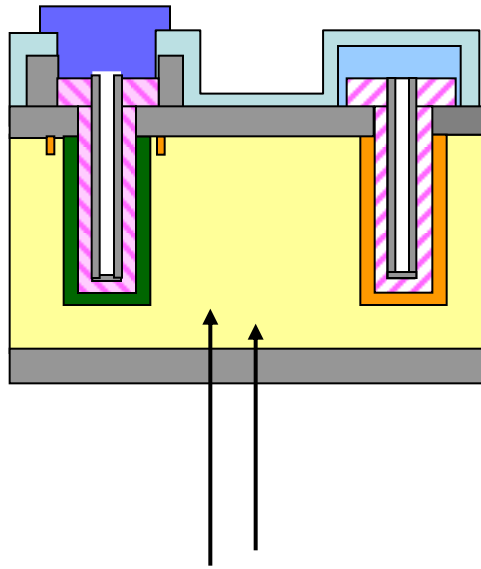
the most challenging ever carried out at IMB-CNM for silicon particle detectors

100 steps, 10 photolithography process

2 deep RIE processes and 2 metallization processes on the same surface

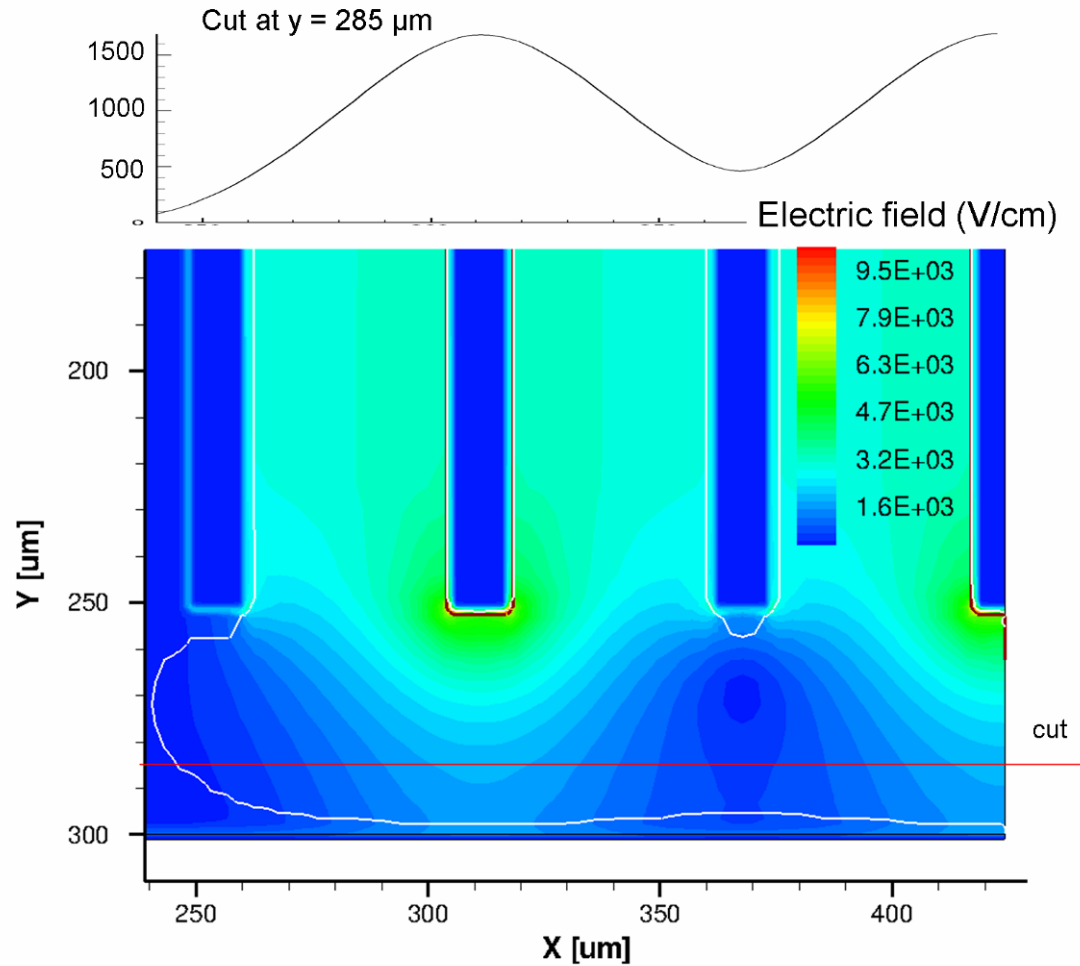
Mask level	Pattern	Comments
P-DIFF	p-stop	(p-stop rings definition around n-type columns)
N-HOLES	n+ columns	(n+ columns etching)
POLY	polysilicon	(definition of the n+ columns on the surface)
P-HOLES	p+ columns	(p+ columns etching)
POLY2	polysilicon	(definition of the p+ columns on the surface)
WINDOW	metal window	(removing SiO ₂ on the guard ring columns and p-type columns to contact with the metal layer)
METAL1	metal (1 st layer)	(1 st metal layer pattern definition)
VIA	metal2	(window opening connection between two metal layers and removing SiO ₂ on the n-type columns)
METAL2	metal (2 nd layer)	(2 nd metal layer pattern definition)
PASSIV	metal contacts	(opening windows on the metal connection pads)
BACK		
WINDOW	SOI wafer	(etching the support substrate up to SOI wafers)

2.2 TCT measurements: laser from the back side

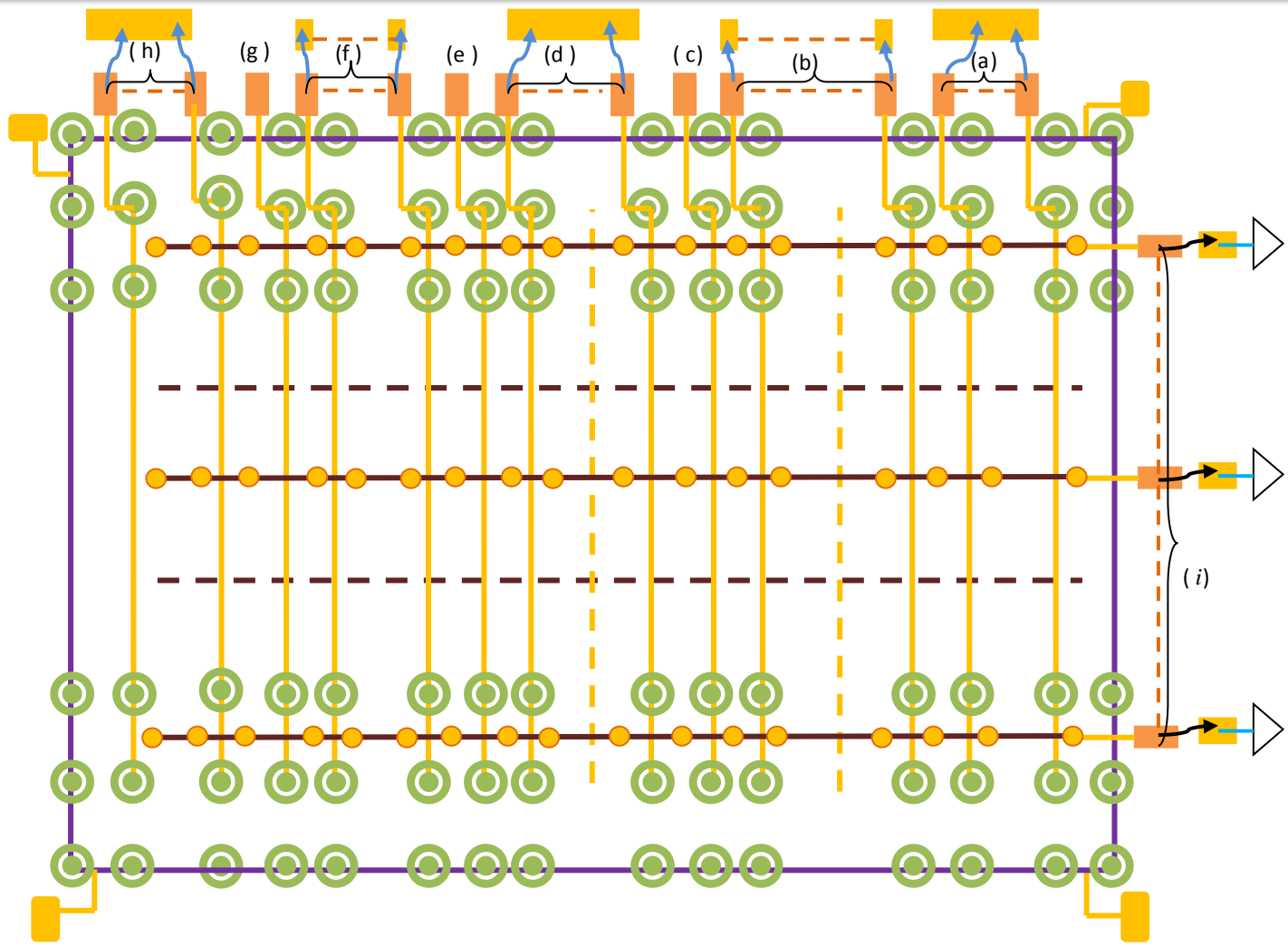


Illuminating the backside with both the lasers of shorter wavelengths (660 nm and 830 nm) no signal can be read.

Low Electric Field



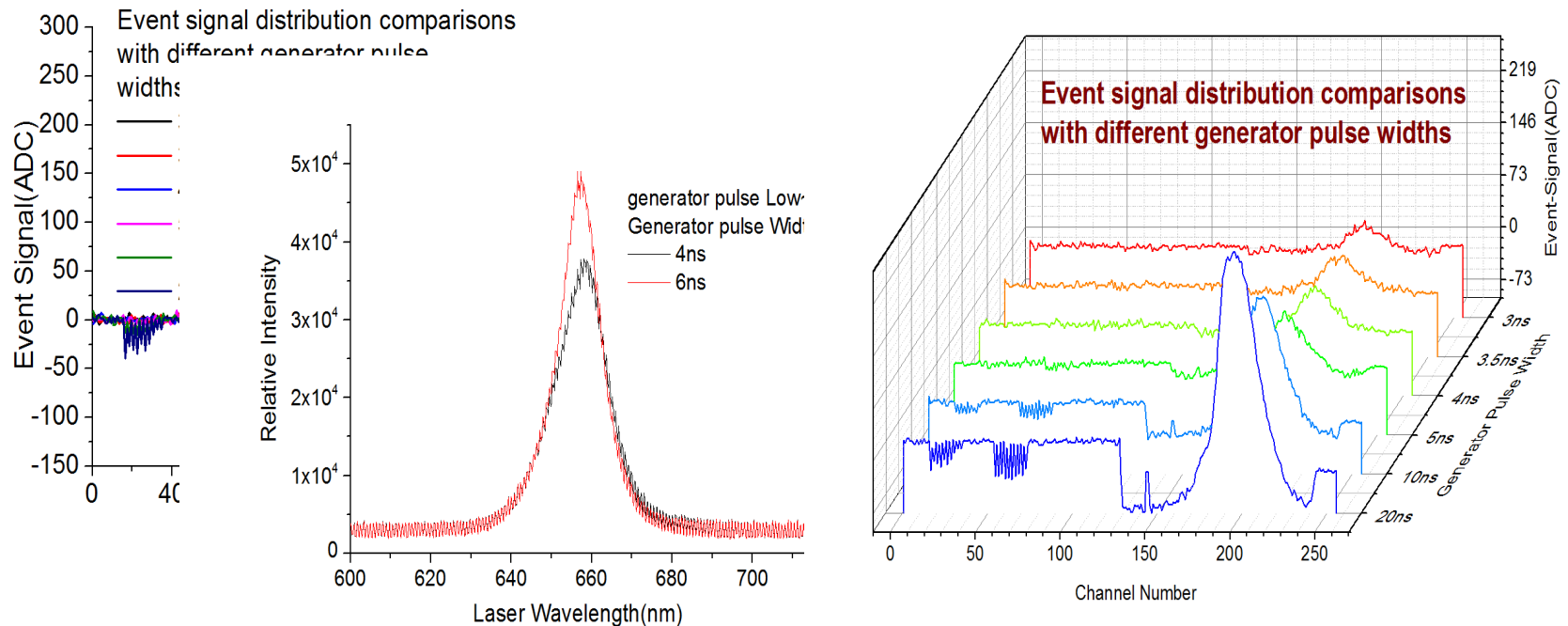
Hardware Parts-Stripixel Detector Bonding



Notes: (1) Ranges of **n-type** strips in (a), (d) and (h) are bonded together to each one block metal respectively, every column of **n-strips** in other ranges((b),(d)and (f)) is respectively bonded to one metal. 3 columns of n-strips ((c),(e)and (g)) are not bonded. (2) each row of **p-type** strips is respectively bonded to each metal which is connected directly to each one readout channels in Chip2.

CCE Characteristics

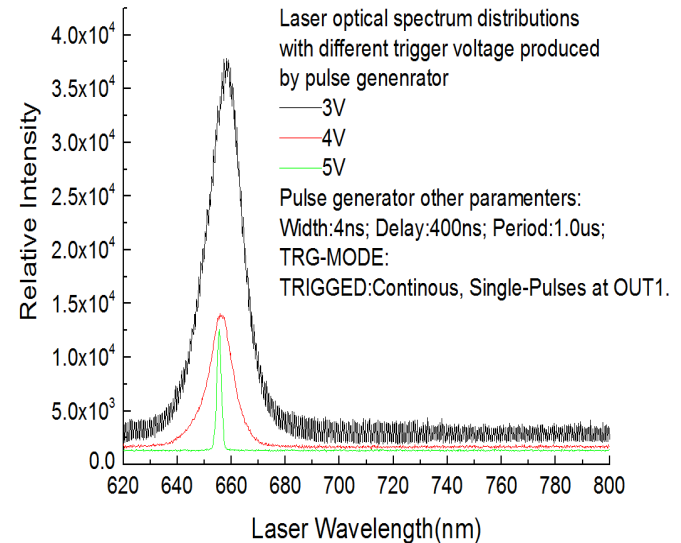
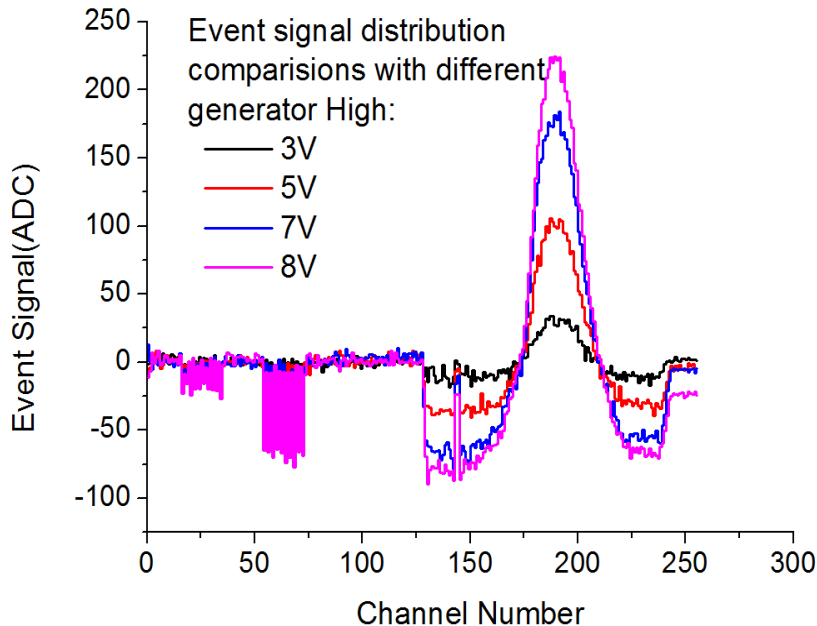
- CCE Distributions vis generator pulse Widths



Detector Channels CCEs are increased with generator pulse signal's being Wider; As Laser Intensity is Increased with Generator Pulse signal Widths Being Wider

CCE Characteristics

- CCE distributions via generator pulse **Highs**:



Detector **CCEs** Changed with pulse signal 's **High->laser spectrum distributions and intensity modification**