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The VFAT production test for the TOTEM experiment.

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VFAT is the front-end ASIC designed for the charge readout of silicon and gas detectors within the TOTEM experiment of the LHC.

A stand alone portable Totem Test Platform (TTP) with USB interface has been developed for the systematic testing of the TOTEM hybrids equipped with VFAT chips. This paper is divided into 3 sections; the first describes the hardware features of the TTP, the second describes the software routines for the control and systematic testing of VFATs, the third presents the production test results including yield.

Author: Dr ASPELL, Paul (CERN)

Co-authors: Mr ROUET, James (C4I); Mr KASPAR, Jan (CERN); Mr KOPAL, Josef (CERN); Mr PETÄJÄJÄRVI, Juha (CERN); Dr VICHOUDIS, Paschalis (CERN); Ms AVATI, Valentina (CERN); Dr SNOEYS, Walter (CERN); Dr BIALAS, Wojciech (CERN)

Presenter: Dr ASPELL, Paul (CERN)

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