

Architecture of the readout electronics for the ATLAS upgraded tracker

Thursday 18 September 2008 11:50 (25 minutes)

The upgrade of luminosity of the LHC (SLHC) will necessitate changing the ATLAS tracker. The new fully silicon tracker will contain pixel layers, short-strip layers and long-strip layers. The silicon strip detector will be organized in staves or super-modules. The readout electronics will follow this organization and be based on front-end ICs, module controllers and super-module controllers. This presentation will describe the proposed readout architecture, the different options for powering the front-end electronics and for controlling the detector, as well as the on-going developments.

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Session Classification: TOPICAL 1 - LHC Upgrades