

TWEPP-08 Topical Workshop on Electronics for Particle Physics

Thursday, 18 September 2008

POSTERS SESSION (16:15 - 18:15)

[id] title	presenter	board
[111] A GOL Based Optical Demo Link to Study System Issues for the ATLAS Inner Detector Readout Upgrade	Prof. YE, Jingbo	
[112] Detector noise susceptibility issues for the future generation of High Energy Physics Experiments	Dr ARTECHE, Fernando	
[63] Operational Experience With The SCT Optical Links	Dr WEIDBERG, Anthony	
[119] Low Power Multi_dynamics Front End for the Optical Module of a neutrino underwater telescope	Dr LO PRESTI, Domenico	
[86] A Radiation Tolerant Current Reference Circuit in a standard 0.13um CMOS Technology.	GROMOV, Vladimir	
[2] Implementation of the control and supervision of ALICE ZDC positioning systems	Mr SWOBODA, Detlef	
[117] Development of a fast readout system for DEPFET sensors	Mr KOCH, Manuel	
[127] Distributed Low Voltage System for the FrontEnd electronics of the HADES RPC TOF wall	Mr GIL-ORTIZ, Alejandro	
[70] LLRF electronics for the CNAO synchrotron	Mr VESCOVI, Christophe	
[38] Instrumentation for Gate Current Noise Measurements on sub-100 nm MOS Transistors	GAIONI, Luigi	
[65] Completion of the CMS Muon Barrel Alignment System and its integration into the CMS detector environment.	Dr SZÉKELY, Géza	
[41] Control, test and monitoring software framework for the ATLAS Level-1 Calorimeter Trigger	Dr LANDON, Murrough	
[96] Grounding, Shielding and Cooling Issues on LHCb electronics at the LHC pit 8.	Dr LACARRERE, Daniel	
[34] Digital part of PARISROC: a photomultiplier array readout chip	Mr DULUCQ, FREDERIC	
[99] DC- DC Power Conversion with Voltage Ratios > 10 for LHC Upgrade Detectors DC- DC Power Conversion with Voltage Ratios > 10 for LHC Upgrade Detectors	Dr DHAWAN, Satish	
[90] A dual scale 1mW full flash ADC for the ILC vertex detector	BOHNER, gerard	
[10] Electronics of LHCb calorimeter monitoring system	Dr KONOPLYANNIKOV, Anatoli	
[32] The Common Infrastructure Control of the Atlas experiment	Mr GUTZWILLER, Olivier	
[5] ATLAS Level-1 Level-2 Trigger Integration Commissioning	Dr ZHANG, Jinlong	
[27] Mezzanine Cards for the EMU CSC System Upgrade at the CMS	Mr MATVEEV, Mikhail	
[58] Testing and calibrating analogue inputs to the ATLAS Level-1 Calorimeter Trigger	Dr STAMEN, Rainer	
[28] Detector Control System for the electromagnetic calorimeter in the CMS experiment – summary of the first operational experience.	DI CALAFIORI, Diogo	

[42] The TOTEM Roman Pot Motherboard	Mr ANTCHEV, Gueorgui	
[9] The Liquid Argon Jet Trigger of the H1 Experiment at HERA	Dr BOB, Olivier	
[122] Commissioning the CMS silicon strip tracker	Dr BAINBRIDGE, Robert John	
[82] The CMS Low Voltage System	Dr LUSIN, Sergei	
[74] DAQ and Control Systems for the CMS Global Calorimeter Trigger Matrix Processor	Mr MARROUCHE, Jad	
[102] A small portable test system for the TileCal Digitizer system	Mr HIDVEGI, Attila	
[115] Evaluation of high-speed single fiber communication using Wavelength Division Multiplexing.	Mr ERIKSSON, Daniel	
[18] Characterization of the noise properties of DC to DC converters for the sLHC	Mr BLANCHOT, Georges	
[108] FPGA Implementation of Optimal Filtering Algorithm for TileCal ROD System	Dr TORRES, Jose	
[11] Development and Testing of an Advanced CMOS Readout Architecture dedicated to X-rays silicon strip detectors	Prof. SISKOS, Stylianos	
[45] Achieving Best Performance with VME-based Data Acquisition Systems and 2eSST	GIORDANO, Raffaele	
[134] Data acquisition systems for future calorimetry at the International Linear Collider	WING, Matthew	
[101] 14-bit and 2GS/s low-power digitizing boards for physics experiments	Mr BRETON, Dominique	
[80] Noise analysis of Radiation Detector Charge Sensitive Amplifier Architectures.	Prof. SISKOS, Stylianos	
[48] The Associative Memory for the Self-Triggered SLIM5 Silicon Telescope	CRESCIOLI, Francesco	
[126] Design Considerations for Area-Constrained In-Pixel Photon Counting in Medipix3	Ms WONG, Winnie	
[71] CMS Tracker, ECAL and Pixel Optical Cabling: Installation and Performance verification	Mr RICCI, Daniel	
[3] High-Resolution Time-to-Digital Converter in Field Programmable Gate Array	Dr LOFFREDO, Salvatore	
[107] Power Distribution in a CMS Tracker for SLHC	Dr CUSSANS, David	
[4] Performance of Specific Multi-Mode and Single Mode Passive Optical Components to Co60 Gamma Rays up to SLHC Integrated Doses	Dr HUFFMAN, B. Todd	
[15] Design and measurements of SEU tolerant latches.	MENOUNI, Mohsine	
[47] A multi-channel 24.4 ps bin size Time-to-Digital Converter for HEP applications	Mr MESTER, Christian	
[40] CMS ECAL LV Control System performance	SINGOVSKI, Alexander	
[76] Data Acquisition System for the KL Experiment at J-Parc	Mr BOGDAN, Mircea	
[130] The VFAT production test for the TOTEM experiment.	Dr ASPELL, Paul	
[23] Sub-Nanosecond Machine Timing and Frequency Distribution Via Serial Data Links	ROHLEV, Tony	
[43] Infrastructures and monitoring of the on-line CMS computing center	Dr RACZ, Attila	
[97] Radiation Damage of SiGe HBT Technologies at Different Bias Configurations	Dr ULLÁN, Miguel	
[91] A front end chip for the INNOTEP project including a 8 bits, 100 MS ADC.	CRAMPON, Sebastien	

[30] Fast transient recorder for spectroscopy experiments	Mr HERRMANN, Florian	
[105] The Alice Pixel Trigger Control and Calibration	TORCATO DE MATOS, Cesar	
[24] A prototype ASIC buck converter for LHC upgrades	Mr MICHELIS, Stefano	
[37] Fast FPGA-based trigger and data acquisition system for the CERN experiment NA62: architecture and algorithms	IMBERGAMO, Ermanno	
[64] Results on the Performance of the CMS Global Calorimeter Trigger for Electrons and Jets	Dr FRAZIER, Robert	
[92] SPECS: a Serial Protocol for the Experiment Control System of LHCb	CHARLET, Daniel	
[17] Commissioning of the SDD data concentrator card CARLOSrx	Dr COSTA, Filippo	
[16] The Data Acquisition System of the MAGIC-II Telescope	Dr BITOSSI, Massimiliano	
[98] PMM2 ASIC : PARISROC	Dr MARTIN-CHASSARD, Gisele	
[57] SKIROC : A front-end chip to read out the imaging Silicon-Tungsten calorimeter for ILC	Mr FLEURY, Julien	
[51] A Prototype of Low Voltage Power Supply Using Piezoelectric Transformer	IMORI, Masatosi	