9th International "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors, Hiroshima, Japan

Contribution ID: 1

Type: ORAL

## Measurements of Low Gain Avalanche Detectors (LGAD) for High Energy Physics applications

Monday 2 September 2013 10:30 (20 minutes)

This work introduce a new concept of silicon radiation detector with intrinsic multiplication of the charge called Low Gain Avalanche Detectors (LGAD). These new detectors are based on the standard Avalanche Photo Diodes (APD) normally used for optical and X-ray detection applications. The main difference to standard APD detectors is the low gain requested to detect high energy charged particles and the possibility to have fine segmentation pitches in order to create microstrip or pixel devices which do not suffer from the limitations normally found in avalanche detectors.

The gain implemented in the non-irradiated devices must retain some effect also after irradiation, with a higher multiplication factor with respect to standard structures in order to be used in harsh envairoments such as expected in colliders experiments.

The investigation of these detectors provides important indications on the ability of this modified electrode geometry to control and optimise the charge multiplication effect, in order to fully recover the collection efficiency of heavily irradiated silicon detectors, at reasonable bias voltage compatible with the voltage feed limitation of the CERN HL-LHC experiments.

Author: PELLEGRINI, Giulio (Centro Nacional de Microelectrónica (IMB-CNM-CSIC))

**Co-authors:** Dr FLETA CORRAL, Celeste (Centro Nacional de Microelectrónica (IMB-CNM-CSIC)); KRAM-BERGER, Gregor (Jozef Stefan Institute (SI)); MANDIC, Igor (Jozef Stefan Institute (SI)); LOZANO FANTOBA, Manuel (Centro Nacional de Microelectrónica (IMB-CNM-CSIC)); Ms BASELGA BACARDIT, Marta (Centro Nacional de Microelectrónica (IMB-CNM-CSIC)); Mr FERNANDEZ, Pablo (Centro Nacional de Microelectrónica (IM-B-CNM-CSIC)); Dr HIDALGO, Salvador (Centro Nacional de Microelectrónica (IMB-CNM-CSIC)); Dr GRECO, Virginia (Centro Nacional de Microelectrónica (IMB-CNM-CSIC))

Presenter: PELLEGRINI, Giulio (Centro Nacional de Microelectrónica (IMB-CNM-CSIC))

Session Classification: Session 1

Track Classification: Simulations and Manufacturing