

## **Tuesday 3 September**

08:40

## Session 3: Strips, pixels

Location: International Conference Center Hiroshima, Dahlia (B2F), 1-5 Nakajima-cho Naka-ku, Hiroshima Japan Conveners: Hartmut Sadrozinski, Valerio Re

08:40-09:00

Development of n-in-p large-area silicon microstrip sensors for very high radiation environments

Speaker

Yoshinobu Unno

09:00-09:20 A double-sided super-module development for the HL-LHC

Speaker

Allan Clark

09:20-09:40

The silicon strip vertex detector of the Belle II experiment

Speaker

Yoshiyuki Onuki

09:40-10:00

Development of planar pixel modules for the ATLAS high-luminosity LHC tracker upgrade

Speaker

Craig Buttar

10:00-10:20

Development of pixel sensors with 25x500 um2 pitch for the ATLAS HL-LHC upgrade

Speaker

Helen Hayward

**POSTER session/Cofee break** 10:20-11:00

11:00-11:20 Development of HPK n-in-p Pixel Sensors for HL-LHC

Speaker

Shintaro Kamada

11:20-11:40

Achievements of the ATLAS Upgrade Planar Pixel Sensors R&D Project

Speaker

Giovanni Calderini

11:40-12:00 Temperature Dependent Measurements of n-in-n Pixel Sensors

Speaker

Reiner Klingenberg

12:00-12:20 Synchronized analysis of testbeam data with the Judith software

Speaker
Andrej Gorisek

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