



Contribution ID: 170

Type: **Oral**

The Alice CTP upgrade

Wednesday 25 September 2013 10:15 (25 minutes)

After three years of successful operation of Alice Central Trigger Processor (CTP) system for proton-proton, Pb-Pb and p-Pb collisions, the Alice CTP is going to be upgraded with a new L0 board in order to improve the performance of the Alice trigger system. The new L0 board will include several new features: an additional trigger level "LM", which will precede the L0 trigger and will improve efficiency of data taking for the Transition Radiation Detector (TRD); a new 10G Ethernet link for CTP readout and control; an extension of the number of clusters and an extension of functionality for classes. Information for the classes with different BC masks will be registered in counters at each trigger level. In addition to these changes, which will come into effect in 2014 at the end of "Long Shutdown 1"(LS1), the first ideas for a CTP upgrade after "Long Shutdown 2"(LS2) in 2018 will also be presented.

Primary author: KRIVDA, Marian (University of Birmingham (GB))

Co-authors: JUSKO, Anton (University of Birmingham (GB)); EVANS, David (University of Birmingham (GB)); BARNBY, Lee Stuart (University of Birmingham (GB)); VILLALOBOS BAILLIE, Orlando (University of Birmingham (GB)); JONES, Peter.G (University of Birmingham (GB)); LIETAVA, Roman (University of Birmingham (GB))

Presenter: KRIVDA, Marian (University of Birmingham (GB))

Session Classification: Trigger