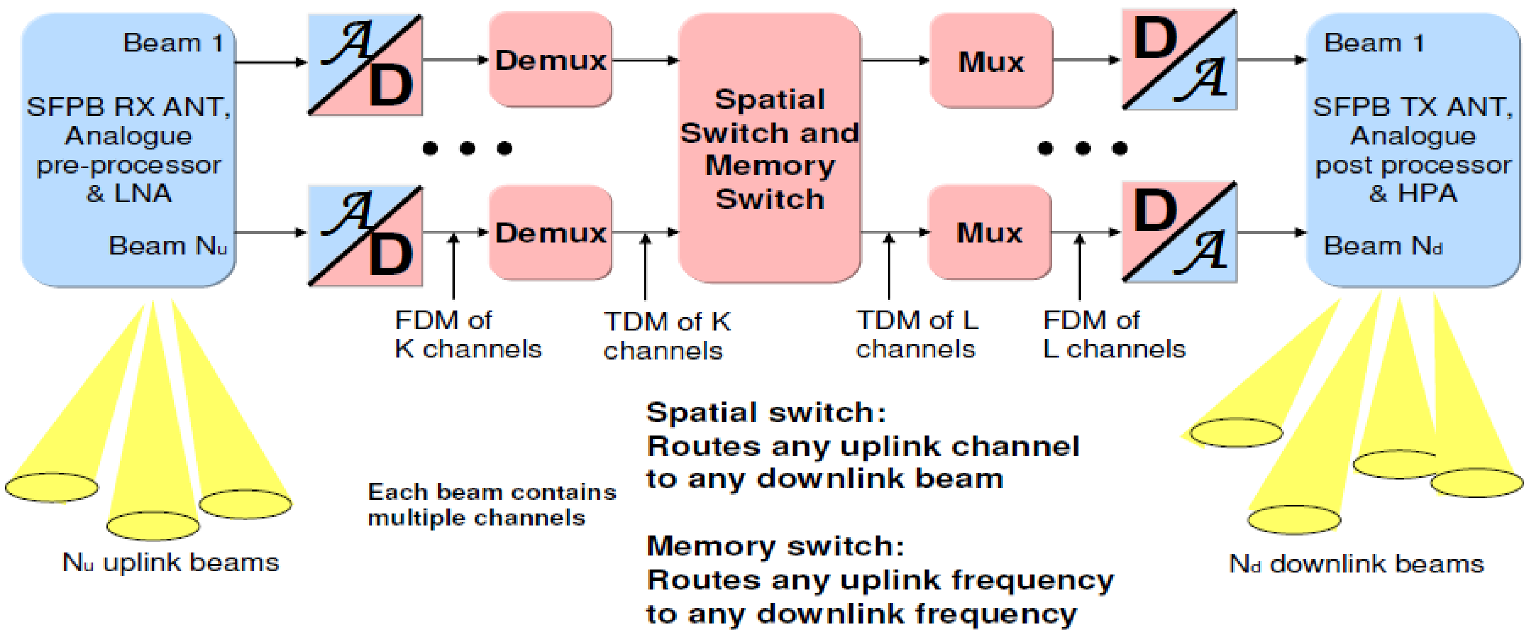
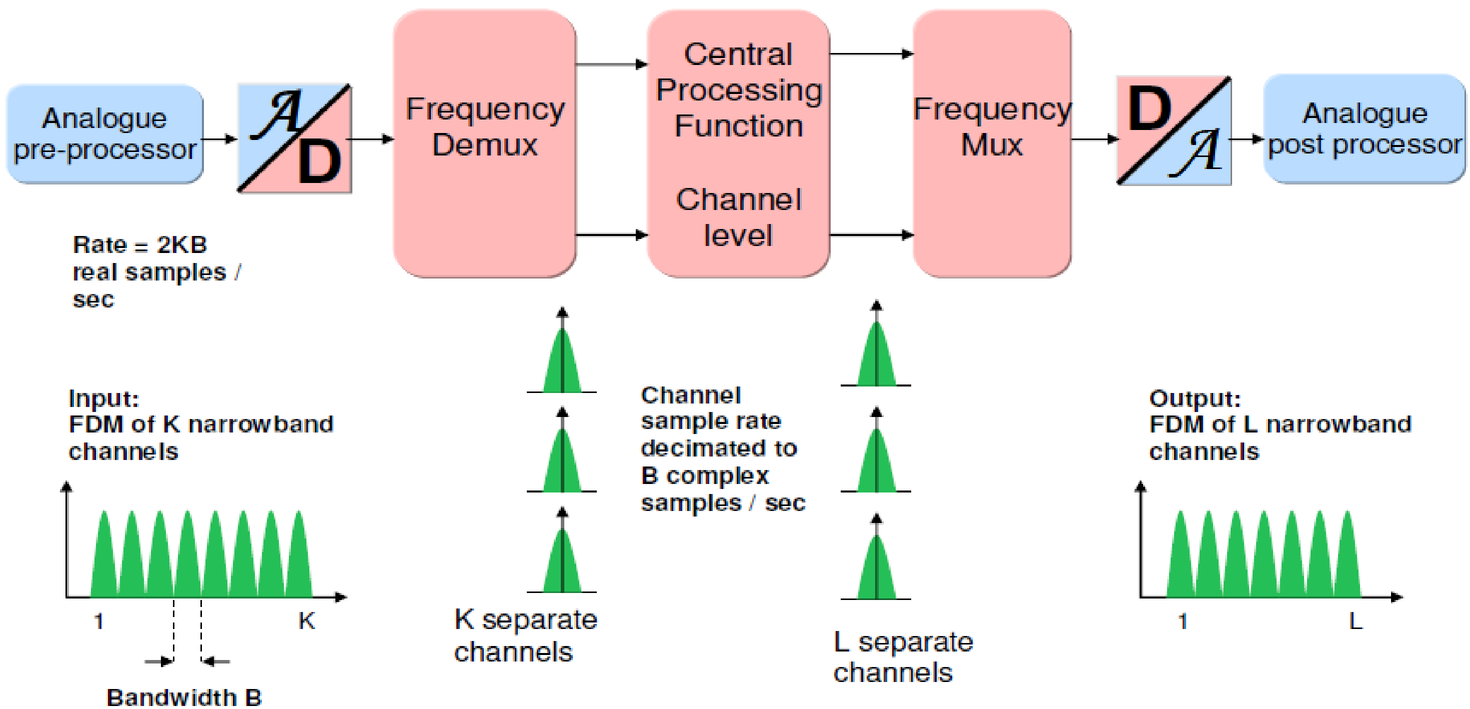
The Design of the Alphasat Payload Processor

Dr. Rajan Bedi
Head of Mixed-Signal Design Group

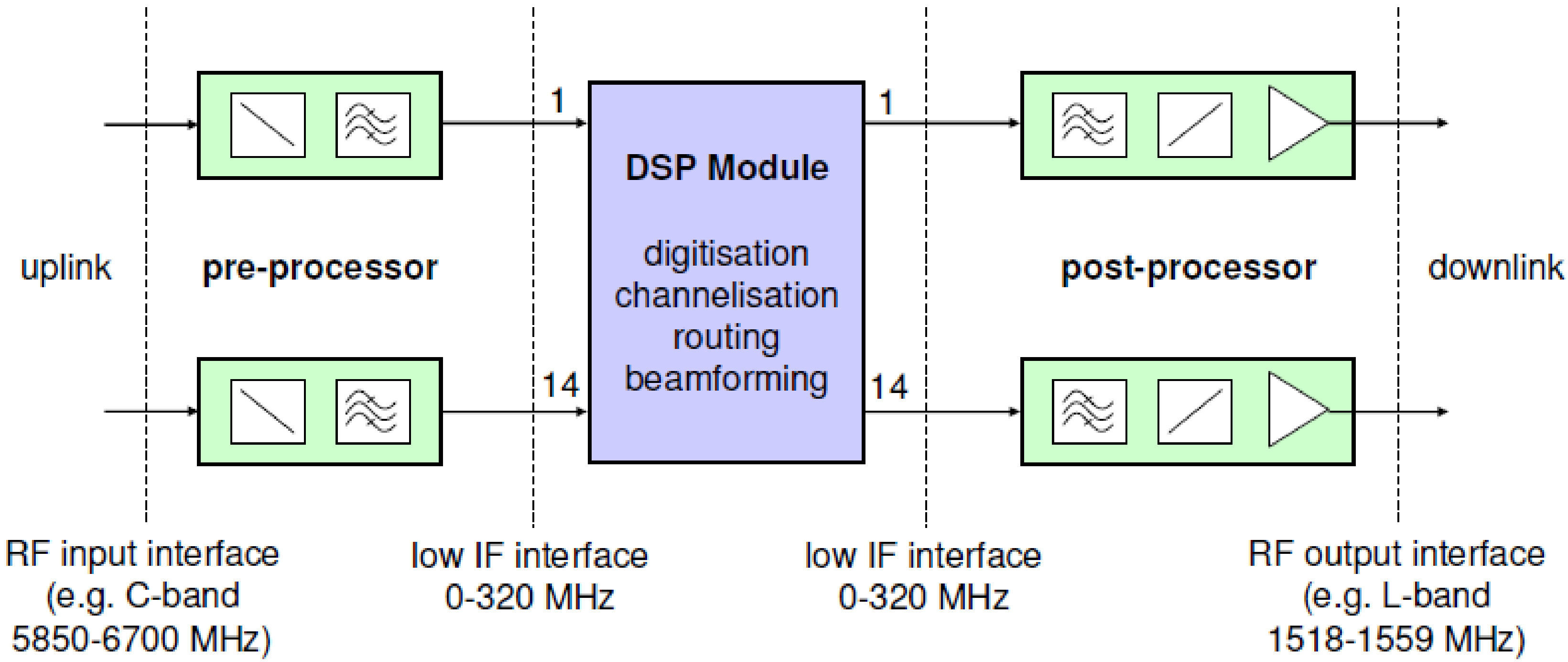
Alphasat Integrated Processor



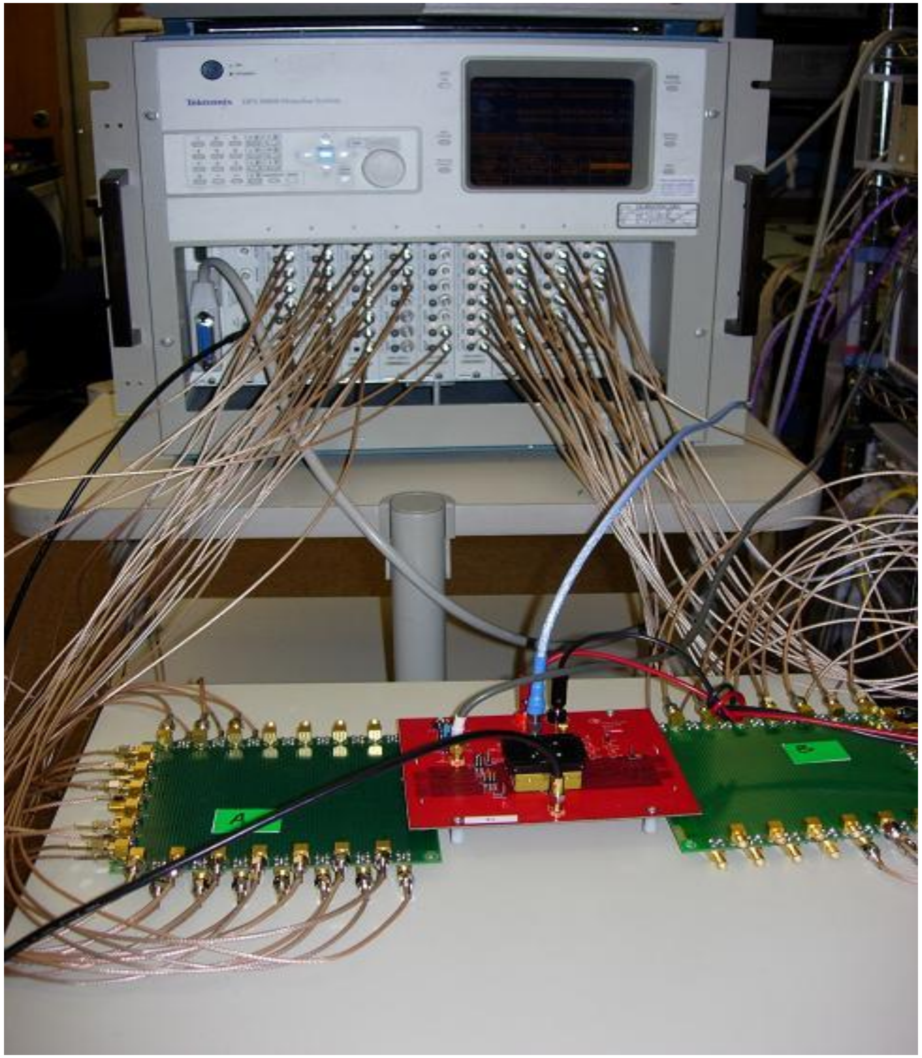
Alphasat Integrated Processor



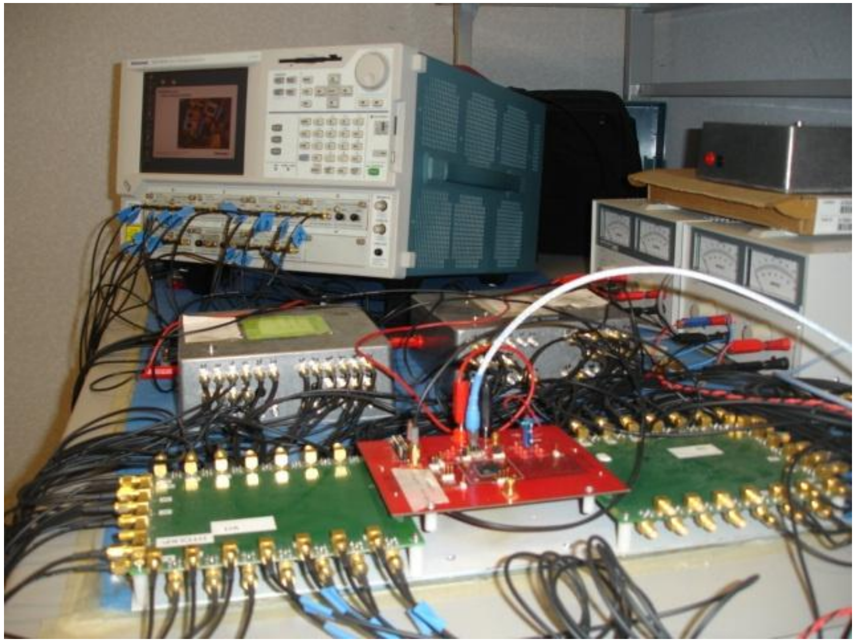
Alphasat Integrated Processor

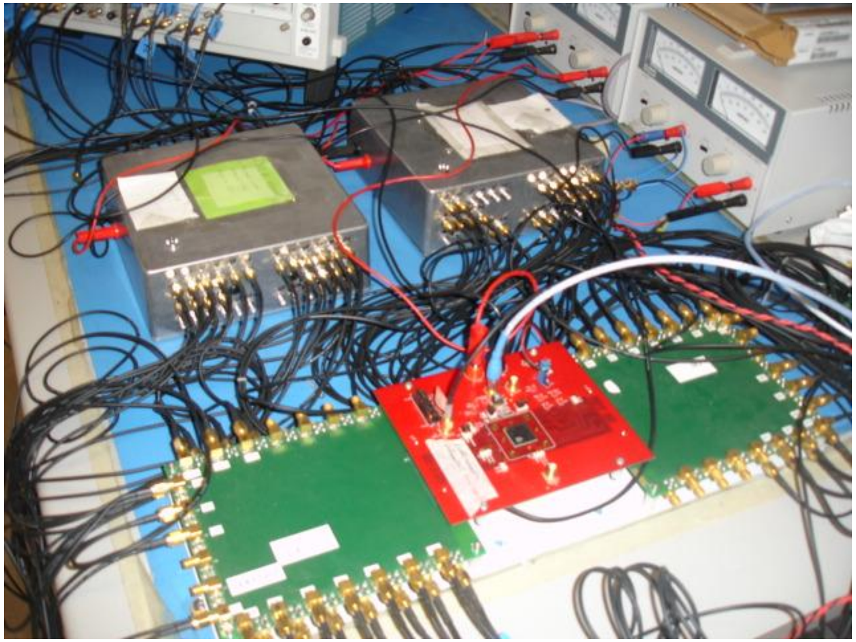


DAC Testing



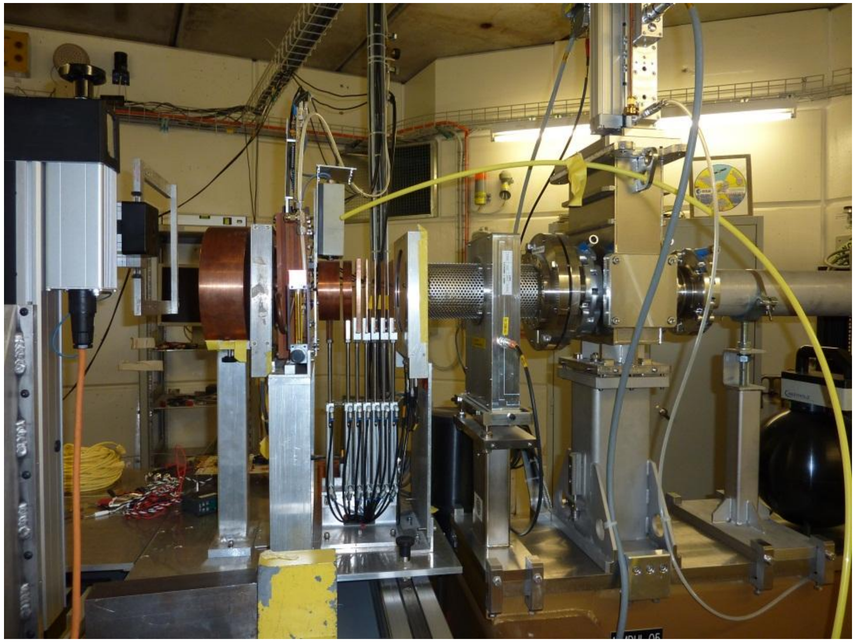
DAC Testing

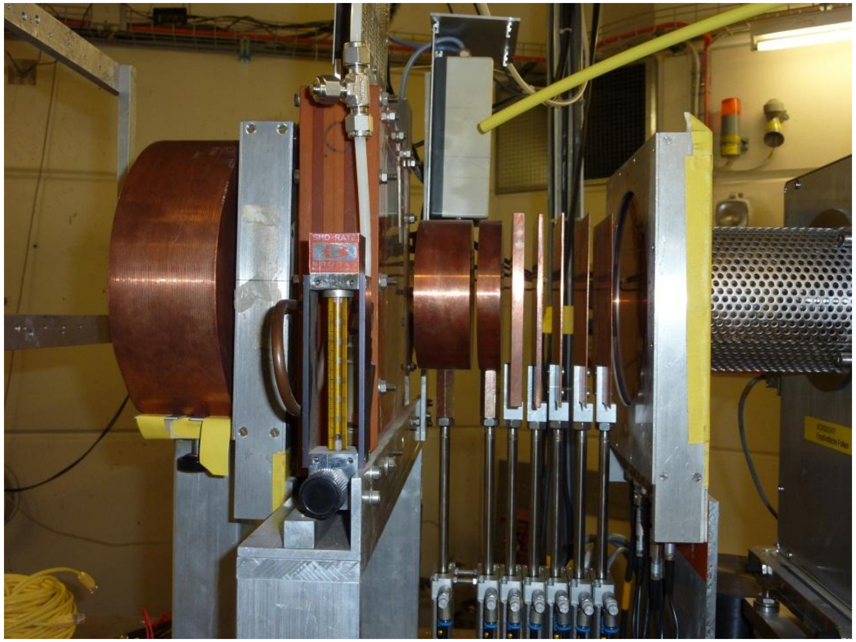




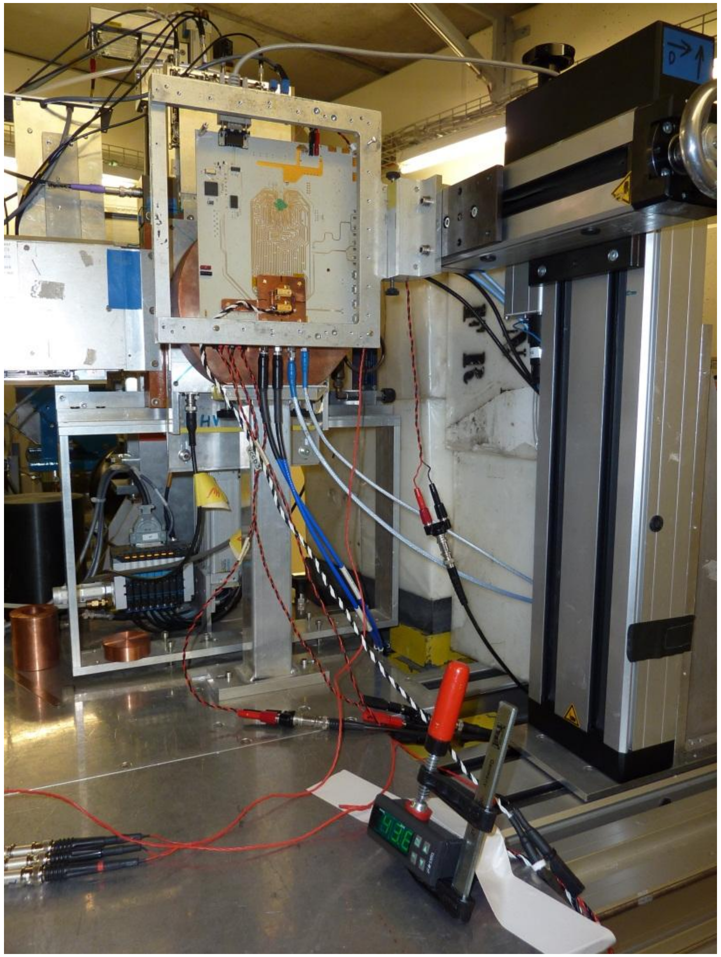
Heavy-Ion Testing

High-Energy Proton Testing

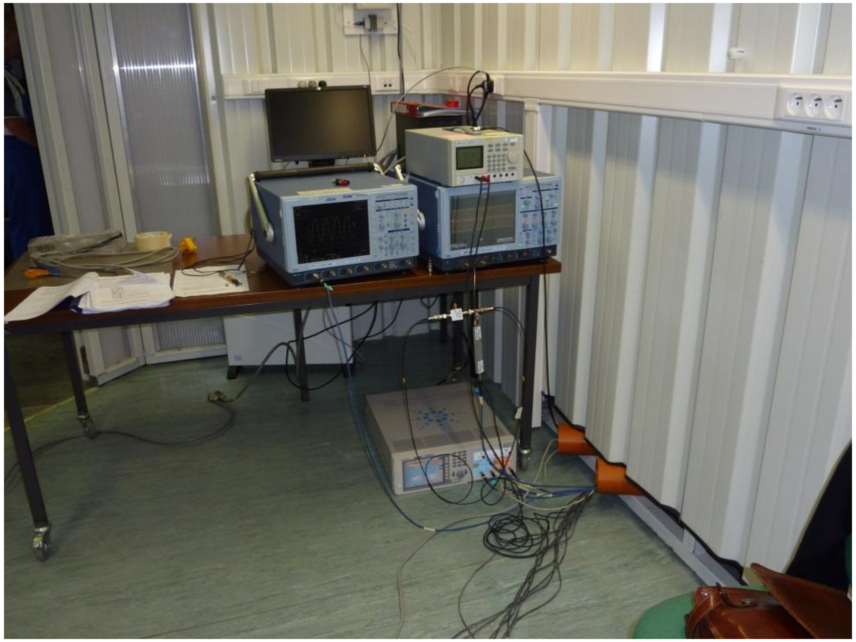




High-Energy Proton Testing

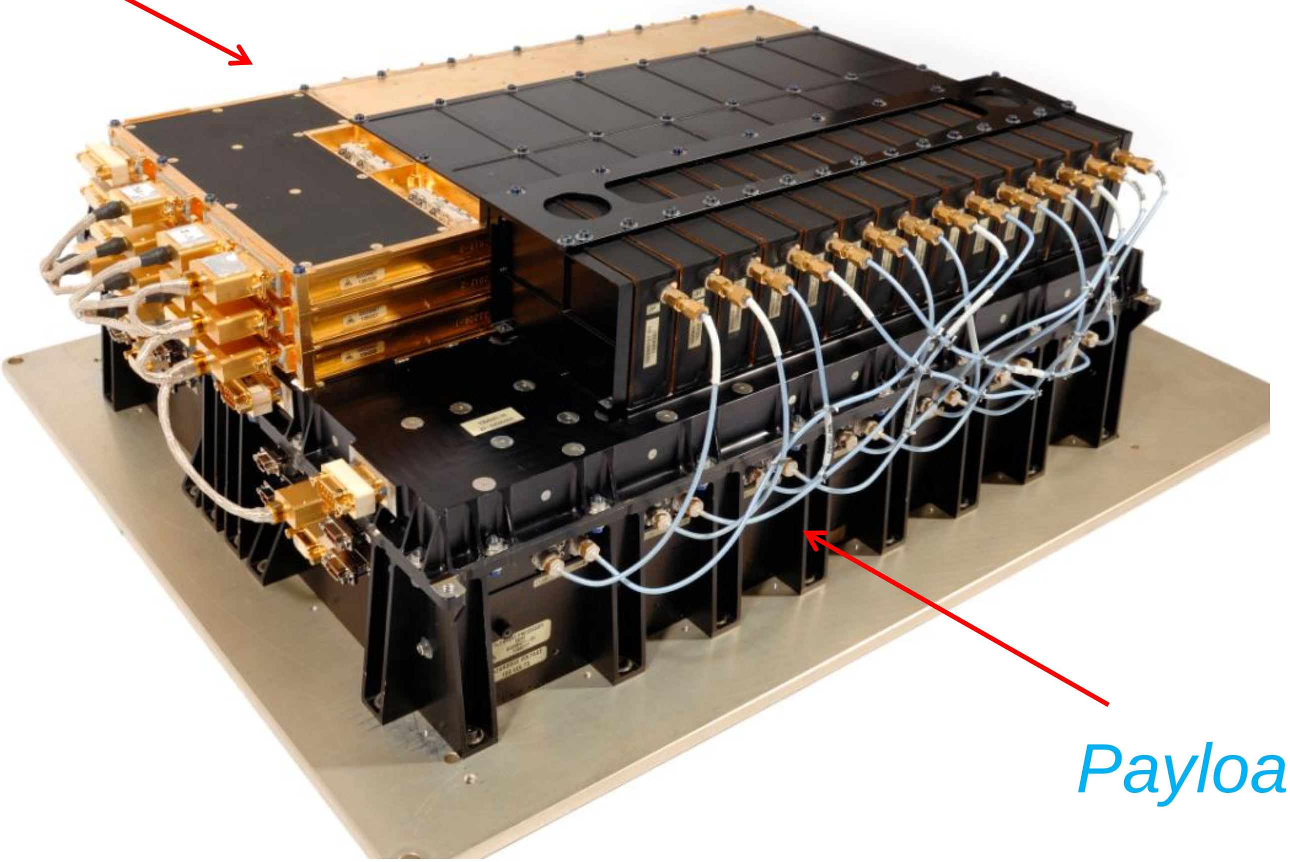


Low-Energy Proton Testing



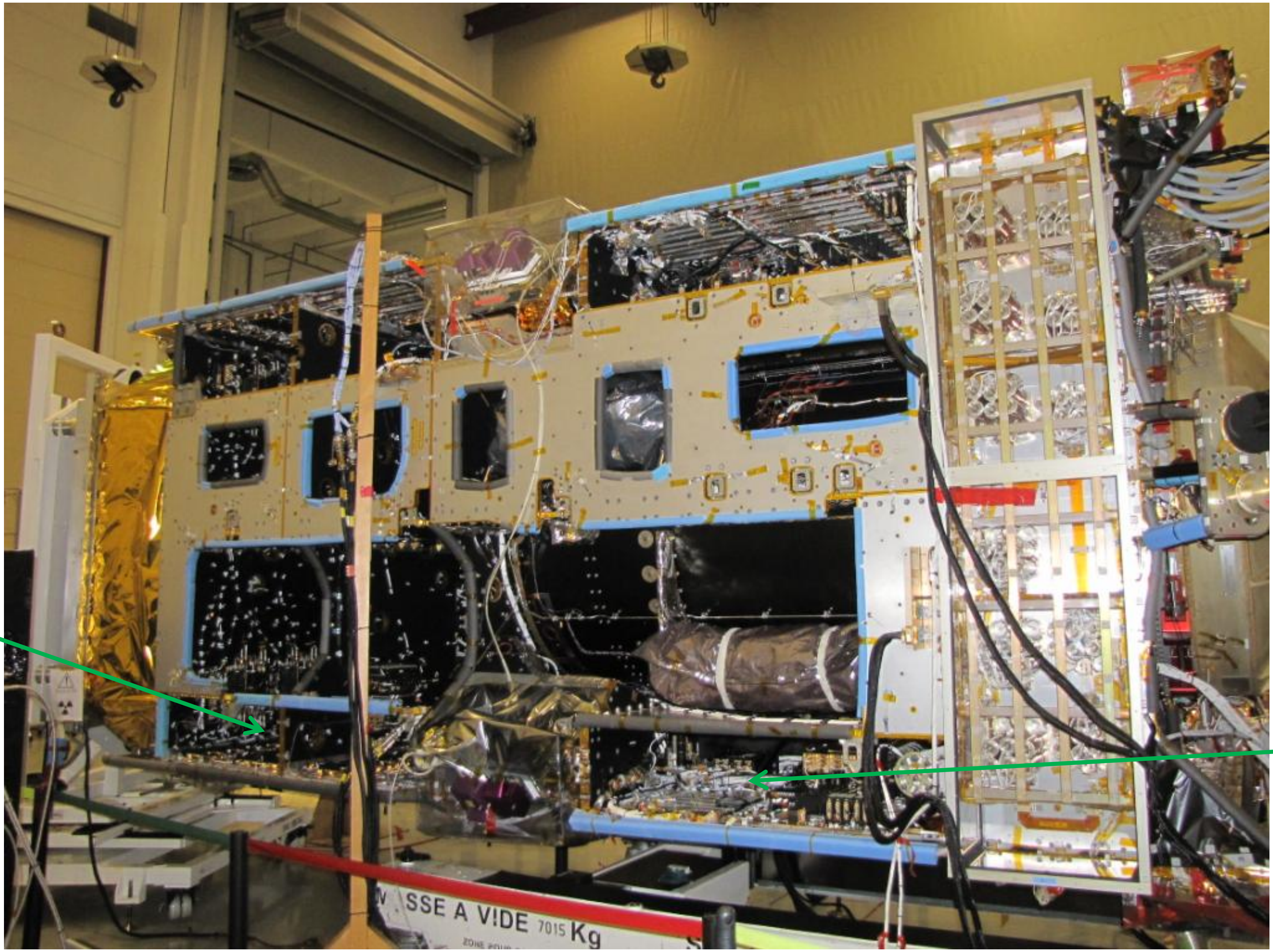
Integrated Processor

RF

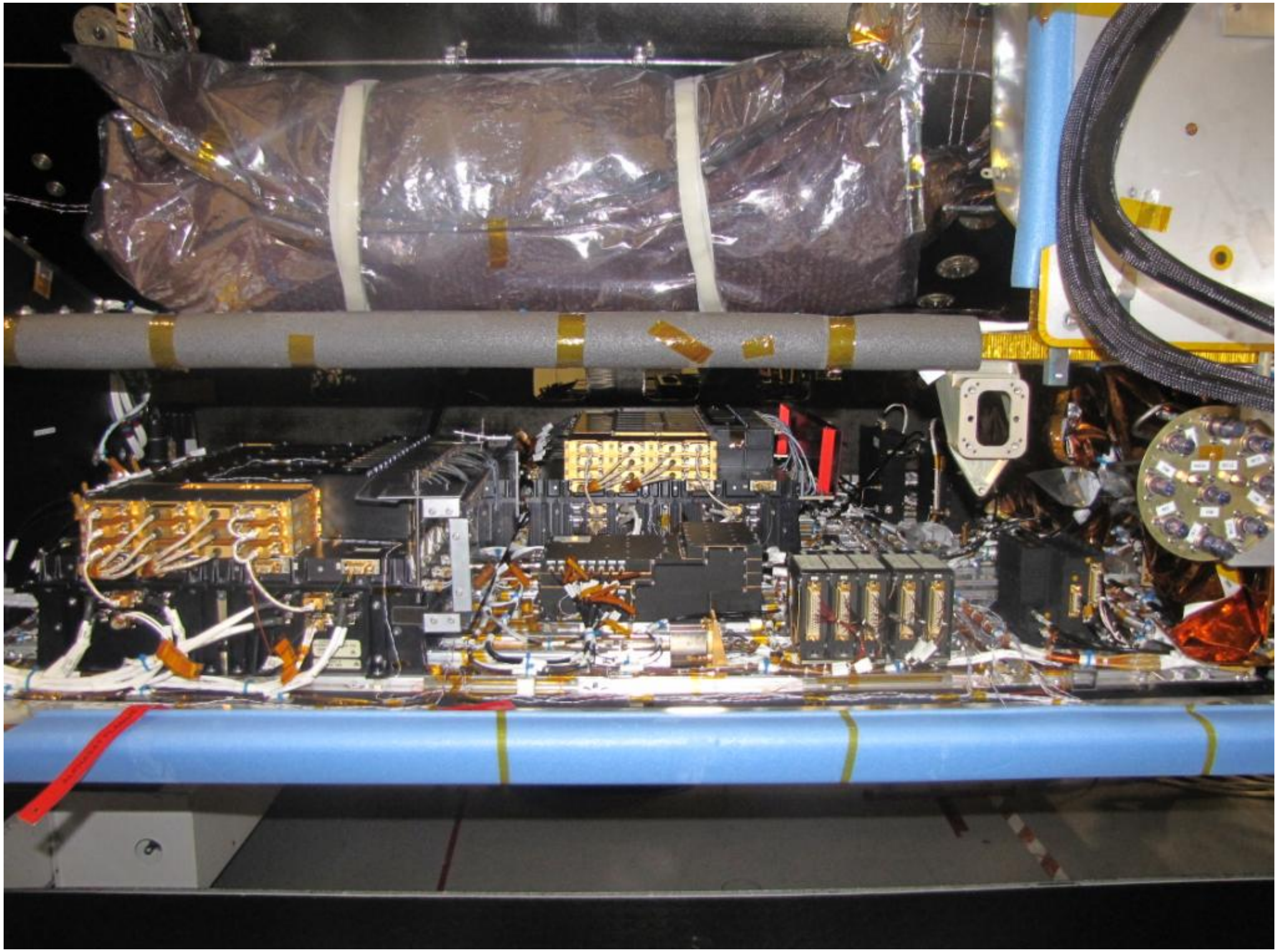


Payload DSP

Alphasat Spacecraft Payload

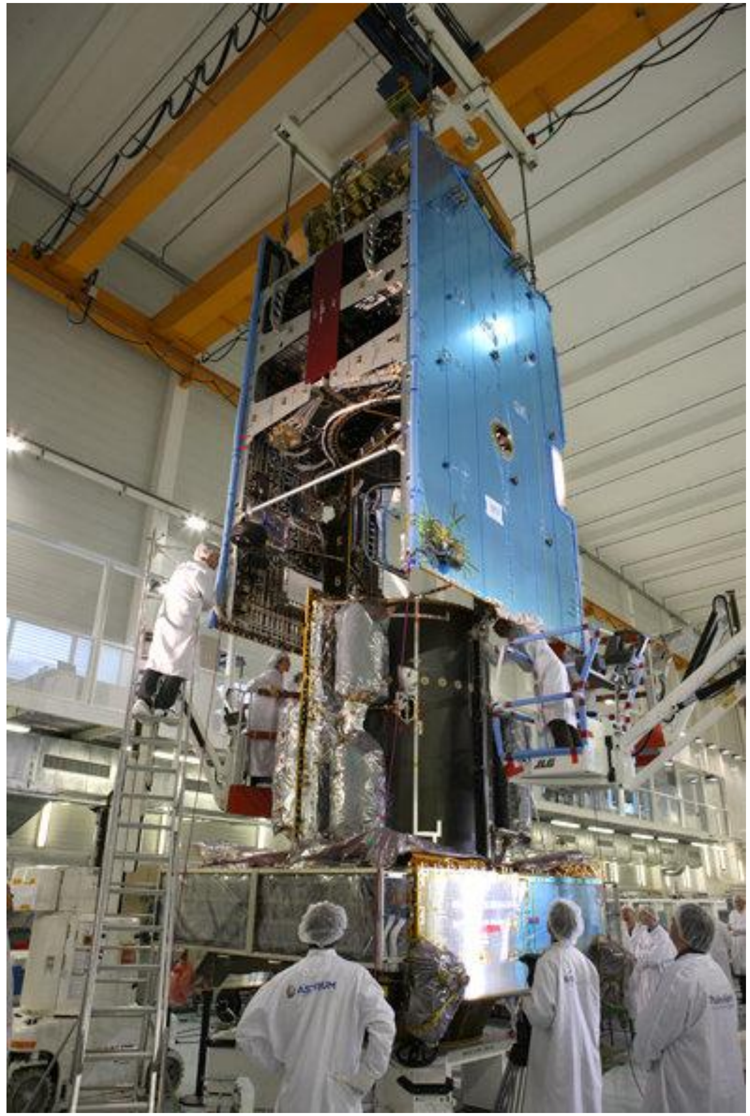


Alphasat Spacecraft Payload



Alphasat & Alphabus

Mating of Alphasat & Alphabus



Alphasat Spacecraft Leaving TVAC

Attaching the 40 m Solar Panels

11 m x 13 m Reflecting Antenna

Leaving Toulouse, France

Arrival in French Guiana

Adding the Propulsion Propellant

Spacecraft Encasing within Ariane 5 Rocket

Spacecraft Encasing within Ariane 5 Rocket

Ariane 5 Ready for Launch

www.edn.com/electronics-blogs/4406636/Out-of-this-World-Design