

TWEPP 2013 - Topical Workshop on Electronics for Particle Physics

Tuesday 24 September 2013

ASICs: A1a - Town Hall, Congress Center (09:50 - 10:40)

-Conveners: Marcus Julian French

time	[id] title	presenter
09:50	[74] Development of variable sampling rate, low power 10-bit SAR ADC in 130 nm IBM technology	MORON, Jakub
10:15	[61] A multichannel Time-to-Digital Converter ASIC with better than 3 ps-rms time resolution	CHRISTIANSEN, Jorgen

ASICs: A1b - Town Hall, Congress Center (11:10 - 12:25)

-Conveners: Marcus Julian French

time	[id] title	presenter
11:10	[2] Pixel front-end development in 65 nm CMOS technology	HAVRANEK, Miroslav
11:35	[184] A 20 mW, 4.8 Gbit/sec, SEU robust serializer in 65nm for read-out of data from LHC experiments	FELICI, Daniele
12:00	[50] Next generation Associative Memory devices for the FTK tracking processor of the ATLAS experiment	BERETTA, Matteo Mario

ASICs: A2 - Town Hall, Congress Center (14:45 - 16:30)

-Conveners: Christophe De La Taille

time	[id] title	presenter
14:50	[62] MISTRAL & ASTRAL: Two CMOS Pixel Sensor Architectures dedicated to the Inner Tracking System of the ALICE Experiment	MOREL, Frederic
15:15	[76] A prototype hybrid pixel detector ASIC for the CLIC experiment	VALERIO, Pierpaolo
15:40	[107] Design of the analog front-end for the Timepix3 and Smallpix hybrid pixel detectors in 130nm CMOS technology	DE GASPARI, Massimiliano
16:05	[185] Monolithic Active Pixel Sensor Development for the Upgrade of the ALICE Inner Tracking System	SNOEYS, Walter

Thursday 26 September 2013

ASICs: B5a - Trumpet 3, Congress Center (09:50 - 10:40)

-Conveners: Luciano Musa

time	[id] title	presenter
09:50	[147] Characterization of the CBC2 readout ASIC for the CMS strip-tracker high-luminosity upgrade	BRAGA, Davide
10:15	[159] Development of Dedicated Front--end Electronics for Straw Tube Tracker in PANDA Experiment	PRZYBOROWSKI, Dominik Wladyslaw

ASICs: B5b - Trumpet 3, Congress Center (11:10 - 12:25)

-Conveners: Luciano Musa

time	[id] title	presenter
11:10	[13] SPADIC - Self-triggered charge pulse processing ASIC for CBM-TRD	KRIEGER, Michael
11:35	[54] Design and Performance of the VMM1 ASIC for Micropattern Gas Detectors	METCALFE, Jessica
12:00	[78] QIE10: A New Front-End Custom Integrated Circuit	DRAKE, Gary

ASICs: B6 - Trumpet 3, Congress Center (14:50 - 16:30)

-Conveners: Marcus Julian French

time	[id] title	presenter
14:50	[203] PETA4 - A Multi Channel TDC/ADC ASIC for SiPM Readout	SACCO, Ilaria
15:15	[141] SamPic0: a 16-channel, 10-GSPS TWDC digitizer chip for picosecond time tagging	GRABAS, Herve Marie Xavier
15:40	[152] ROC chips for imaging calorimetry at the International Linear Collider	SEGUIN-MOREAU, Nathalie
16:05	[103] Petiroc and Citiroc : Front-end ASICs for SiPM read-out and ToF applications	FLEURY, Julien