

Mini-CV

Teddy Loeliger (M.S. degree and PhD degree in Electrical Engineering from ETH Zurich in 1994 and 2001) has worked on analog and mixed-signal integrated circuit design for large-area photosensing applications in CMOS technology at ETH Zurich. After that, he was with IBM Zurich Research Laboratory, Rueschlikon, and designed the integrated analog front-end for an AFM-based data storage system. Furthermore, he was Professor for Microelectronics at HSR University of Applied Sciences Rapperswil and managed a full-custom analog and mixed-signal ASIC design team. In 2010, he joined DECTRIS Ltd., where he is currently leading the ASIC design team and is working on mixed-signal front-end ASICs for single-photon counting hybrid-pixel x-ray detectors.