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The ALICE Trigger Overview

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The ALICE Central Trigger Processor (CTP) is designed to select events with different features at rates which can be scaled down to satisfy physics requirements and restrictions imposed by the bandwidth of Data Acquisition system. The challenge of the ALICE trigger is to make optimum use of the component detectors which are busy for widely different periods following the valid trigger, and to perform trigger selection for several different running modes: p-p, Pb-Pb and p-Pb with widely varying luminosities. In this presentation the CTP logic and design are summarized. The performance of ALICE trigger during the first years of LHC data taking is presented. The plans for trigger upgrades after current shutdown as well as after year 2017 are discussed.

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