

The University of Manchester





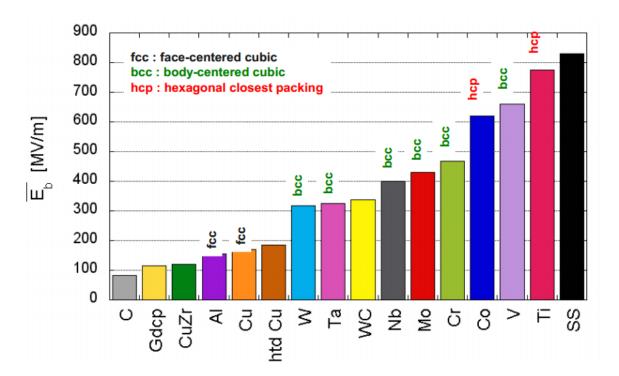
# **DC Spark Experiments**

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# Why study breakdown in a DC System for CLIC

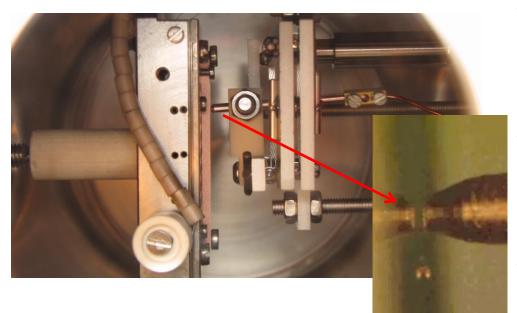
RF tests are expensive and time consuming. DC tests allow many more tests to be carried out.

The physics in DC tests is also simpler, no pulse surface heating, pre-breakdown magnetic fields etc. In order to understand the physics of RF breakdown, DC breakdown must be understood first!



For example it would never have been practically possible to get this lovely plot using only RF tests.

#### What are the CERN DC spark systems?



High voltage is applied across the electrodes and the resulting current and voltage waveforms are analysed (largely automatically) and recorded.

The CERN DC spark systems consist of an anode and cathode in a rod-plane geometry in ultrahigh vacuum. I will be talking about system I which is powered by the High-Rep-Rate circuit.

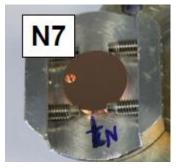
The gap size can be varied from 0-100um by using a stepper motor. It is possible to monitor and actively control the gap with an accuracy of ~1.5um.

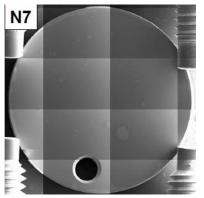
The diameter of the anode is 2.3mm and has a hemispherical tip.

The cathodes have a good surface quality.

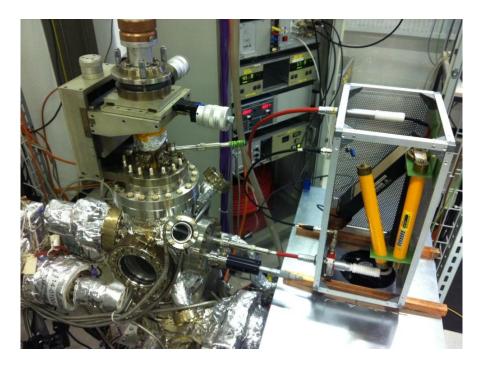
From these we can tell whether a BD occurred and measure several properties of the BD such as the turn on time, the position of the BD within the pulse, the burning voltage and even the gap distance!

We are not currently able to measure the field enhancement factor  $\beta,$  with this setup however.

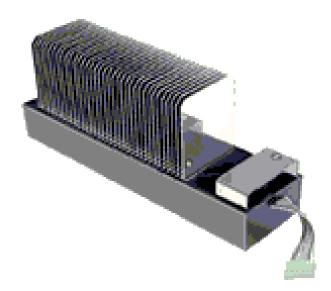




# What is the High Rep Rate Circuit?

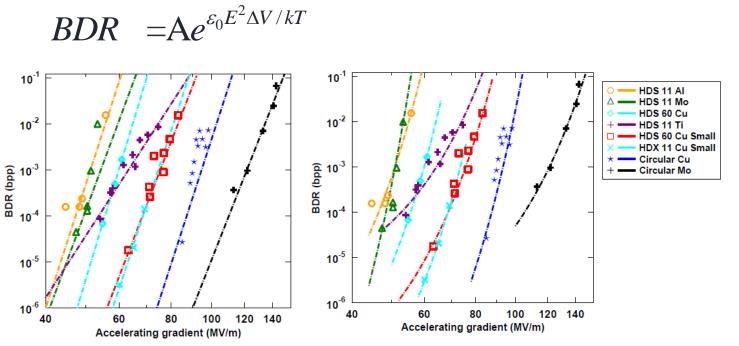


The picture above shows the HRR circuit. The metal box housing the switch is placed as close as possible to the vacuum chamber to minimise stray capacitance. The HRR circuit uses a solid state switch to supply high voltage pulses (up to 10kV) at a rep rate of up to 1kHz. The energy is stored on a 200m/1us long coaxial cable.



#### Motivation behind the HRR system

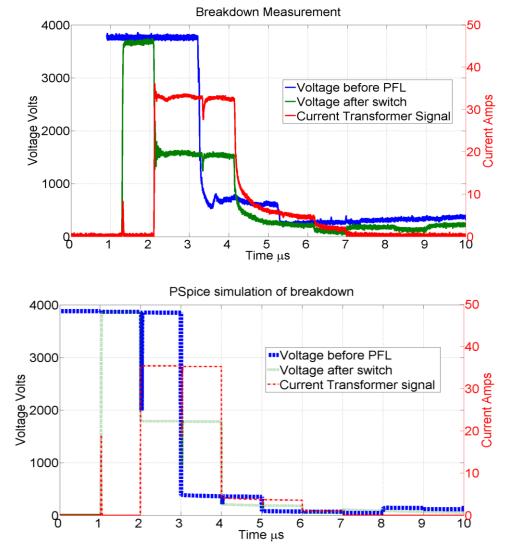
- CLIC is interested in the low breakdown regime ~10^-7 BDs/pulse/m.
- With the older mechanical relay the maximum operating frequency is only 0.5Hz.
  - 10^7/0.5Hz ~ 7.7months
- By using a solid state switch the HRR system can operate at 1kHz
  - 10^7/10^3Hz ~ 3 hours



We should also be able to test the BDR vs. E stress model scaling theory proposed by Fluyra Djurabekova.

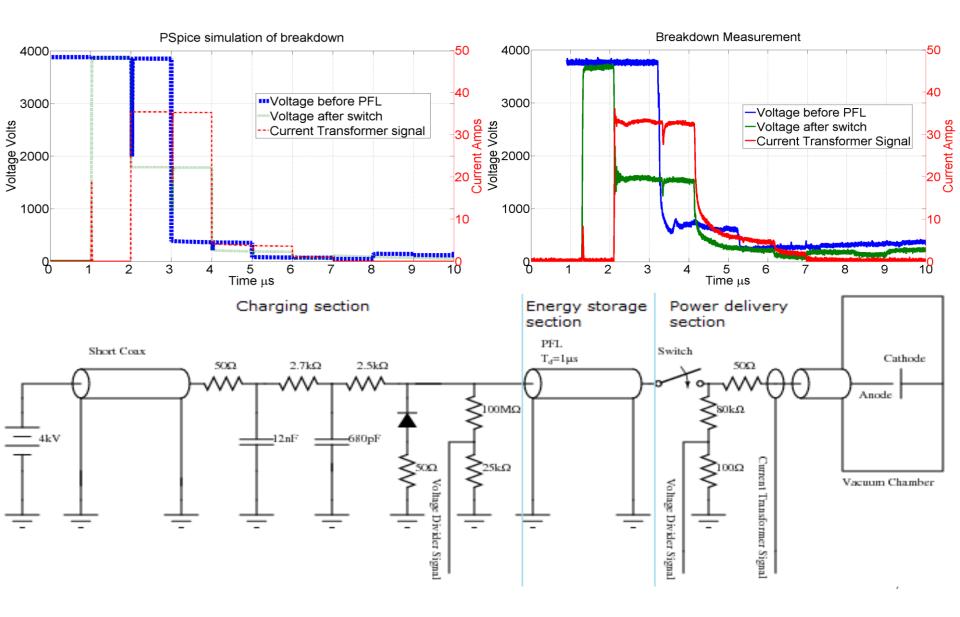
# The High Rep Rate System

Measured voltage and current signals in the HRR circuit during a breakdown. The switch is closed at 1us and the breakdown occurs at 2us.



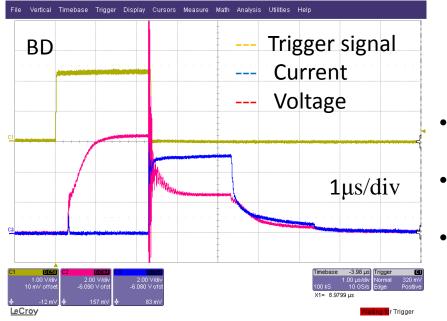
PSpice simulation of the HRR circuit during a breakdown. The switch is closed at 1us and the breakdown occurs at 2us.

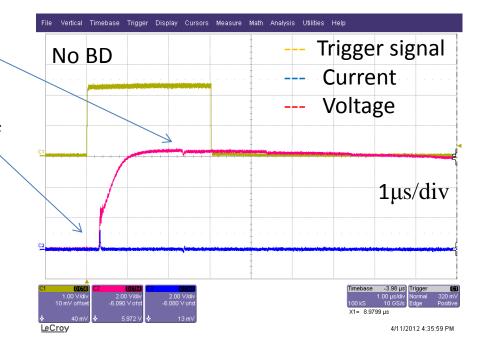
#### The High Rep Rate System



### Measured traces

- Fast voltage rise, but slow voltage fall time
- Pulse length adjustments not useful
- Small and brief initial charging current
- The voltage measurements made now are of a much higher bandwidth.





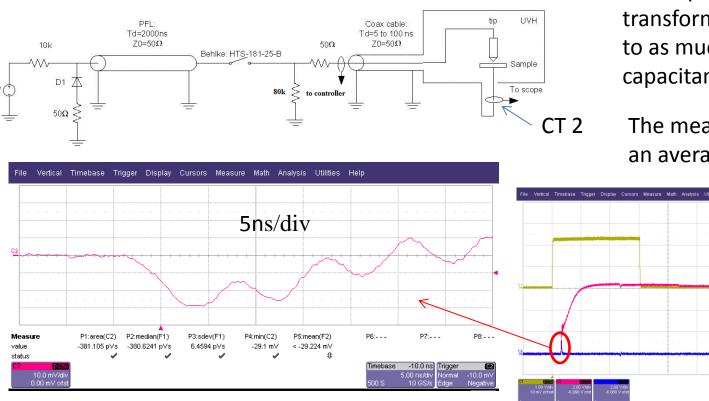
- The turn on time is how quickly the voltage drops after a BD.
- The BD position is the position or time the BD occurs within the pulse.
  - An estimation of the burning voltage can be obtained by averaging the voltage fluctuations after the BD.

#### Gap monitoring

# **Pulse Integration Method**

The pulse integration method is used to monitor the gap distance.

Current transformer 2 as shown in the circuit diagram is used to measure the transient current flow when the switch is closed. The area of this current pulse is dependent on the gap capacitance.



In this position the current transformer is insensitive to as much stray capacitance as possible

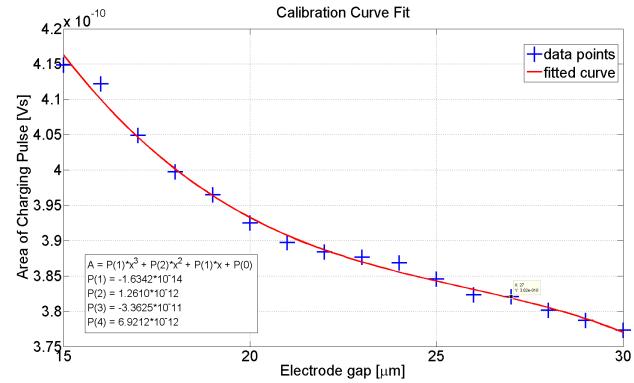
The measurement used is an average of many pulses.

1µs/div

1/11/2012 4:25:50 PM

#### **Calibration Curve**

A cubic function is fitted to the calibration data and used to convert subsequent integral measurements to a gap distance.



#### How good is the measurement?

We are able to measure the gap to +-1.5um, as yet it hasn't been determined if this is a limit on the gap measurement or the gap tends to actually vary by this amount.

The error introduced when going into contact is of the order of 1um, but this only introduces a fixed offset.

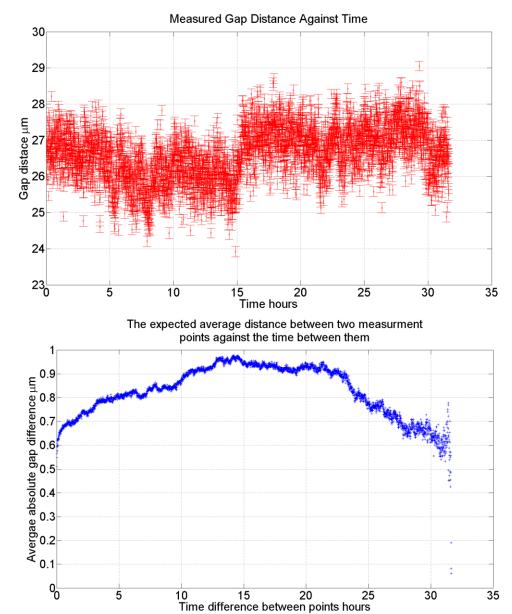
# Change in Gap without BDs

The system was left pulsing at 100Hz 2000V for a day in order to measure the gap variation with no BDs over time.

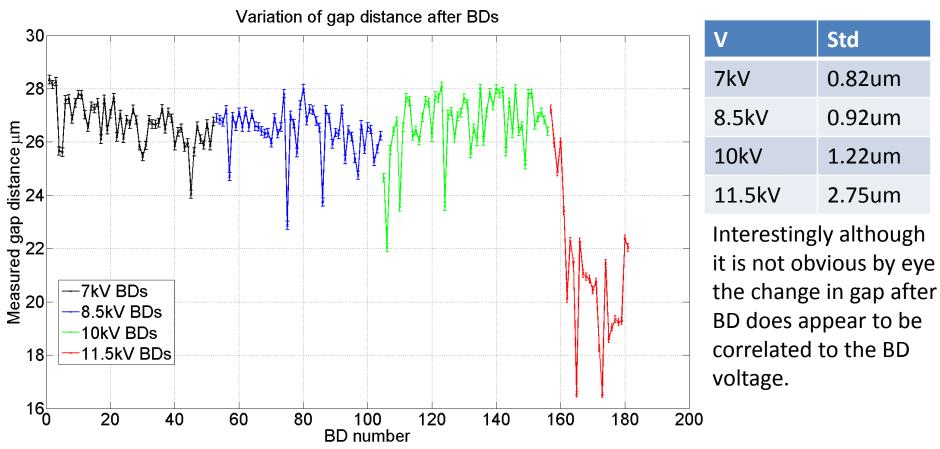
This plot was generated from the data in the previous plot it shows  $\Delta X$  against  $\Delta T$ , where:-

$$\label{eq:deltaX} \begin{split} \Delta X(\Delta T) &= \text{mean}(\text{abs}(d(t) - d(t + \Delta T)), \\ \text{over all } t \end{split}$$

And d(t) is the measured gap distance at time t



# Change in Gap with BDs

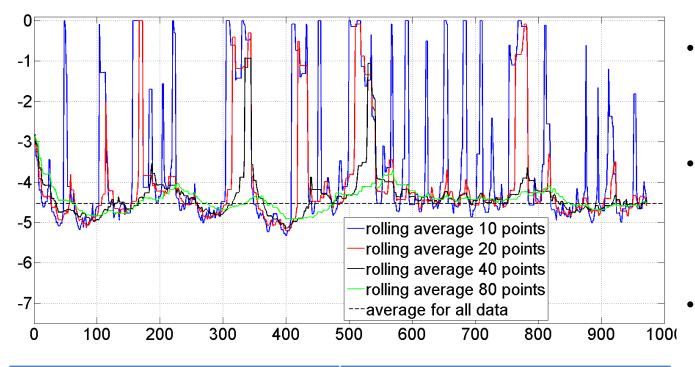


In this plot each point represents a gap measurement @2000V after a single BD was forced to occur at a higher voltage indicated in the legend. The gap was **not** reset by going into contact after each series.

Oddly the gap appears to suddenly get smaller at 11.5kV.

#### BD statistics at fixed voltage

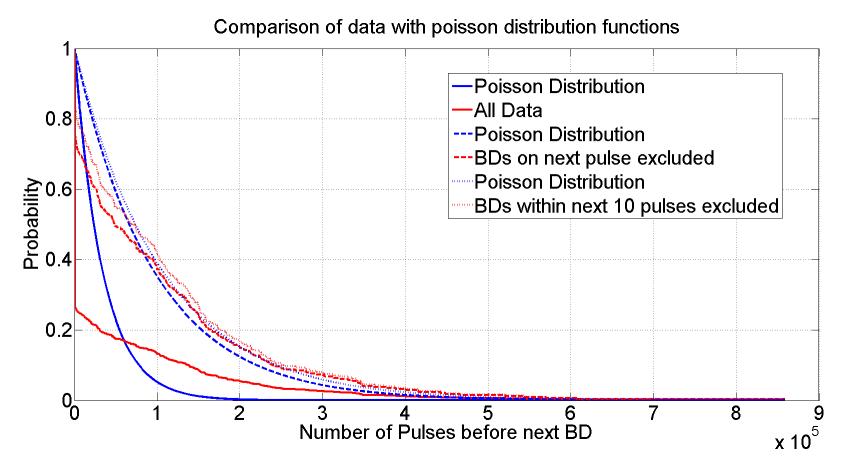
# Rolling average



Total #BDs	972
Average BDR	2.95*10^-5 BDs/pulse
Std(pulses between BD)	8.54*10^4
Ratio(immediate BDs/Total BDs)	0.64

- Bigger voltage steps more BDs per voltage level.
- Clusters mean the calculated BDR for 20 BDs may not reflect the underlying BDR.
- Gap seems to be changing once stepper motor arrives this will not be such a problem.

#### BDR Analysis of long run experiment



This experiment was performed at a surface electric field of 170MV/m and a gap size of 20um. The large number of BDs which occur immediately following a previous BD cause the data to deviate significantly from the Poisson distribution that might otherwise be expected. Even when BDs on the next pulse or within the next 10 pulses are excluded the distribution is still not Poissonian.

#### BDR vs E

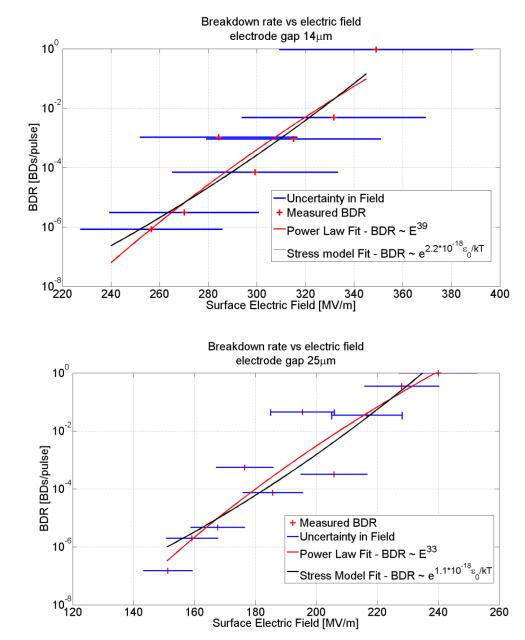
### BDR vs E 14um and 25um gap

For each experiment the gap is first set to the required distance. Then the voltage is set at the highest value and the HRR circuit begins pulsing at 1000Hz.

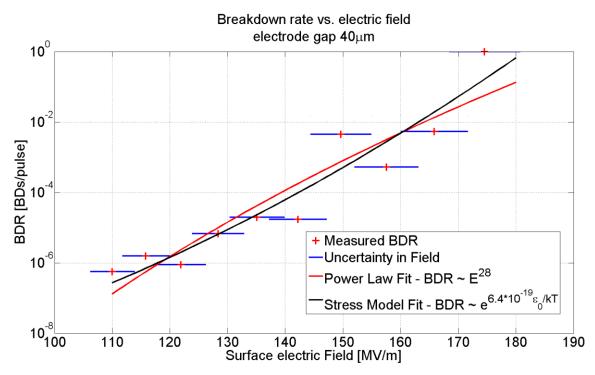
After 100BDs have been recorded or 10^7 pulses and at least 10BDs the voltage is reduced by 5%.

Every 10mins if no BD has occurred the HRR circuit is made to pulse at a predetermined voltage too low for a BD to occur, so the gap can be measured and automatically corrected.

As the uncertainty in the gap is always around 2um the error in the field is much larger at smaller gap sizes.



#### BDR vs E 40um gap



Both the power law model and the stress model fit the data well. Going to a lower BDR in the future should help distinguish between them. The exponents obtained for the power law model are very similar to those obtained in high power RF tests of accelerating cavities.

The fitted exponent tends to decrease for a larger gap.

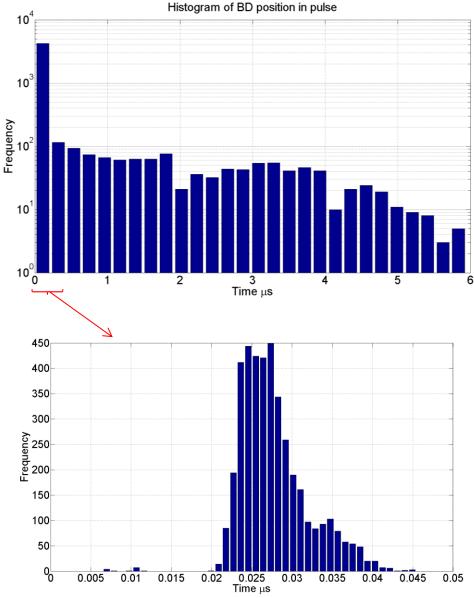
### **BD** position



# **Distribution in BD position**

Almost all BDs occur at the beginning of the pulse. The distribution of non immediate BDs is quite flat up to 3us at which point the voltage across the gap begins to fall. A small drop in voltage due to a reflection from the end of the PFL results in fewer BDs at 2us.

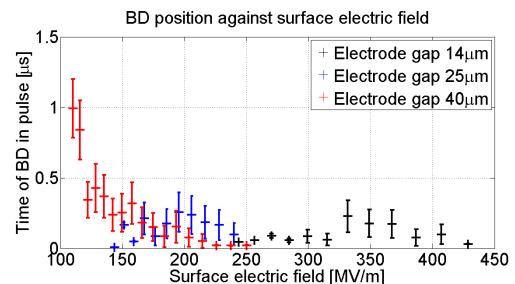


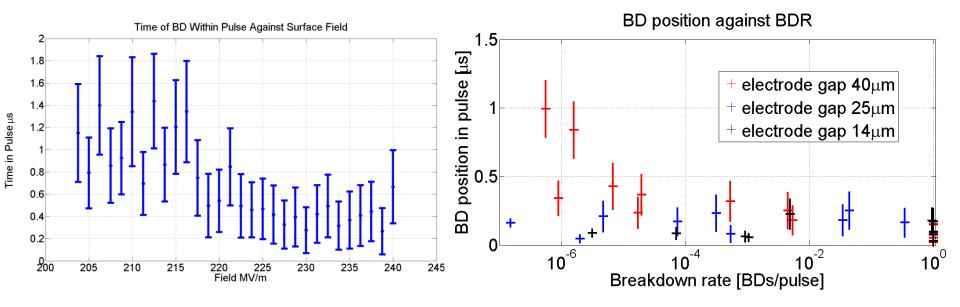


# **BD** position dependencies

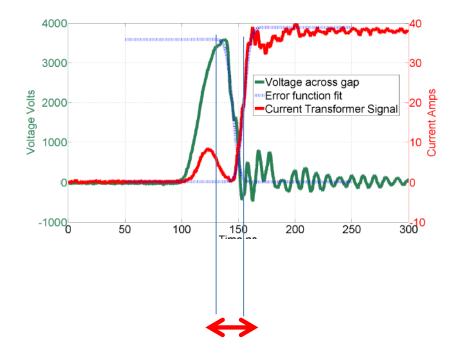
The position of the BD within the pulse, in some experiments, is seen to occur later for lower fields and BDRs.

The graph below shows some old data taken at a gap of 20um but before gap monitoring was possible.





#### Measured Turn on Times



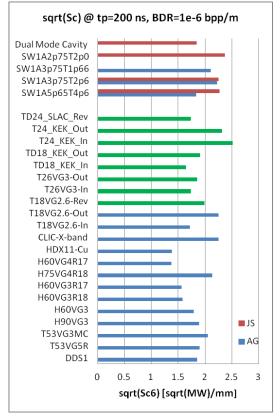
# Firstly - why are we interested in turn on time?

RF tests indicate that low group velocity, and consequently narrow bandwidth structures are able to sustain much higher surface fields than high group velocity, large bandwidth, structures.

Further study has led to the idea that the process which governs the turn on time is the instantaneous power flow available to feed the breakdown during its onset.

In other words a high group velocity structure could more quickly replenish local energy density absorbed by a growing breakdown leading to faster turn on times.

An accurate measure of the rise time of breakdowns in the DC systems under electrostatic conditions is an essential precursor to understanding whether the transient response of RF systems to the breakdown currents determine breakdown limits.



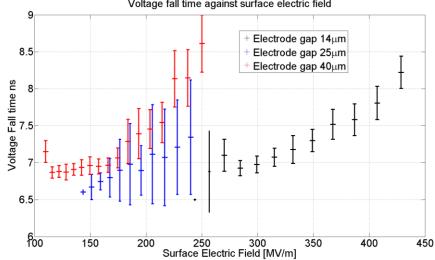
For more background see references below

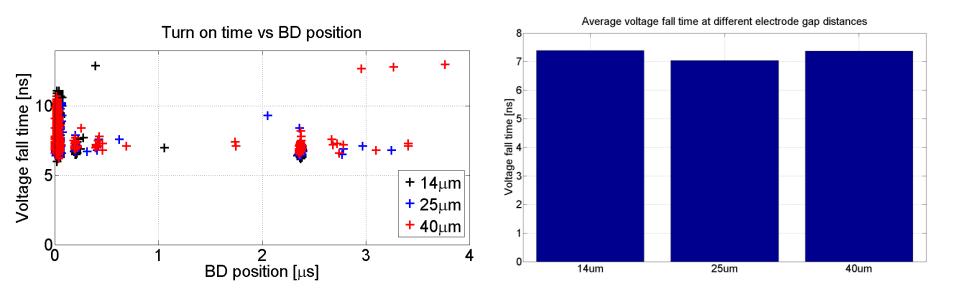
[1] C. Adolphsen 2005, "Advances in Normal Conducting Accelerator Technology from the X-Band Linear Collider Program", PAC 2005 pp.204-8.

[2] A. Grudiev, S. Calatroni and W. Wuensch 2009, "New local field quantity describing the high gradient limit of accelerating structures", PRSTAB, vol. 12, no. 10, pp.102001-1 -102001-9.

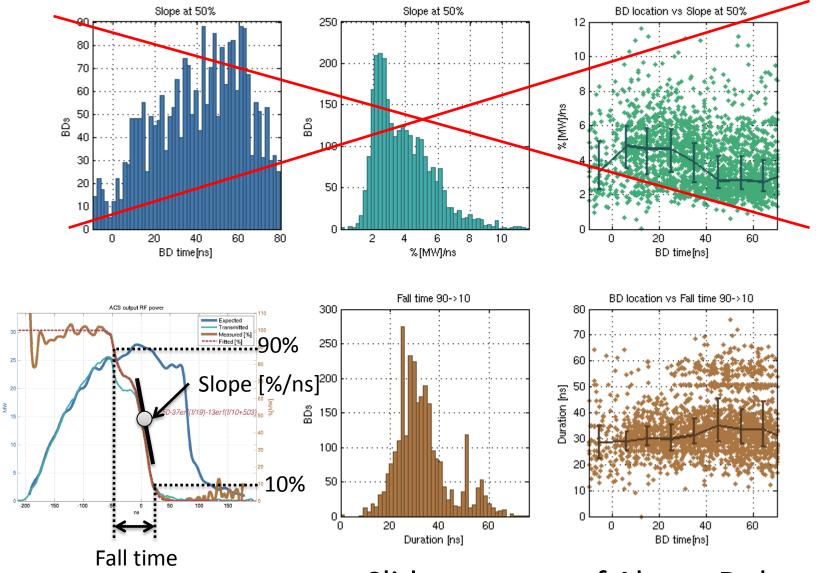
# Turn on time measured in the DC Spark system

The turn on time appears uncorrelated with gap size or BD position, but is generally higher at higher surface fields.



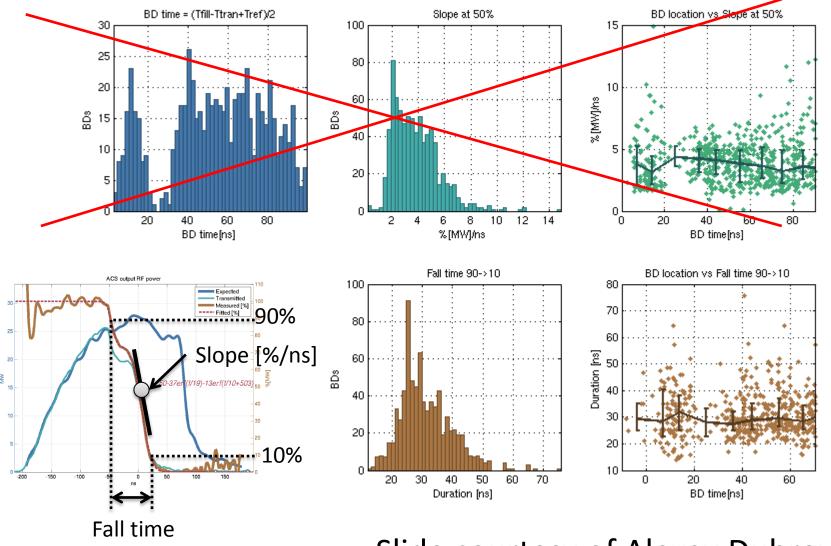


#### Falling edge duration - TBTS



Slide courtesy of Alexey Dubrovskiy

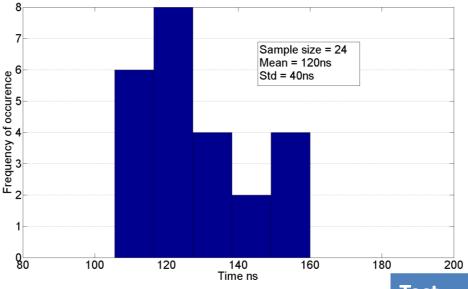
#### Falling edge duration - KEK



Slide courtesy of Alexey Dubrovskiy

# Falling edge duration - KEK

Swiss FEL breakdown turn on times



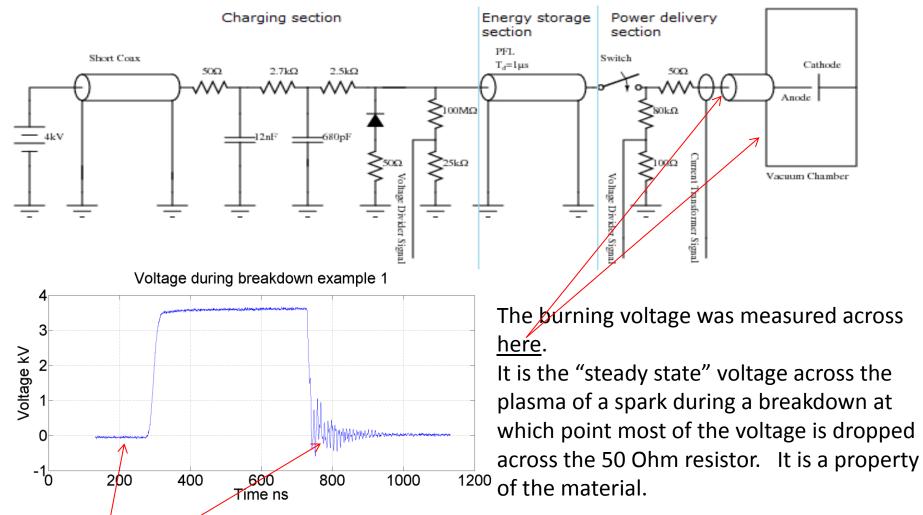
The Swiss FEL turn on times are much longer than in the DC case and the variation is much greater, this is keeping with other RF breakdown turn on time measurements.

The summary table on the right suggests the characteristic size of the system breaking down may govern the turn on time.

Test	Measurement	Result
Simulation		0.25ns
New DC System	Voltage Fall Time	~7ns
TBTS (X-Band)	Transmitted Power Fall Time	20-40ns
KEK (X-Band)	Transmitted Power Fall Time	20-40ns
Swiss FEL (C-Band)	Transmitted Power Fall Time	110-140ns

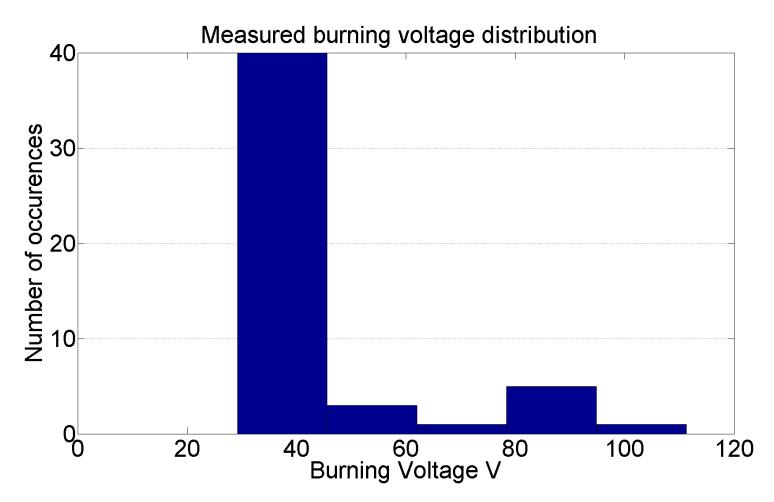
#### Measured Burning Voltages

# **Measured Burning Voltages**



Subtract average voltage with switch closed from Average voltage during breakdown after initial voltage fall.

# Measured Burning Voltages



The literature gives a value for the burning voltage of clean copper of ~23V. This is lower than what I have measure so far in the DC spark system. But I have not measured or corrected for the short circuit resistance of cables etc.

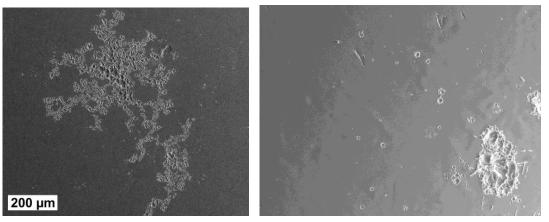
#### The Fixed Gap System

# Motivation behind the fixed gap system

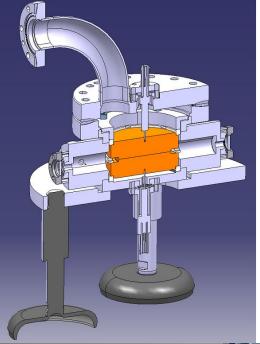
This fixed gap system solves two key issues...

- 1. There is no need to measure the electrode gap, it is fixed.
- 2. The surface area is very much larger, so hopefully breakdown will usually occur on "virgin" surface which hasn't seen a breakdown yet.

Also the system is very compact 30cm x 30cm. This will allow the whole system to be placed inside a 2T magnet we have at CERN, enabling us to study the effect an external magnetic field has on the BDR.

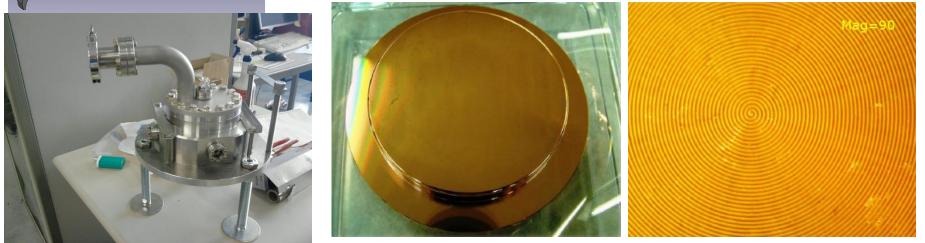


These SEM pictures well illustrate the difference in experimental conditions in RF tests (Right) which even after 100s of hours of testing shows minimal surface modification compared to just 5 breakdowns in the present DC spark system (Left).



# The Fixed Gap System

Despite the comparatively large size of the anodes, the system is very compact. Four antennas are included in the design to pick up the radiation from breakdowns.



The surface of the electrodes are 80mm in diameter and have a surface tolerance of <1um. The picture on the right shows the high precision turning.

#### **Future Plans**

- Probe lower BDRs and try to improve error on field (better gap measurement) in order to determine which is the better BDR vs. E model.
- Finish building HRR Circuit II.
- Use HRR Circuit II in conjunction with the fixed gap system to study BDR vs E for much larger cathode area, both with and without external magnetic field.
- Investigate memory effect/BD clusters further.