

# Instrumentation of a Level-1 Track Trigger in the ATLAS detector for the High Luminosity LHC



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on behalf of the ATLAS Collaboration



## Introduction

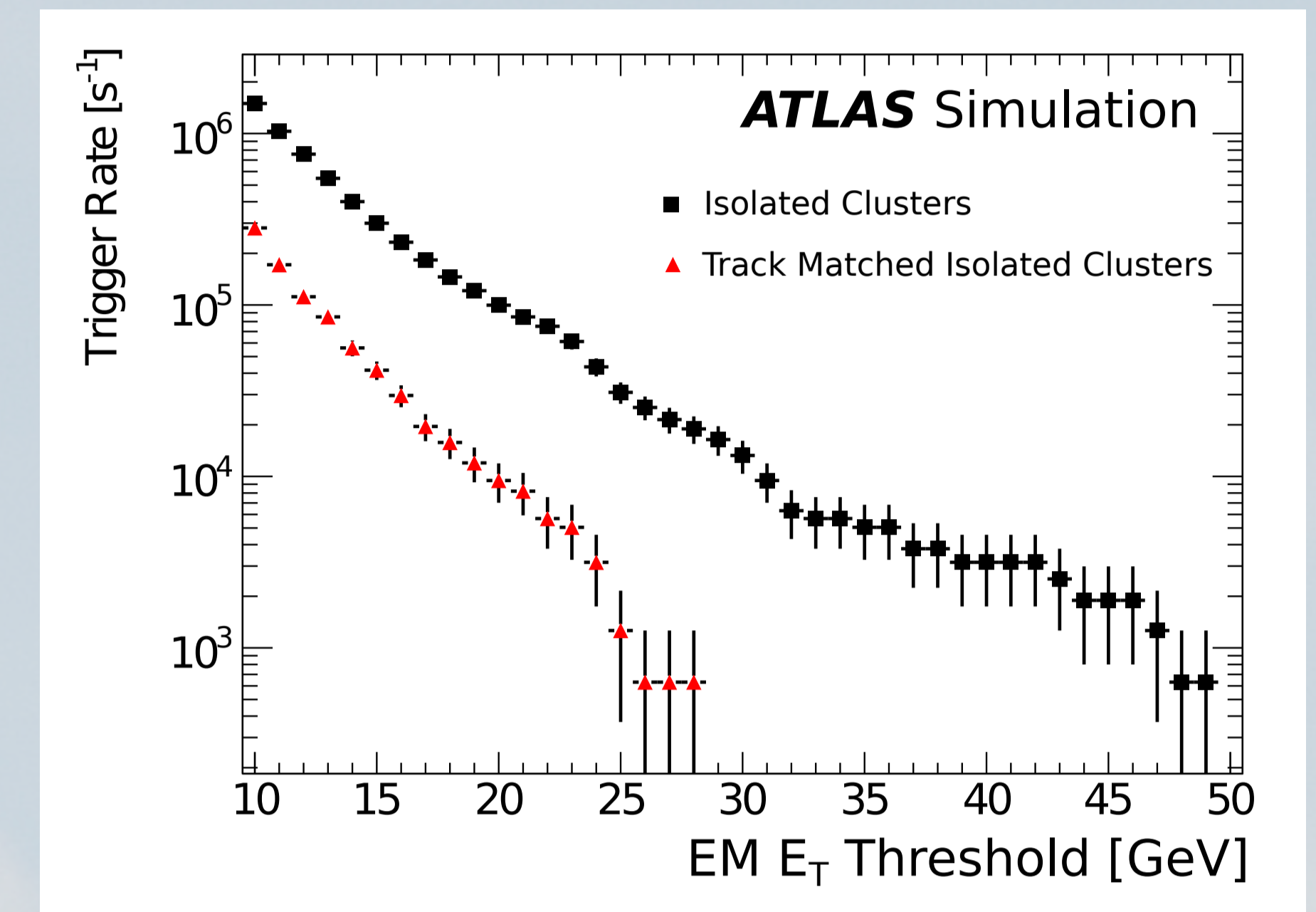
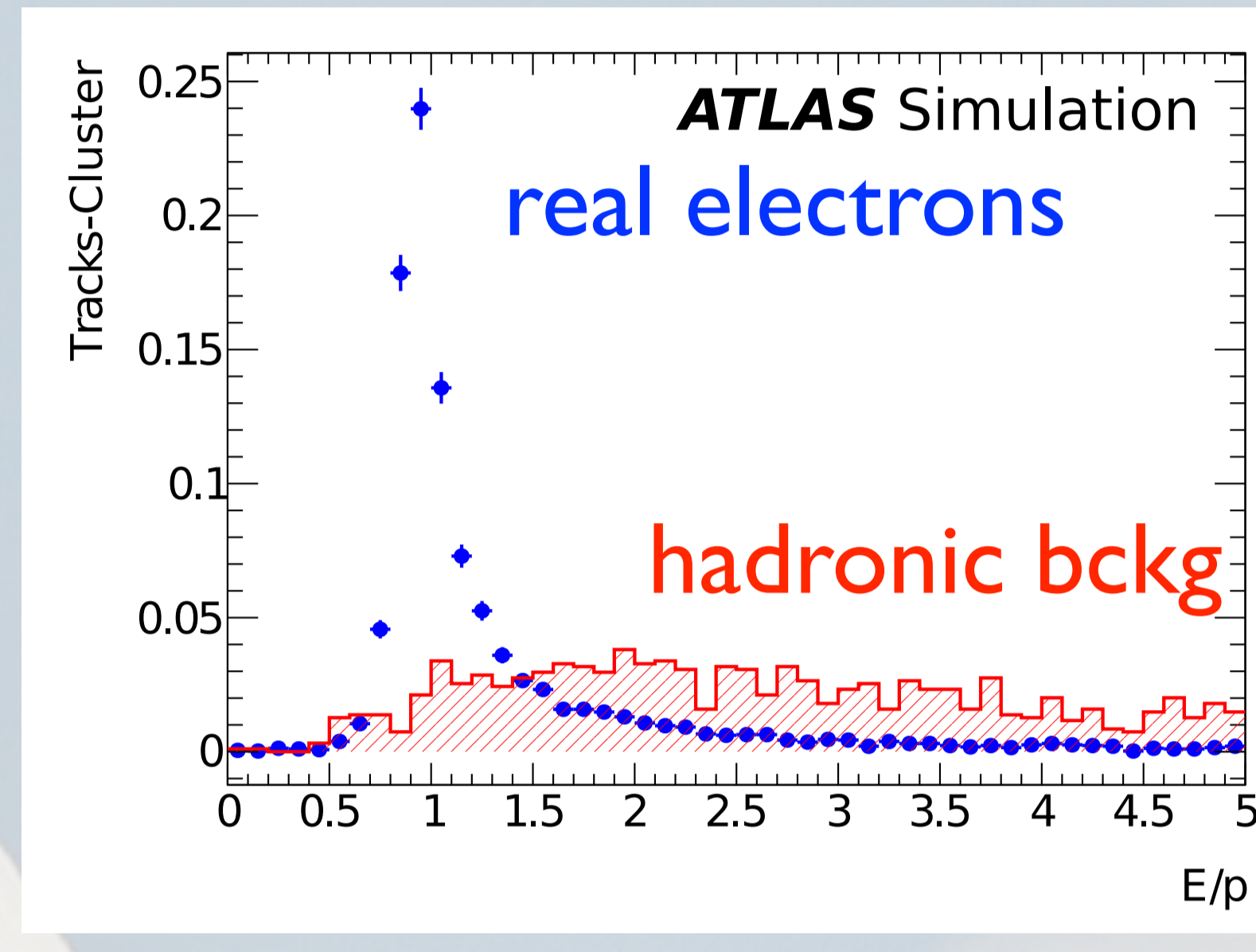
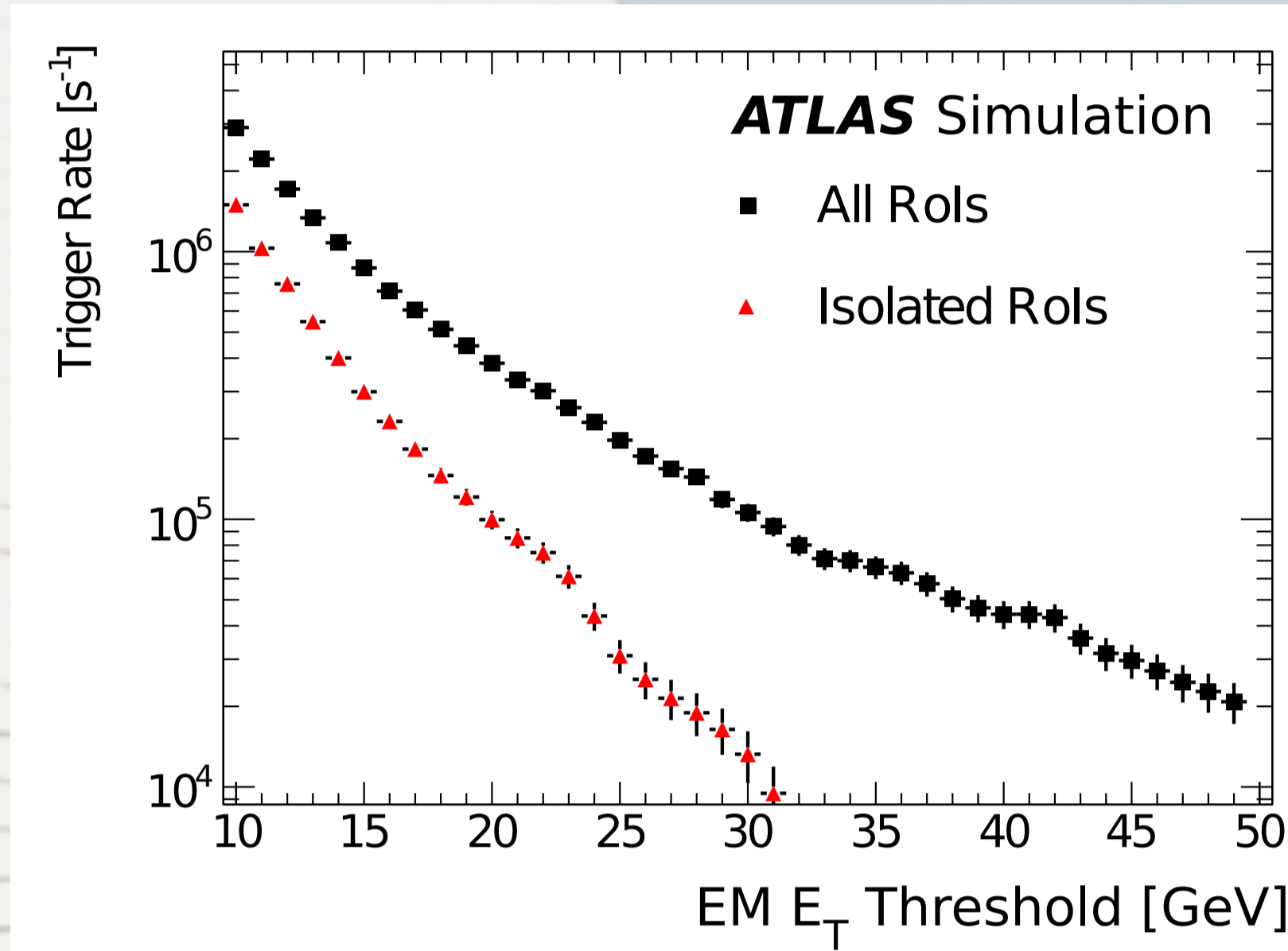
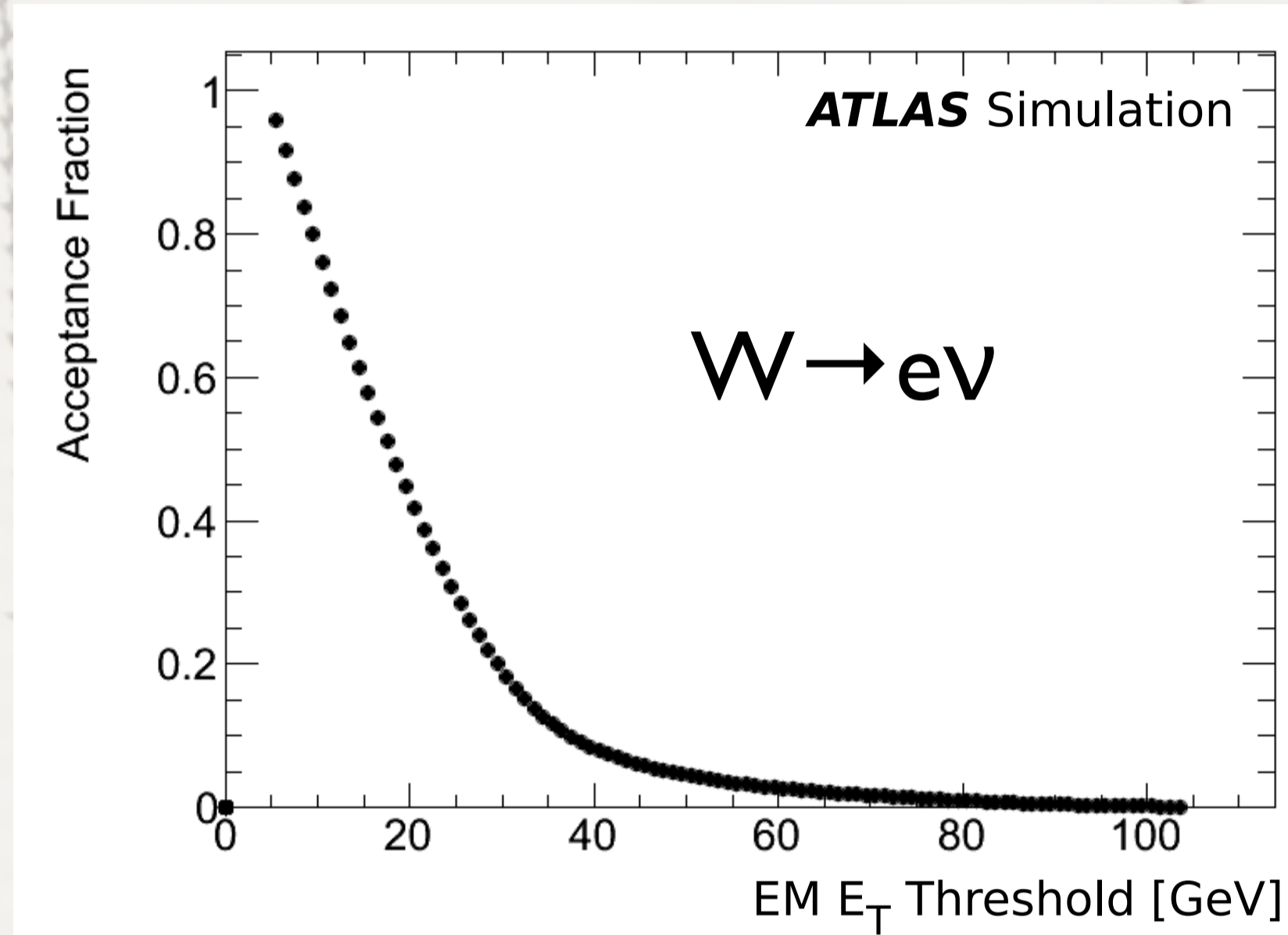
- HL-LHC in 2020:  $L=5 \times 10^{34} \text{cm}^{-2}\text{s}^{-1}$  and  $3 \text{ab}^{-1}$  of data per experiment
- Need lepton triggers at thresholds of  $p_T \sim 25 \text{ GeV}$
- But event rates:  $\sim \times 5$  and number of interactions per bunch crossing:  $\langle \mu \rangle \sim 120$

## Current ATLAS L1 Trigger and upgrades

- Hardware L1: 40 MHz to 75 kHz, software L2 (RoI scheme): 3kHz, Event Filter: output rate:  $\sim 300 \text{ Hz}$
- L1: Coarse calorimeter and muon information, latency  $< 2.5 \mu\text{s}$
- Want to keep single lepton trigger rate below 20kHz
- During the long shut down of  $\sim 2018$ :
  - complete replacement of Inner Detector tracker
  - replace almost all of the on-detector FE electronics
  - FE not replaced: muon drift tubes  $\rightarrow$  L1A  $\sim 200\text{kHz}$  and latency  $\sim 20 \mu\text{s}$

## Benefits of Tracking at L1

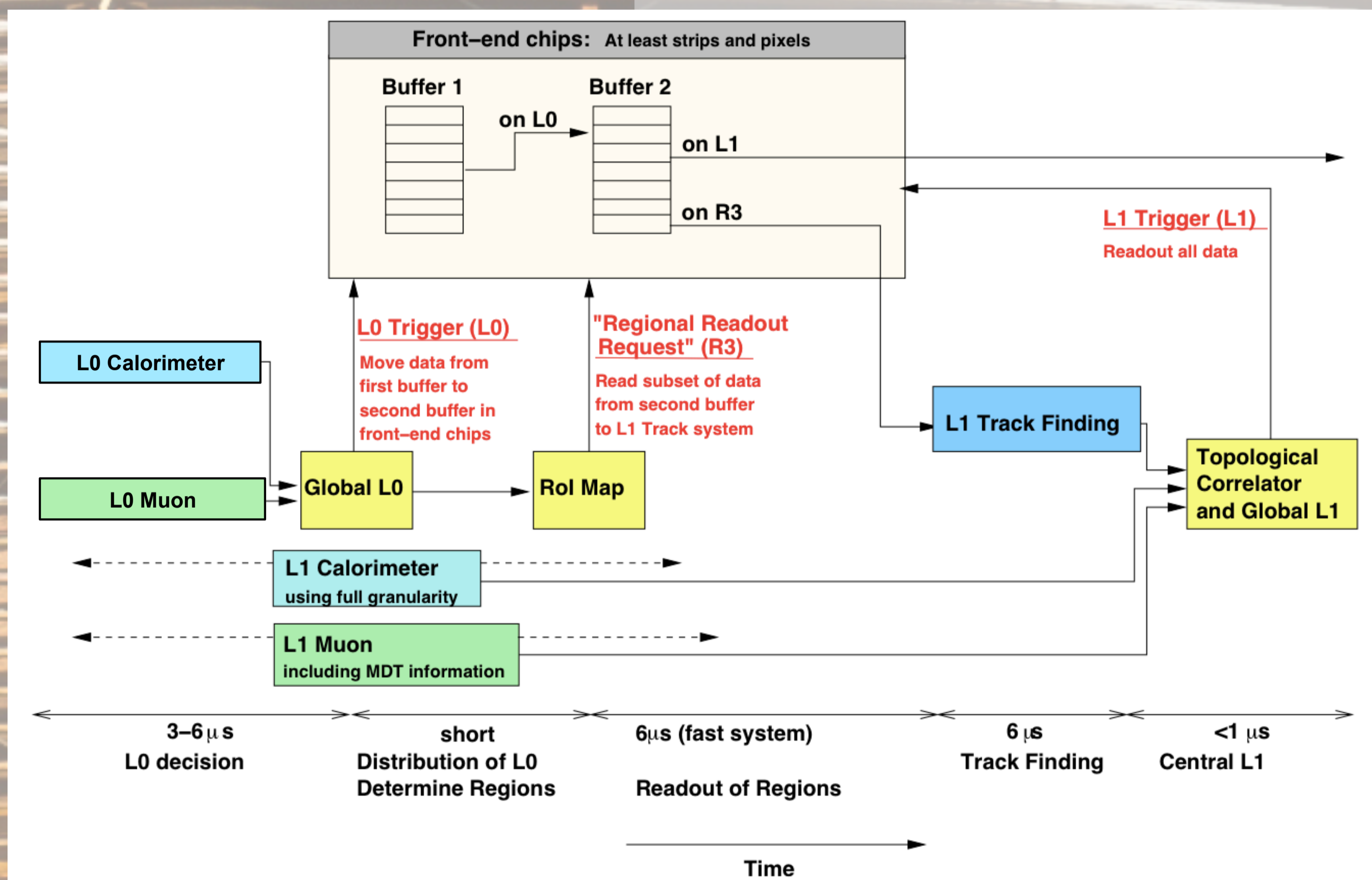
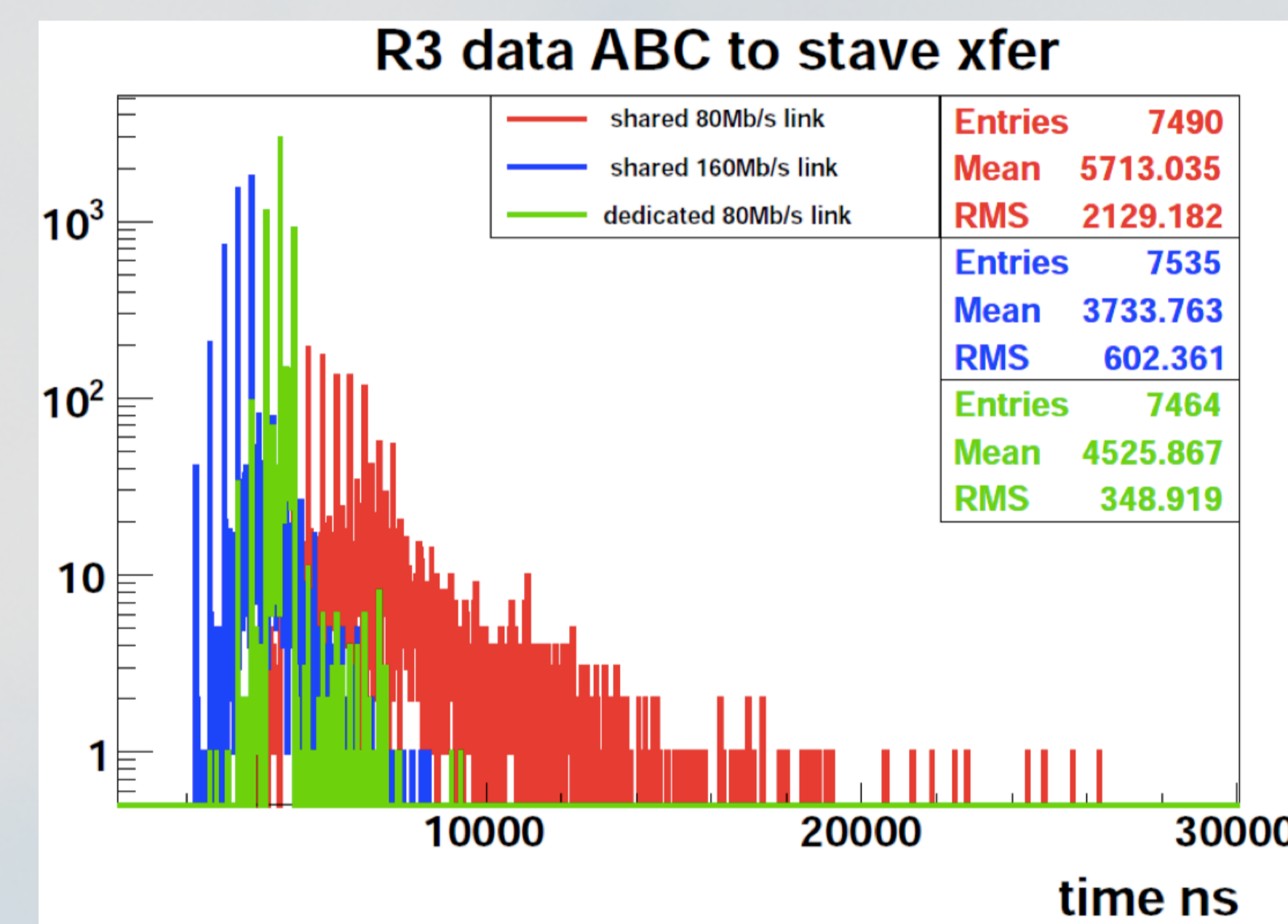
- Reduce L1A rate (eg current HLT)
- Ensure objects are from Primary Vertex
- Reduce fake couple of leptons
- Possible track based isolation
- Added flexibility to trigger system



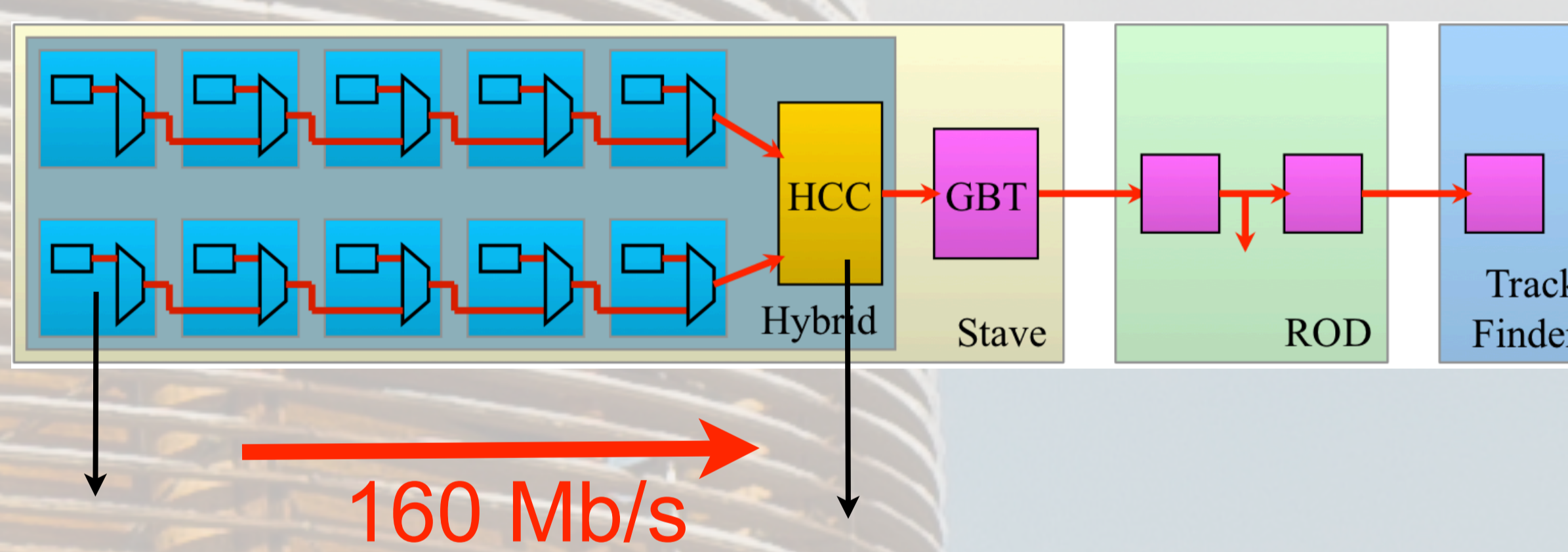
## Option 1: L0/L1 trigger design using a Double Buffer Front-End Architecture

- L0: upgraded Calo+Muon triggers, 500kHz, latency  $< 6.4 \mu\text{s}$ , builds RoI
- L1: Track Trigger, final L1 Accept within  $20 \mu\text{s}$

Bandwidth reduction:  
Bandwidth =  $(100\text{kHz} + 500\text{kHz} \times 10\%) \times \text{event size} = 150\text{kHz} \times \text{event size}$   
 $\rightarrow 1.5$  times bandwidth without track trigger



Schematic of FE readout configuration:



160 Mb/s

Results:

- For  $\langle \mu \rangle = 200$ , L0A=300kHz, L1A=75kHz, R3=3kHz (fROI=1%)
- 160Mb/s link shared between R3 and L1 is optimum
- 98.5% of R3 data is received within  $5.5 \mu\text{s}$
- separate buffers on the HCC for R3 and L1 (R3 prioritized)
- bandwidth required: 125 Mb/s (for  $\langle \mu \rangle = 140$ , L0A=500kHz, L1A=200kHz, R3=50kHz, fROI=10%)

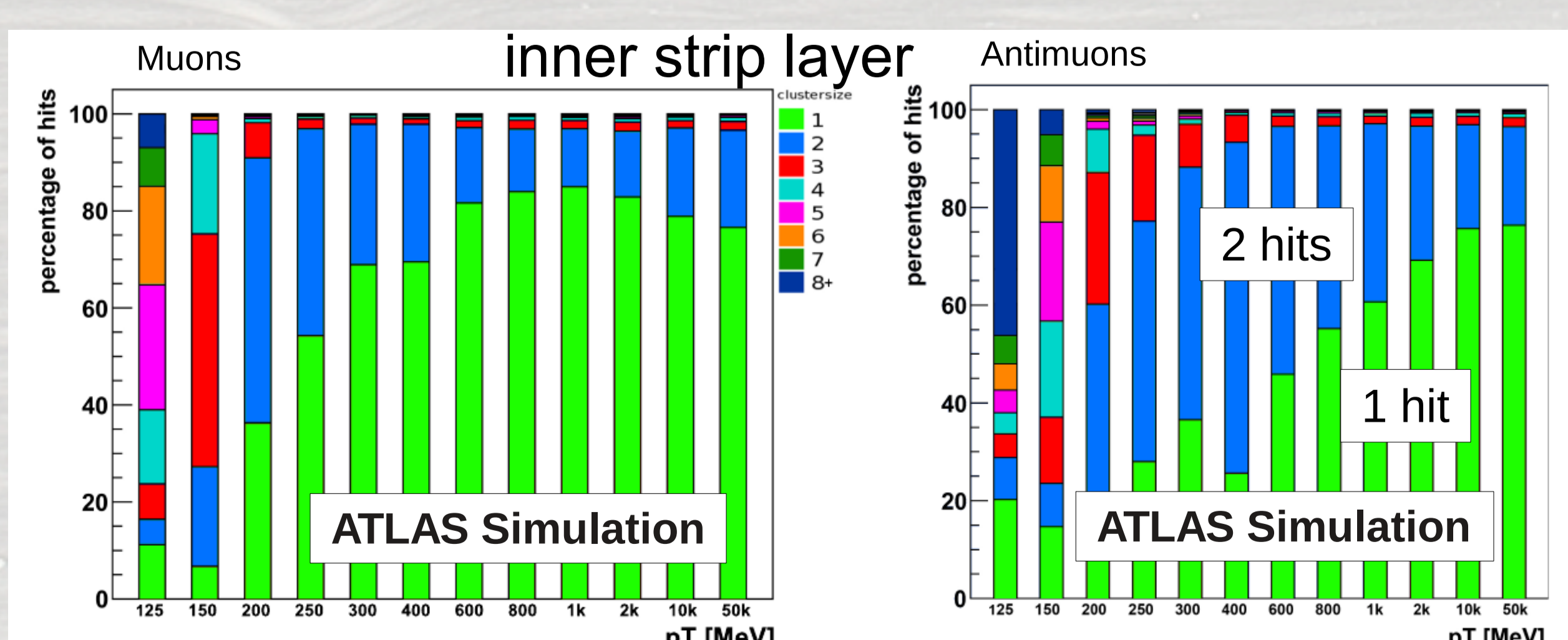
Benefits:

- minor changes to new tracker design
- low momentum tracks
- isolation criteria possible

## Option 2: Single hardware trigger level by using trigger layers in the new tracker

- filter high momentum tracks using doublet layer coincidences in closely spaced layers
- 2 local filtering methods studied, bandwidth reduction factor: 12-25

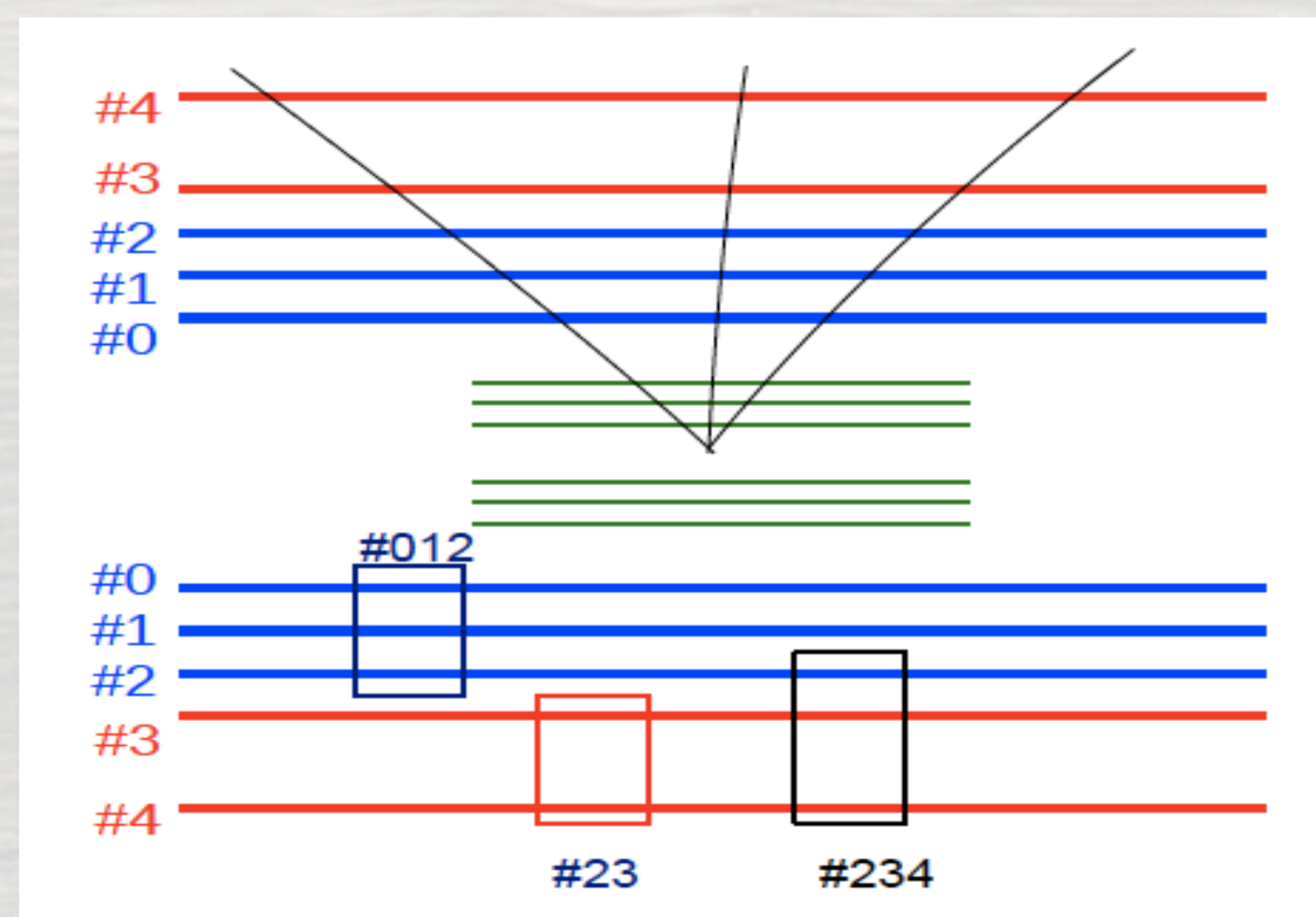
Cluster Size Method:



utopia design: strip sensors tilted by  $10^\circ$

- cluster size is reconstructed on FE
- lower  $p_T$  tracks  $\rightarrow$  charge sharing over several adjacent strips
- optimized cut:  $N \leq 2$

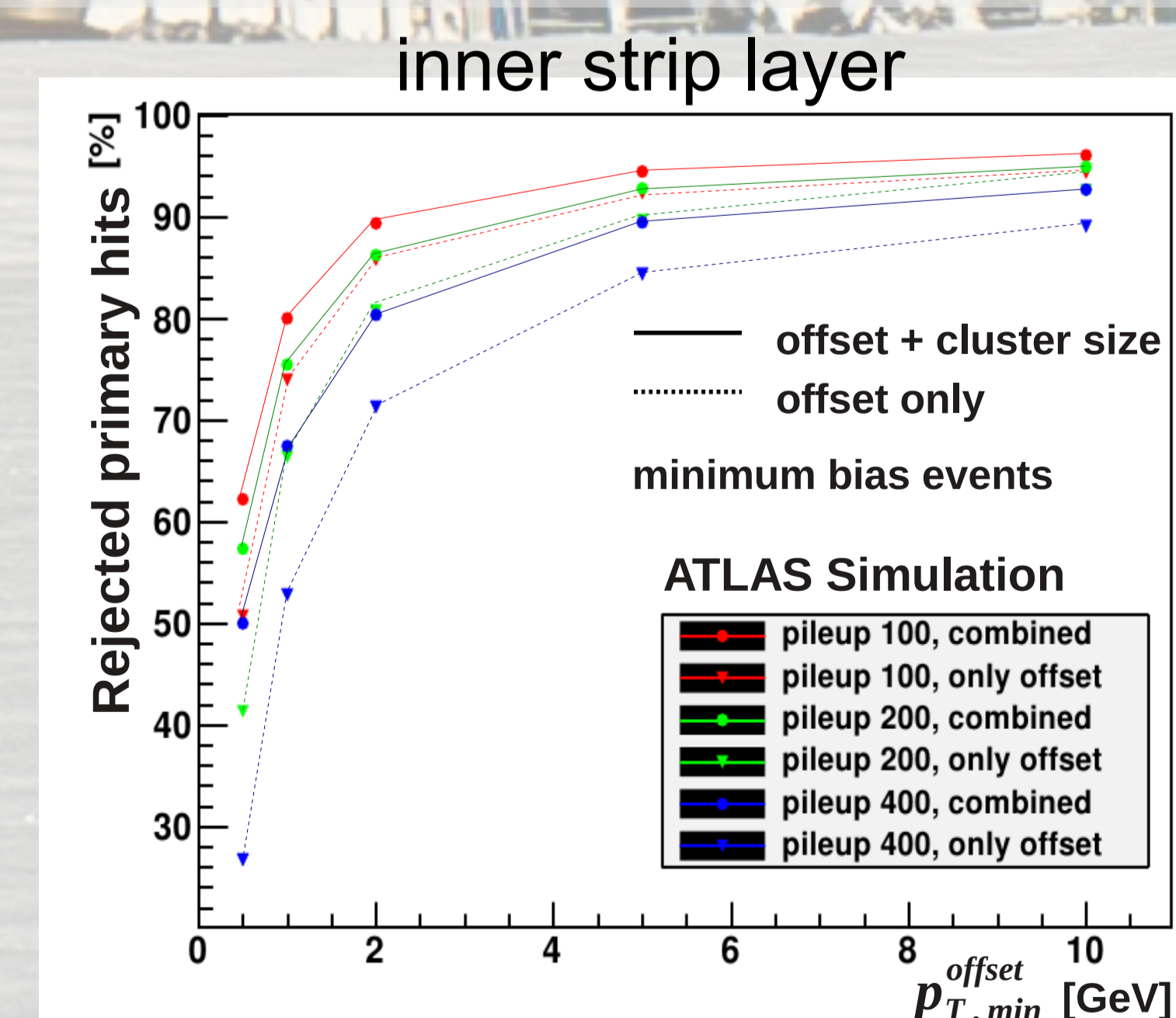
Offset Method:



- set stereo angle to 0
- match hits in inner and outer layers
- azimuthal distance related to  $p_T$
- Best is to combine Cluster Size and offset

Global track reconstruction:

- hits from 2 or 3 layers combined, helix fitted and  $\chi^2$  cut applied
- will be done by associative memory



Track trigger rates of order 1 MHz achieved for  $p_T > 10-15 \text{ GeV}$ , with  $\sim 75-80\%$  eff

Benefits:

- no major changes to FE of subdetectors
- finds all ID tracks
- redundant information

