#### Thermal characterization of Low Gain Avalanche Diodes





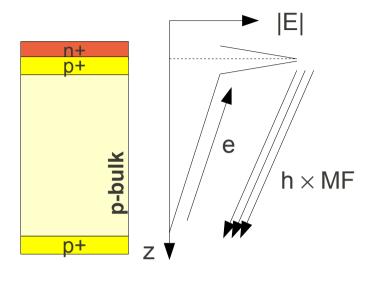
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- Conclusions

### LGAD = pad diodes with internal gain



Highly resistive p-wafer

P-well = multiplication layer below n-implant

-⊡--Gain @ 200 V -○--Gain @ 400 V -∆--Gain @ 500 V

High E-field, secondary ionizations

50

40

30

20

10

5,0x10<sup>12</sup>

Gain

Main **technological challenge**: tune doping to achieve gain while keeping breakdown voltage at a reasonable high value.

More info: [1] S. Hidalgo, 22<sup>nd</sup> RD50 Meeting Albuquerque 2013 [2] G. Pellegrini, Hiroshima symposium, Sept. 2013 Small modifications in the Boron implant dose (~ 2 × 10<sup>12</sup> cm<sup>-2</sup>) induce great changes in Gain and V<sub>BD</sub>

1,0x10<sup>13</sup>

Player Implant Dose (cm<sup>-2</sup>)

1D Simulation @ Pad Centre

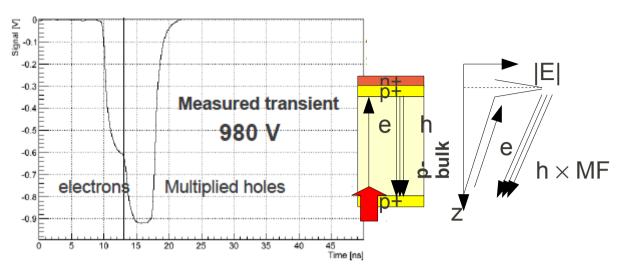
1,5x10<sup>13</sup>

Breakdown Voltage (kV)

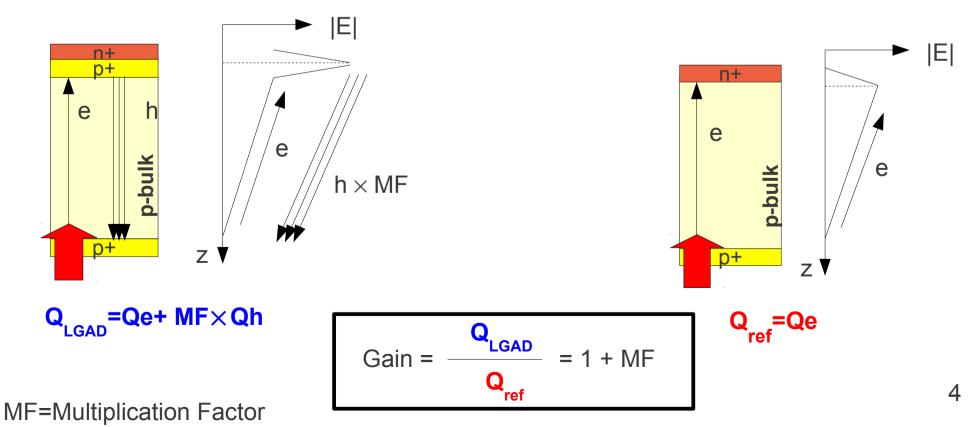
### **Calculation of gain**

Several methods available:

1) If the gain is high, we can easily separate e drift from hole drift and integrate the pulse directly



2) Comparing collected charge of a **LGAD** (left) with one **reference** detector (right) without amplification (wafer 9)



# **Available samples**

LG Avalanche Diodes available at CERN & IFCA-Santander:

SR	2	W7F3, <b>W7J9</b>
DR	6	W7D2, W7B4, W7H8, W7D8, W7H10, W8D2
SC	7	W7C3, W7C5, W7G7, W7E9, W8C3, W8C5, W8K9
DC	8	W7I10, W7E2, W7E4, W7E6, W7I8, W8C4, W7C4, W7E10

Reference diodes W9 (same run, different wafer), no multiplication

SR	2	W9F11, W9H11
DR	6	W9D10, W9H10
SC	7	W9E9, W9E11
DC	8	W9E8, W9G8

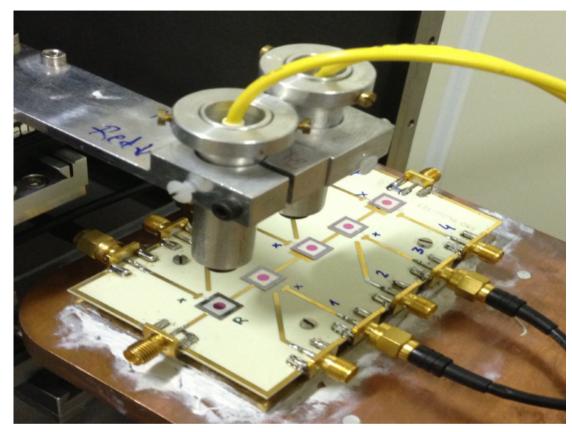
#### **Color legend:**

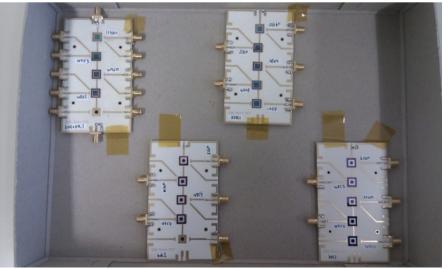
Temp measurement  $\leftrightarrow$  Mounted in mTCT board Mounted in single boards (for Santander)  $\leftrightarrow$  XY scan

#### Nomenclature of the diodes"RingWindow":

Sx=Single N diffusion (JTE)Dx=Double N Diffusion (JTE+Ring)xR= Rectangular window,xC=Circular window

#### **TEMPERATURE SCANS**





SSD facility at **CERN** 

**Up to 5 diodes** (4+1 reference) mounted in a mTCT board

**2 laser heads** (red/IR), fiber splitted into 4 ends: top red/IR, bottom red/IR.

Laser fixed, diode selected using **1D stage moto**r

**Board on Cu support,** cooled using a Huber Unichiller CC3. Temp read out on-board extreme using PT1000.

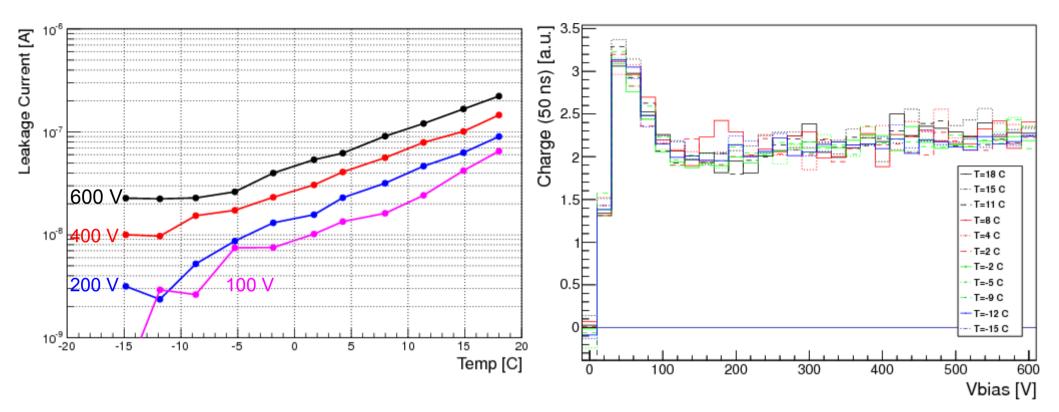
Temp sequence T=20C..-15C in steps of ~5C.

Top side is biased, backplane grounded.

Red bottom injection.

### LGAD: temperature effects All measurements are red TCT, rear illumination

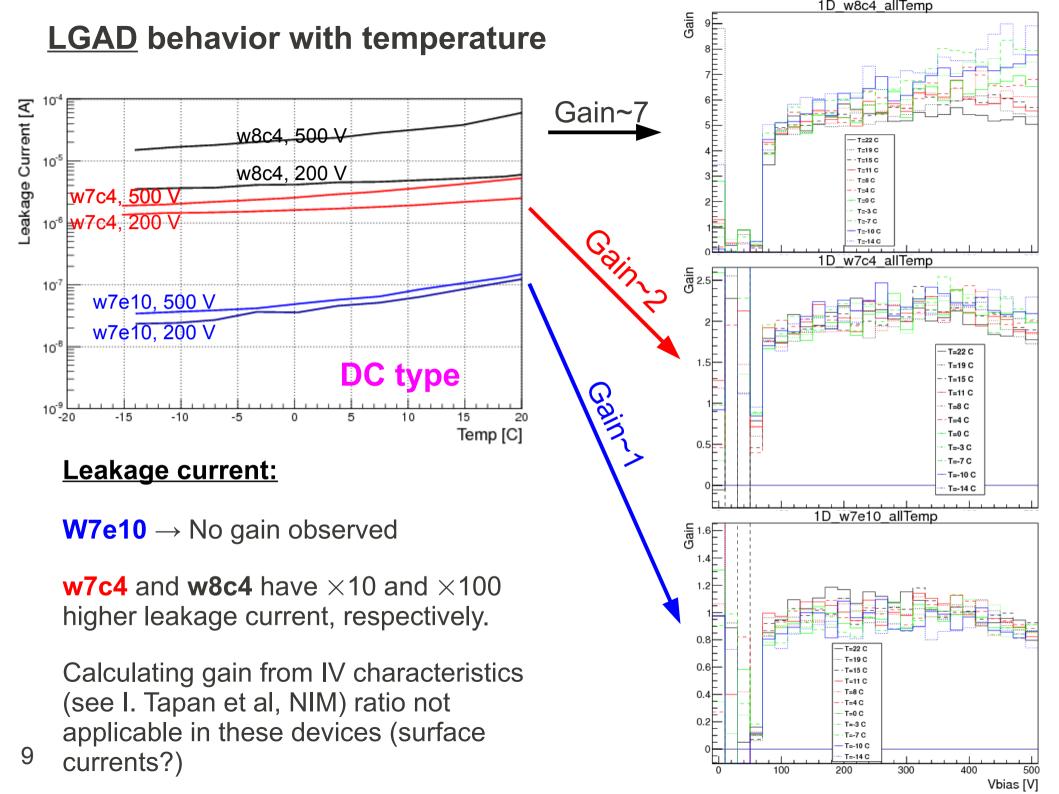
#### Standard diode, behavior with temperature (DC type, w9g8)



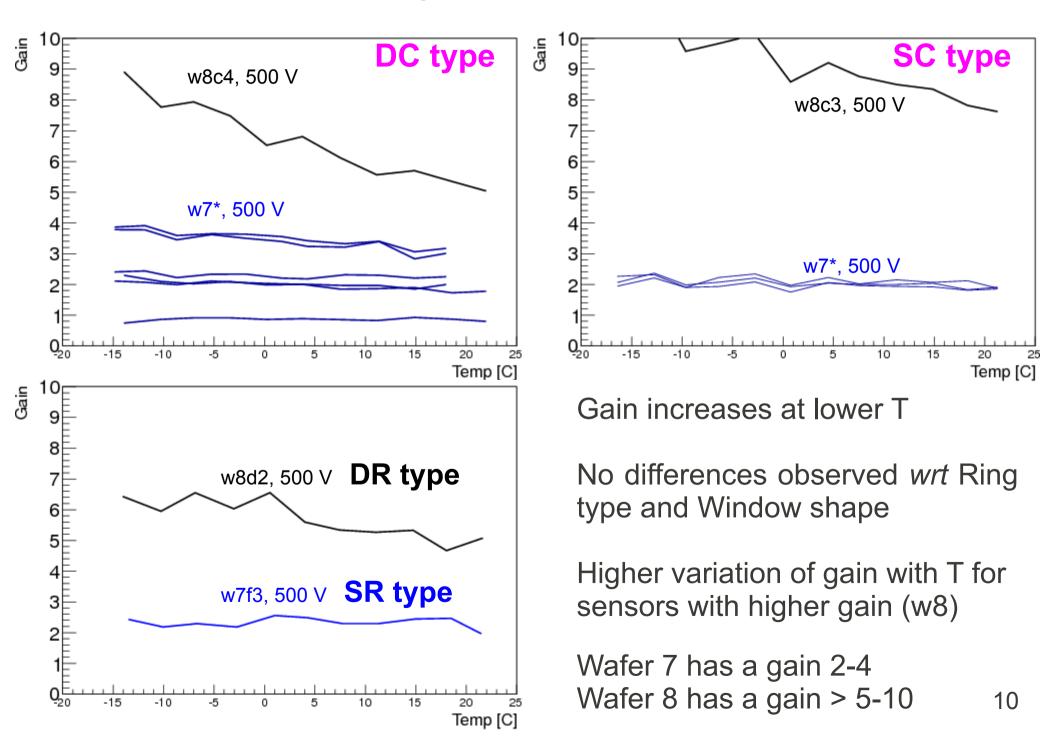
Leakage current decreases with T, as expected

# Collected charge does not depend on temperature

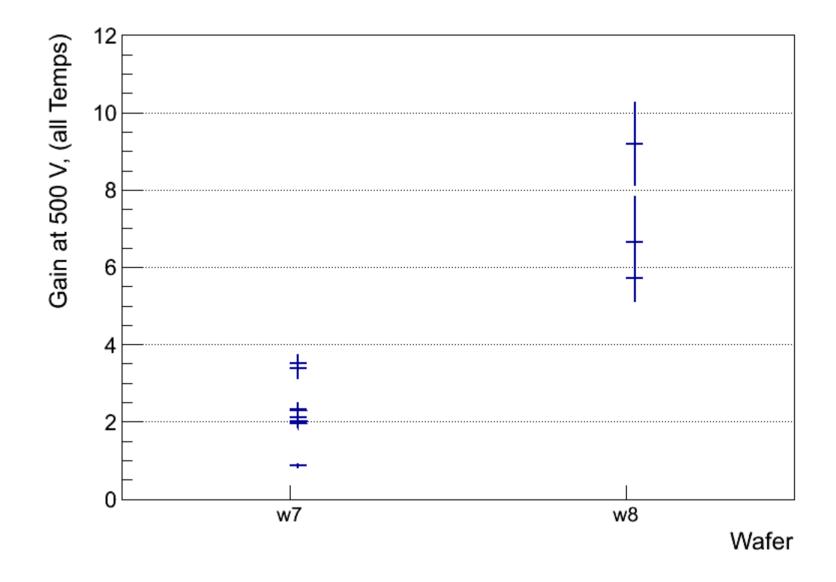
Gap does not change with T, within T $\subset$ [-20,20] C  $\rightarrow$ absorption in Si for red light does not change in this T-range.



#### **LGAD** behavior with temperature

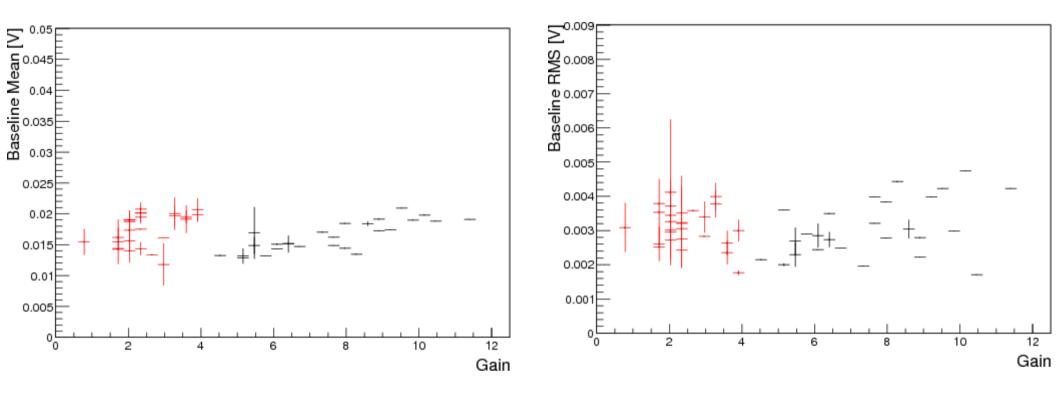


## Gain vs wafer



Error bar is spread due to variation of gain with temperature (RMS)

### Noise vs Gain, irrespectively of T



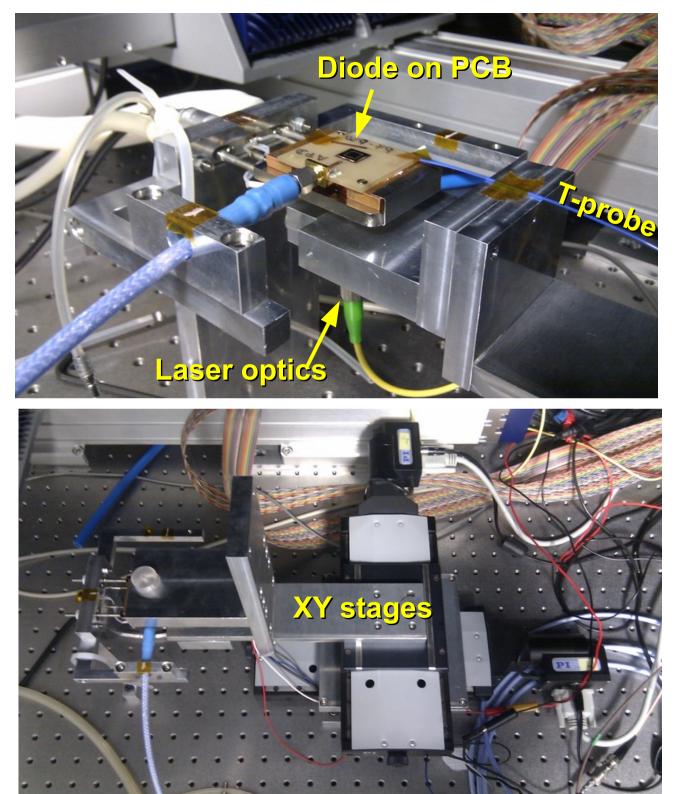
Neither the baseline mean, nor the RMS of the baseline depend on the gain. Measurements at different temperatures are shown here.

#### Note:

The baseline is defined as the signal before the actual current transient. For these measurements we have integrated the baseline over 15 ns before the transient. The scope does 256 averages before acquisition.

# LGAD: gain uniformity

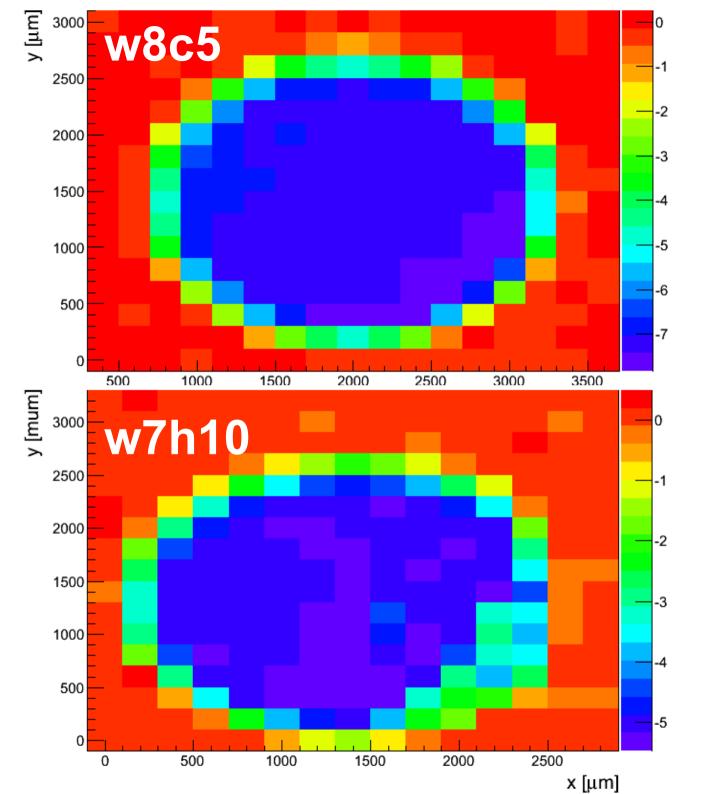
All measurements are red TCT, rear illumination



Diode mounted on Peltier element (Linkam)

Red TCT, rear incidence

XY stages



Showing Charge mappings:

Q=Q(x,y)

Very good uniformity

Circular motif seen is actually the window in the cooling support (to allow rear illumination).

#### Conclusions

13 LGAD (+5 references) have been measured at different temperatures

By measuring the reference diodes (no amplification) we observed that absorption of red light in Si does not change with T in the range [-20,20] C.

Charge collected vs bias voltage has a flat response in ref.diodes

- Higher gain detectors, also have higher leakage current
- Gain increases as temperature decreases

Gain for wafer 7: 2-4 Gain for wafer 8: 5-10

• The electronics noise (calculated from the baseline) does not depend on the gain.

Even if each sensor is different, the uniformity of gain within each device is very good.

Next steps? Proton irradiation in "fine" steps. I would take devices with highest amplification when unirradiated. tbd