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# The CBC2 ASIC for 2S Modules at HL-LHC

*Tuesday 8 April 2014 16:45 (15 minutes)*

I will present the front-end readout ASIC for the High-Luminosity Upgrade of the CMS Strip Tracker. The CMS Binary Chip 2 is a full-scale prototype ASIC with 254 channels, capable of identifying high transverse-momentum trigger primitives by correlating hits between the two closely-spaced silicon strip sensors of the Tracker module. I will present the performance of the chip, together with the plans for its final version and an overview of current R&D activities, which include a first beam test of a prototype module and preliminary irradiation testing.

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