



Production Summary 10/'07-02/'08

- 160M Monte Carlo events
 produced since October 07
 - On request of Physics, DPG and HLT groups
- Total CSA07 event counts:
 - 80M GEN-SIM
 - 80M DIGI-RAW
 - 80M HLT
 - **330M RECO**
 - 250M AOD
 - 100M skims (mixed RECO/AOD)
 - 920M events
- Events were processed + reconstructed in several steps, several times





- CSA07 signals:
 - 80M GEN-SIM, 80M DIGI-RAW, 80M HLT, 80M RECO
- CSA07 soups: 250M RECO, 250M AOD, 100M skims
 - Three calibrations applied: 10/pb⁻¹,100/pb⁻¹ 0/pb⁻¹
 - Events produced: RECO, EXPRESS, AOD, skim, ALCARECO
- The CSA07 signal samples really evolved over time. We started from 50M and went up to 85M by now (not a real problem)
- Data volume of CSA07 samples right now (without counting repetitions): 1.9 PB
 Capacity and Free
- Delivery of the samples is mostly done with small remainders pending.



CMSA data in CASTOR@CERN: 3.7PB



- In CSA07 a lot was learned and a lot was achieved...
- The production infrastructure is in full operations
- CSA07 analysis identified tasks to be addressed
 - For Offline: detailed list of development work
 - Two strategies for Computing:
 - Integrating development, deployment and commissioning in a new task force:

Processing And Data Access (PADA)

- coordinated by I.Fisk and J.Hernandez
- Testing the computing infrastructure in CCRC08/CSA08 in February and May '08

1) Detector Installation, Commissioning & Operation	Oct	2) Preparation of Software, Computing & Physics Analysis
	Nov	S/w Release 1_7
Tracker Insertion	Dec	2007 Physics Analyses
	Jan	First Results Out
	Feb	S/w Release 1_8 Functional Tests CSA08 (CCRC)
Test Magnet at low current	Mar	
CMS Cosmic Run CCR_0T Beam-pipe Closed and Baked-out GREA: Global Run End of April	Apr	S/w Release 2_0 CCR_4T, Production startup MC samples
1 EE Endcap Installed, Pixels installed	Мау	CCRC08, CSA08 (iCSA08) Combined Computing Readiness Challenge
2nd ECAL Endcap Ready for Installation	Jun	S/w Release 2_1 All basic software components ready for
	Jul	fCSA08 or Beam



Processing and Data Access: PADA

- The Processing and Data Access Task Force is a series of tasks and programs of work
 - designed to bring the elements of the Computing Program into stable and scalable operations.
- It covers the integration, commissioning and scale testing of data processing workflows at the Tier-1 and Tier-2 sites and includes:
 - Validation of the infrastructure for organized processing and user analysis
 - including the sites and the CMS workload and data management tools.
 - Validation of the distributed production system
 - performing functionality, reliability and scale tests.
 - Help sites to commission, configure and optimize the networking and storage through scale testing data transfers and data processing.
 - Improvement in the efficiency of accessing data across the CMS computing system from global transfers to local access.



PADA tasks + schedule

Transfer Commissioning (DDT Phase 2)

Site Commissioning

CMS Service Testbed

Production Component Validation

Analysis Server Validation



Monitoring and Information Integration

Dynamic Tier-2 Data Management

User Driven Organized Processing

Distributed Analysis Functionality and Scale Testing

Succeeded to find names • for 5 tasks, more to go





Data recording at CERN

1a) readout from P5, use HLT, w. stream definition, use Storage Manager, transfer to T0, perform repacking, write to CASTOR (D.Hufnagel)

- Goal: verify dataflow for CMS, commission the new 10GB fiber
- Status:
 - 13.2.08: First successful transfer on new 10 GB fibre at 100MB/s (limited by transfer node)
 - Next step: integrate into transfer system run in parallel to normal data transfers



CCRC08 test: Data recording at CERN

1b) CASTOR data archiving test (M.Miller / DataOps team)

- Goal: verify CASTOR performance at full CMS and ATLAS rate
- Status:

very successfully completed, reached rate of 1.5 GB/s

- Good coordination with CERN-IT, quick response
- Test at all-VO rate, other VO's didn't stress the system





CCRC08 test: High Rate Processing at T0

Coordinated by M.Miller / DataOps

Goal:

- "high-rate" processing of cpu/RAM limited jobs
- Originally: measure interaction with other VO's on same WN BUT: CMS does not share WN with other VO's @ CERN (for now)

Setup:

- regular operations (physics requests)
- ReReco with 0pb⁻¹ conditions of Stew and Gumbo

Status:

- started with 41k jobs of the 80 TB Stew AllEvents
- Finished in expected time
- Not much action from other VO's, no sign of WN problems
- Again turning into a CASTOR I/O test



CCRC08 test: High Rate Processing at T0

Wednesday snapshot

Summary:

- processing runs routinely
- Small level of IO errors (2%), cured by retries
- Will test: copy files to local disk







CCRC08 Transfer tests

Goal: use SRMv2 data transfers where possible

Target rates:

- T0-T1: 25/40/50% of full 2008
- T1-T1: 50% in+outbound
- T1-regional-T2: full/high rate
- T2-regional-T1: full/high rate

A detailed Plan worked out:

- cycle through different parts of all link combinations per week

Status of SRM-v2 deployment (~5.2.08)



Tests are progressing well

- individual problems are being addressed and result in delayed testing
- More detailed analysis available at the end of February



Coordinated by G.Gomez-Ceballos, Josep Flix

Goal: measure performance of:

- Migration from Tape to Buffer: pre-stage test.
- Reprocessing exercise: use all available CMS CPU-slots at T1s

Plan:

- Select one (or more) dataset(s) of ~10TB size existing at T1.
- Remove all the files from disk (aka, T1_Buffer).
- Fire the staging from Tape to Buffer of all files.
- Monitor the process and provide some measurements/plots
- Run Re-reconstruction over CSA07 data present at all T1s
 - Measure performance



CCRC08 Re-Reconstruction tests

Status:

- buffer to tape migration successfully finished at all sites
 - Results: total staging time 8-44h, rate: 67-250MB/s observed
 - Except IN2P3, performance was poor, reconfigure and redo



- high performance processing without overlap with ATLAS
 - Finished at FNAL(1200 slots), CNAF(1000-1300 slots), FZK(600 slots), ASGC(300 slots)
 - Next to go: IN2P3 and PIC, RAL, but no problem foreseen
- Processing test together with ATLAS planned at two Tier-1's:
 - special queue for Atlas and CMS is setup at IN2P3 and PIC



Coordinated by DataOps

Goal: Production tests of FastSim Monte Carlo

Status:

- Physics groups want to use 50M of the CSA07 samples (100pb⁻¹ calibration), reading AOD's.
- Fast Simulation production based on CMSSW_1_6_9 ready to start
 - cfg file which can run over RECO and AOD files is tested
 - data ops ready to start the production (data handling: use resources at T1 or ship AOD data to T2)



CCRC08 CAF tests

Coordinated by P.Kreuzer

Goal:

- ramp-up CAF resources
- verify basics CMS use cases at scale

	CPU 70% Dual quad-core (16GB RAM) 30% Dual Dual-core (8GB RAM)	Disk	Таре	
T0 2008	3000 slots (1000 slots in '07)	400 TB (420TB in '07)	3 PB	
CAF 2008	1200 slots (128 slots in '07)	1600 TB (35 TB in '07)		
CAF CCRC'08	250 slots	150-200TB		

Status: good progress made

note: 3000 slots =~5.3MSI2K , 1200 slots =~ 2.1MSI2K

- resources configured according to plan
- Regular CAF meetings with user representatives (Global Run, ALCA and Physics)
- Plan for CCRC08 (week 3 and 4):
 - Transfer GR data fromT0 to CAF and populate local DBS
 - Finalize RPC workflow
 - Test/Run HcalCallsoTracks workflow
 - Test/Run Muon Alignment workflow
 - Setup and test CRAB, local submission
 - Collect list of CAF groups and users per group. Provide to IT, both for batch/interactive CAF



Computing Summary

- The Computing infrastructure is fully utilized for ongoing production
 - Finished original CSA07 production (and much more)
- Detailed analysis of CSA07 performance was performed.
 - Direct result for Computing: defined PADA tasks and CCRC08 functional tests
- The PADA taskforce addresses deployment, integration, commissioning and scale testing. It will bring the elements of the Computing Program into stable and scalable operations.
- The CCRC08 functional tests in February complements CSA07 and test additional functionality:
 - Important scale testing at T0
 - Data-handling and full-scale reprocessing at T1
 - Data transfer tests using the new SRMv2 protocol
 - Role-out of Fast Simulation for Monte Carlo Production
 - Ramp-up of CAF resources and scale testing of CAF use-cases
- Detailed planning of CCRC08(May), iCSA08 and f-CSA08 is going on, expect to agree on initial scope and goal during CMS week.





Last 2 weeks: integrated tape system usage

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Rates ultimately limited by IGbs on 13 t0input servers

CMS CASTOR TEST - Performance observed:

Averaged 633 MB/sec write (I.I GB/sec during test)

Averaged 548 MB/sec read (~400 MB/sec during test)

Read Spike: regular stagein, 101 drives => 5 GB/sec