



# HEPiX Spring 2014 Workshop

## Tuesday 20 May 2014

### Computing and batch systems: Computing and batch systems - Auditorium Marcel Vivargent (14:00 - 15:40)

-Conveners: Michele Michelotto; Gilles Mathieu; Wolfgang Friebe

time	[id] title	presenter
14:00	[15] Batch System Review	Mr JOUVIN, Michel
14:25	[25] A Year of Condor at the RAL Tier 1	COLLIER, Ian Peter
14:50	[11] Future of Batch Processing at CERN	PEK, Janos Daniel
15:15	[57] The Art of Running HTCondor as a batch system	BOCKELMAN, Brian Paul

### Computing and batch systems: Computing and batch systems - Auditorium Marcel Vivargent (16:10 - 17:25)

-Conveners: Michele Michelotto; Gilles Mathieu; Wolfgang Friebe

time	[id] title	presenter
16:10	[12] Support for Linux Control Groups	Mr GRUBER, Daniel
16:35	[16] CC IN2P3 experience with Univa Grid Engine	POULAT, Suzanne
17:00	[21] Scheduling of multicore jobs	RAUSCHMAYR, Nathalie

# Wednesday 21 May 2014

## Computing and batch systems: Computing and batch systems - Auditorium Marcel Vivargent (16:35 - 17:50)

-Conveners: Michele Michelotto; Gilles Mathieu; Wolfgang Friebe

time	[id] title	presenter
16:35	[9] Intel Ivybridge vs. AMD Opteron: performance and power implications	Dr WONG, Tony
17:00	[38] Beyond HS06 - Toward a New HEP CPU Benchmark	ALEF, Manfred
17:25	[46] Evaluation of avoton CPU	CHIERICI, Andrea