



# Performance Plots for Proposal

## Plot Categories

- 1) Verification of New eFastOR Simulation
- 2) Low-Level Data-MC comparisons
- 3) eFastOR Performance Predictions



# Verification of New Simulation



## Plots to Produce

- Monopoles: timing, other monopole diagnostics?
- Old aspect of simulation: LT, HT hit maps,...
- New aspects of simulation: ToHT for streamers, electrons, pions,....
- etc....

## Plots for Proposal

- probably should count on 2 – 4 plots

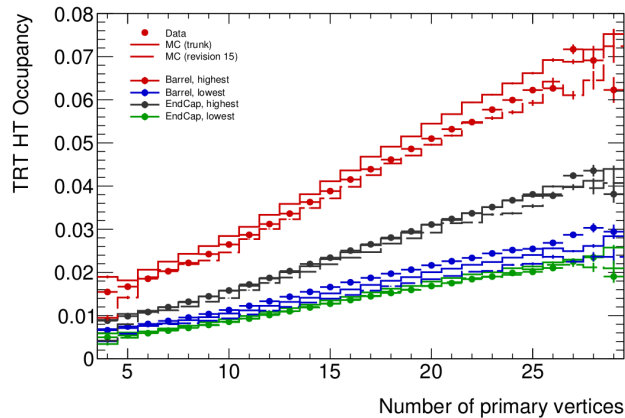


# Low Level Data-MC Comparison

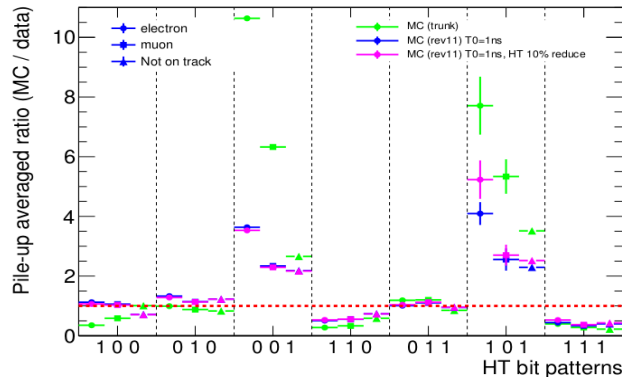


## Plots to Produce

- HT occupancy vs  $\langle \mu \rangle$  (KyungEon)

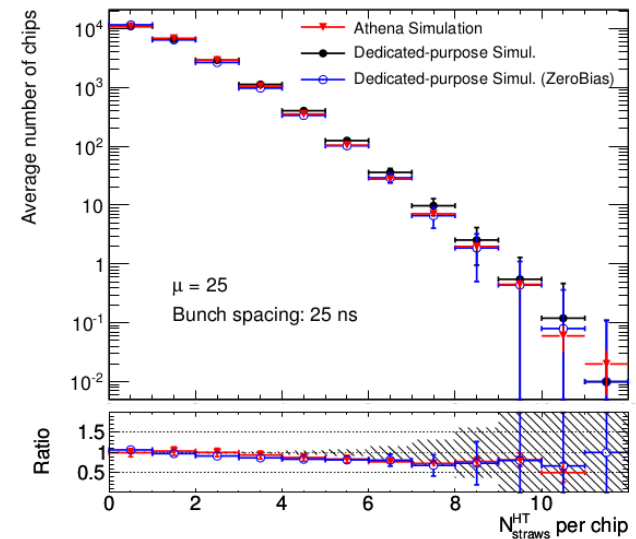


- HT bit patterns and lumi dependence (KyungEon)



## Comp to Dedicated Sim

- $N_{HT}$  straws per chip distrib: MC vs dedicated simulation (Ximo)



## What Else ?

- what should we include in the Proposal ?

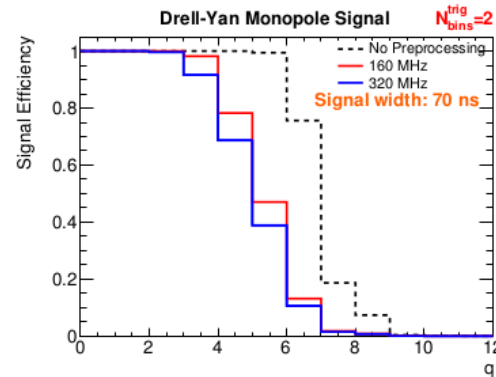
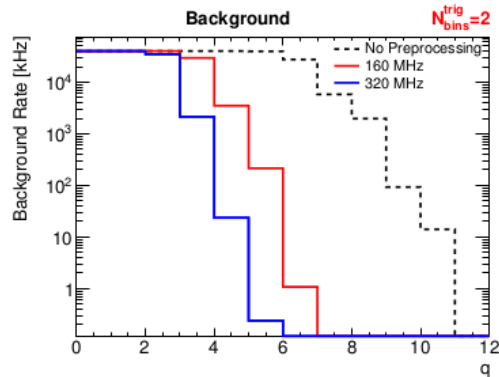
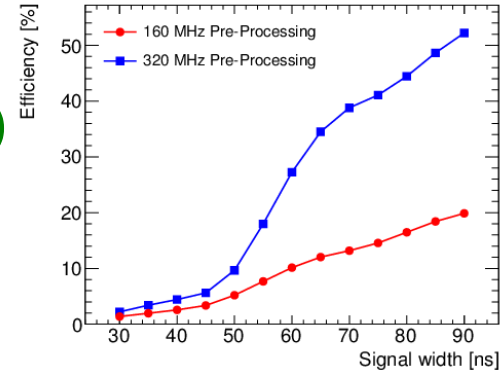


# Performance Predictions



## Plots to Include in Proposal

- Efficiency vs Signal Width (old plot)
- Bgrd (and eff) vs occupancy cut
  - > or other algorithm tuning



- Bgrd Rate vs Efficiency at different lumi's
  - > fixed timing & algorithm cuts
- Other performance plots...