

Session Program

19-23 May 2014

Front End Electronics (FEE 2014)

Break & Poster Session

Argonne National Laboratory, Energy Sciences Building
Argonne National Laboratory 9700 S. Cass Avenue Lemont, IL 60439

Tuesday 20 May

10:55

Break & Poster Session

Poster Session |

Location: Argonne National Laboratory, Energy Sciences Building, Argonne National Laboratory 9700 S. Cass Avenue
Lemont, IL 60439

CMOS ASICs for MicroBooNE and LBNE

Speaker

Hucheng Chen

Self-Triggered Front-End ASIC for ATLAS Muon Upgrade

Speaker

Gianluigi De Geronimo

Development of MAPS Based Telescope for Neutron Spectroscopy

Speaker

Maciej Kachel

DSSC-System

Speaker

Matteo Porro

Jungfrau and Gotthard: charge integrating detectors with automatic gain switching for SwissFEL

Speaker

aldo mozzanica

Time-Based Silicon Strip Readout ASIC for the PANDA Detector at FAIR

Speaker

A Goerres

Serial Powering for Silicon Strip Detectors

Speaker

Mitchell Franck Newcomer

VIPRAM

Speaker

Siddhartha Joshi

11:25