

AMICSA 2014 - Fifth International Workshop on Analogue and Mixed-Signal Integrated Circuits for Space Applications

Tuesday 1 July 2014

Applications for Radiation Hardened Analogue and Mixed-Signal ASICs: Read-Out-IC - 503/1-001 - Council Chamber
(09:30 - 10:50)

-Conveners: Francis Anghinolfi

time	[id] title	presenter
09:30	[15] FAIR, a front-end ASIC for infrared detector readout	Dr SCHRADER, Jan-Rutger
09:50	[44] Analog Front End Integrated Circuits for Mixed Signal Spacecraft Applications	FERGUSON, Bruce
10:10	[28] Design and test of MROD, a 2 Channels Video Chain Mixed-Signal ASIC for High Resolution Mission	Mr AYZAC, Philippe
10:30	[23] A complete space based CCD biasing solution in a 0.35 μ m high voltage CMOS ASIC	MORRISSEY, Quentin