

AMICSA 2014 - Fifth International Workshop on Analogue and Mixed-Signal Integrated Circuits for Space Applications

Tuesday 1 July 2014

Applications for Radiation Hardened Analogue and Mixed-Signal ASICs: Wired and Wireless Communication - 503-1-001 (11:10 - 12:30)

-Conveners: Francis Anghinolfi

time	[id] title	presenter
11:10	[22] Digital Step Attenuators for Microwave Applications	Mr CHRISTIE, Andrew
11:30	[25] Low-Power Analogue Receiver ASIC for Space Telecommand Applications	Mr BIGONGIARI, Franco
11:50	[17] Use of IHP's 0.25 μm BiCMOS Process in the Development of European LVDS Devices	Mr LÓPEZ, Jesús
12:10	[2] Rad-hard High Speed LVDS Driver and Receiver	Mr MASSON, Thierry

Applications for Radiation Hardened Analogue and Mixed-Signal ASICs: Power - 503/1-001 - Council Chamber (14:00 - 15:20)

-Conveners: Richard Jansen

time	[id] title	presenter
14:00	[32] Radiation Tests of Point-of-Load DC-DC Converter and Extended Common Mode LVDS Components	Mr BURKHAY, Volodymyr
14:20	[11] A radiation-tolerant Point-of-Load buck DCDC converter ASIC	FACCIO, Federico
14:40	[19] A mix-signal radhard micro-controller: DPC	Mr FOSSION, Marc
15:00	[45] How chips paved the way to the Higgs particle	HEIJNE, Erik