



# AMICSA 2014 - Fifth International Workshop on Analogue and Mixed-Signal Integrated Circuits for Space Applications

## Monday, 30 June 2014

### Radiation Hardened Technology for Mixed-Signal IC: (1/3) - 503/1-001 - Council Chamber (11:50 - 12:30)

-Conveners: Boris Glass

time	[id] title	presenter
11:50	[3] SEE Characterization of a Magnetometer Front-End ASIC using a RHBD Digital Library in AMS 0.35um CMOS	Mr RAMOS-MARTOS, Juan
12:10	[31] Mixed-Signal Design Methodology for Various Radiation Environments with Applications to a 0.35 µm, 65 V Quadruple-Well BCD Technology	Mr KERWIN, David

### Radiation Hardened Technology for Mixed-Signal IC: (2/3) - 503-1-001 (14:00 - 14:40)

-Conveners: Richard Jansen

time	[id] title	presenter
14:00	[4] NEW ID MOS PDK for SPACE APPLICATIONS	Mr LATIMIER, Paul-Emile
14:20	[34] Radiation-hardened high-voltage ASIC technology qualification for space application.	SOUYRI, Marc

### Radiation Hardened Technology for Mixed-Signal IC: (3/3) - 503/1-001 - Council Chamber (15:40 - 17:00)

-Conveners: Richard Jansen

time	[id] title	presenter
15:40	[26] ATMEL mixed signal space offer: SOI 150nm radiation hardened process	Mrs BRIOT, Valerie
16:00	[10] 180nm CMOS Mixed-Signal Radiation Hard Library as base for a full ASIC supply chain	STEINKAMP, Jan
16:20	[27] DARE180X: A 0.18µm mixed-signal radiation-hardened library for low-power applications	Mr FRANCISCATTO, Giancarlo
16:40	[12] The development of a radiation tolerant low power SRAM compiler in 65nm technology.	Mr BROUNS, Robin