Contribution ID: **8**   Type: **not specified**

Read-Out Electronics: where data come from

Tuesday, 25 February 2014 13:30 (1 hour)

This lecture will focus on electronics used in HEP: while the software tools available to program very performant firmware are more and more powerful, hardware limitations can not be (easily) overcome. High speed digital signals, today required in HEP setups, need to be handled as analog signals: signal propagation, interaction between signals and electromagnetic interferences must be carefully considered. Moreover, the power consuming of ASICs and FPGAs strongly depends of the operations required and may be a limitation of the board.

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