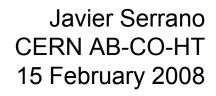
CERN General Machine Timing System: status and evolution

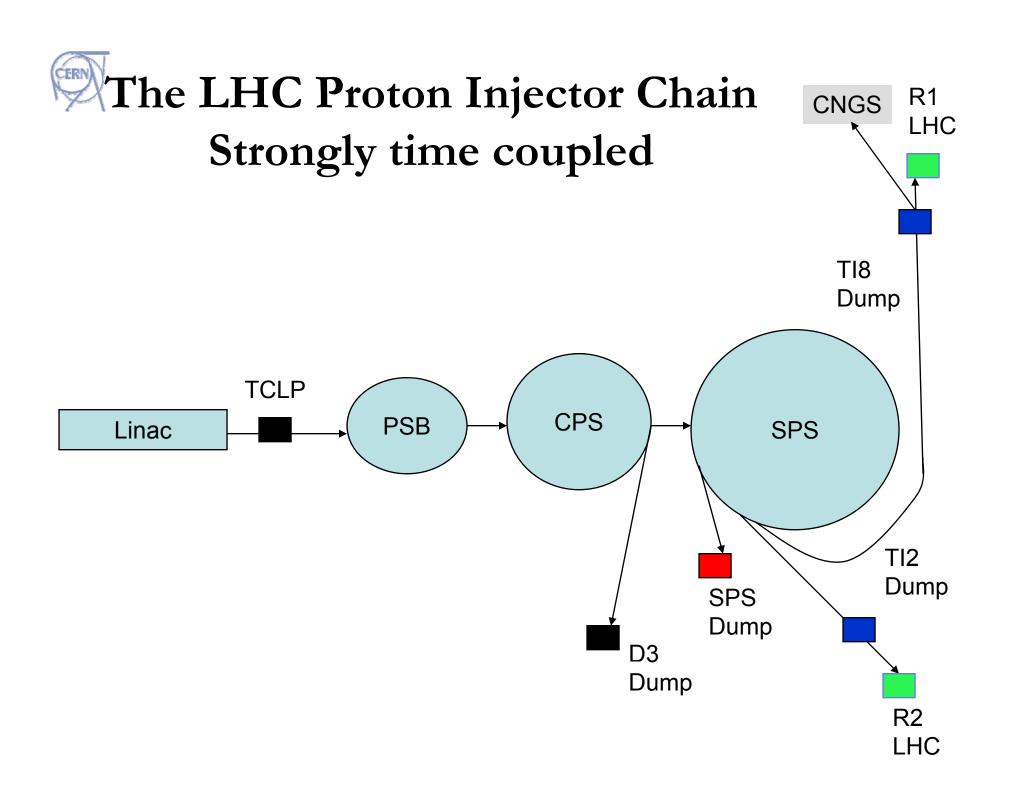


Outline

- Motivation
 - Why timing systems at CERN?
 - Types of CERN timing systems.
- The General Machine Timing System
 - General topology.
 - Sequencing.
- The timing renovation project
 - OWhy?
 - O How?

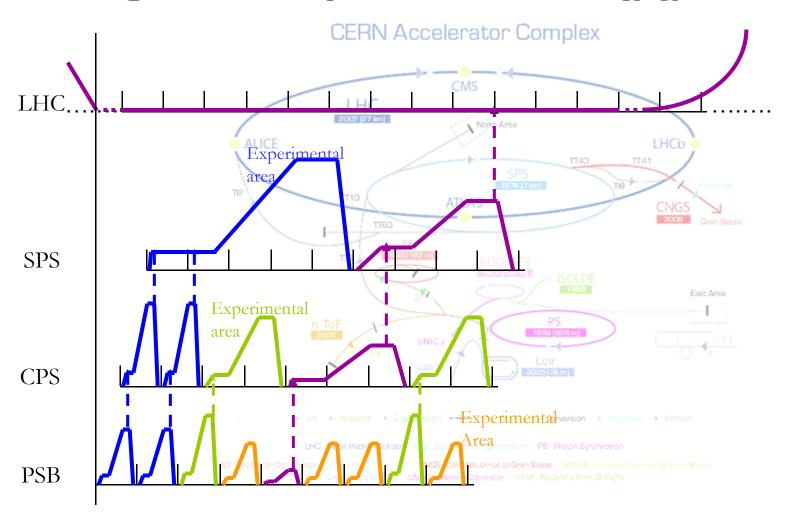
Why timing systems at CERN?

- CERN is like a factory: its end products are BEAMS.
- Manufacturing a beam requires a series of sequential "cycles" in cascaded accelerators.
- Orchestrating this process we have the Central Beam and Cycle Manager, sending messages out.
- Receivers react on these messages by producing interrupts/front panel pulses.



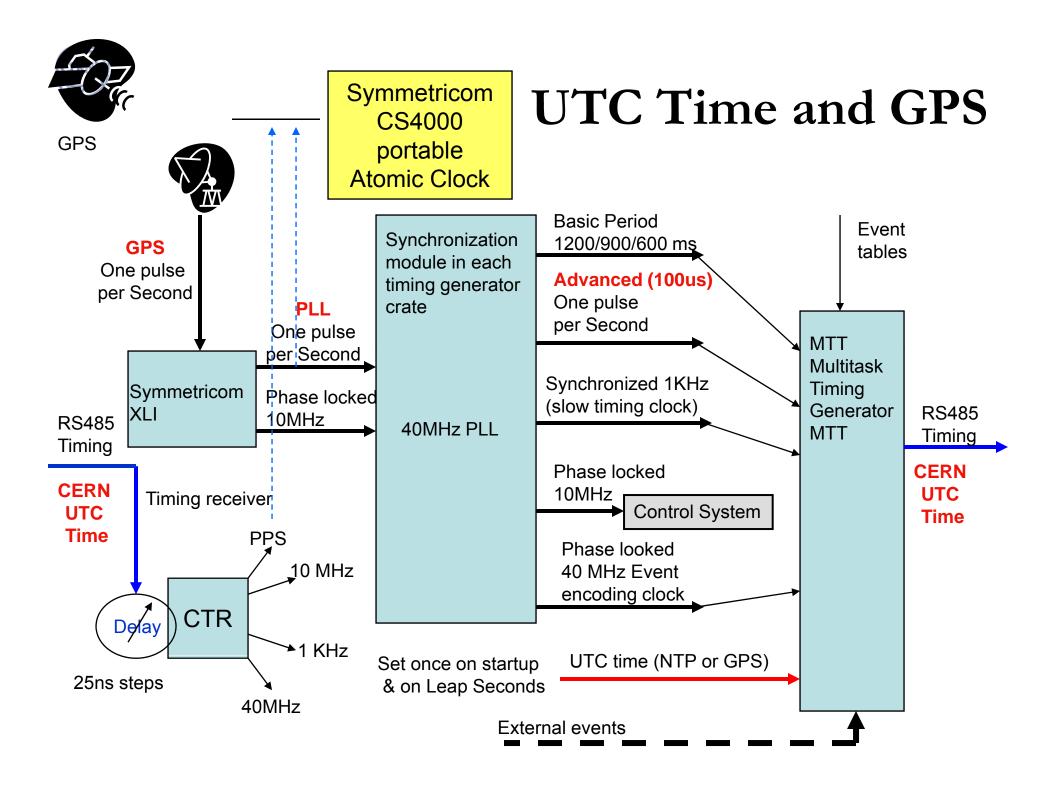


CERN accelerator network sequenced by central timing generator



Types of CERN timing systems

- General Machine Timing (GMT)
 - Based on UTC-synchronous 40.000 MHz.
 - 500 kbit/s over fiber and twisted pair (RS-422).
 - Granularity: 1 ms.
 - O Jitter < 1 ns.
- Beam Synchronous Timing (BST)
 - Based on TTC technology (see below).
 - Encodes messages in TTC data channel using bunch crossing frequency for LHC (40.079 MHz). Fiber-based.
 - Granularity: 1 LHC revolution (89 μs).
 - Jitter < 1ns.</p>
- Timing Trigger and Control (TTC)
 - Technology to multiplex Revolution tick and data in a single stream.
 - Experiments use it without data to have better clock recovery.





CBCM Sequence Manager

% В	ЕАМ СО	ORDINA	TION DIA	GRAM E	DITOR: I	Edit BCD	/scrubb	ing SPS/										_ 🗆 ×
File Edit R.Checker Tools Specialist Help																		
				d							6		*		· *	[3	
Туре																		
Description Rule violations Selection																		
General description																		
Name scrubbing SPS																		
ı	Desc.																	
Cr	eated Mon	Jun 02 07:0:	2:20	Uį	odated Wed	Jun 11 13:5	54:16											
Ot	Other informations																	
	Daylor 100																	
Bo	Bcd length 20 ▼▼																	
L																		
			4.5		1	2	3	4	5	6	7	8	9	10	11	12	13	44
S						LHCTEST	J	4	LHCTEST	0	-	LHCTEST	9	10	LHCTEST	12	ZERO	14 ZERC
	P																	
S	S				LHCTEST			LHCTEST			LHCTEST			LHCTEST			ZERO	ZERC
С	<- 1 ->	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Р	LHC			LHC			LHC			LHC			EASTB TOF		TOF	EASTB		
S		ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERC
Р	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
S	LHC	TSTLHC	ISOGPS	LHC	TSTLHC	ZERO	LHC	TSTLHC	ZERO	LHC	TSTLHC	ZERO	EASTB	ISOGPS	TOF	EASTB	ZERO	TOF
В	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ZERO	ISOGPS	ZERO	ZERO	ISOGPS	ZERO	ZERC
										10.1		11.1						
Ī	4 BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB	88888888888888	888888888888888888888888888888888888888	88888888888888	99999999999999999999999999999999999999	999999999999999999	999999999999999999999999999999999999999	988888888888888888888888888888888888888	999999999999	888888888888888	888888888888888888888888888888888888888	999999999999999	388888888888888888888888888888888888888					•
☐ Fa	ault: Error d	etected. Ca	n't strip Bcd	for machin	e SPS Exce	ption: cern	ps.cbcm.sı	vapi.SrvExc							nested exc	eption is: Cl	ocm Contex	t Excepti
	1	1/Jun/200	3 13:54		User	CPS.L	ABO.SUP	ERUSER	RES	S PSPC	8476:BCD e	ditor:LABO.	SUPERUSEF	🗸 RChe	ecker			



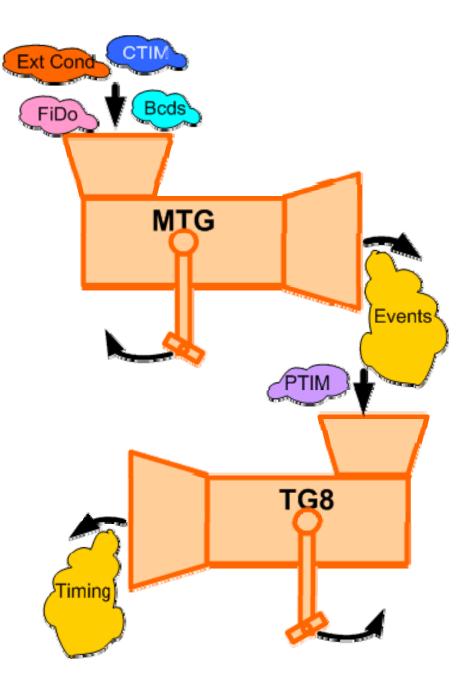
The MTG

• Inputs

- BCDs
- External conditions
- Timing description (CTIM)
- External timings
- FiDo programs

• Outputs

- Telegrams events
- Timing events
- Time events
- ...



The timing renovation project: why?

- Lack of bi-directionality in GMT:
 - Forces us to have a parallel data path (technical network) for control and diagnostics → forces us to support only platforms with an embedded computing engine.
 - Cabling delay compensation cannot be done automatically → need for costly/unreliable measurement campaigns.
- Lack of bandwidth in GMT
 - 500 kb/s current rate was chosen for backwards compatibility.
 - Forces us to have different networks for different accelerators → cabling and software hassle.

The timing renovation project: how?

- Identify commonalities with other projects, then launch collaborative effort.
- Promising enabling technologies:
 - Ethernet and PTP.
 - Bidirectional optical links, with active or passive fan-outs.
- Boundary conditions:
 - Be as standard as reasonably possible.
 - End up with a completely open source product.
- Open issues:
 - Time scheduled vs. event based systems.
 - Unclear advantages of plain Ethernet + protocol stack.
 - O How to mix companies and open source?