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Introduction - Lecture 1

Monday, 16 June 2014 11:45 (1 hour)

In this lecture we start by discussing Moore's law which has led to an incredible transistor count becoming available for hardware designers.
As a result, modern microprocessors have a high level of complexity and an unprecedented level of parallelism that software programmers are asked to exploit. We define the seven dimensions of performance which will be discussed during the week. The first two (pipelining and superscalar execution are discussed here, whereas the others will be discussed in dedicated lectures).

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Session Classification: Efficient Computing